

1	2	3	4	5	6	7	8	9	10	11	12	13	14
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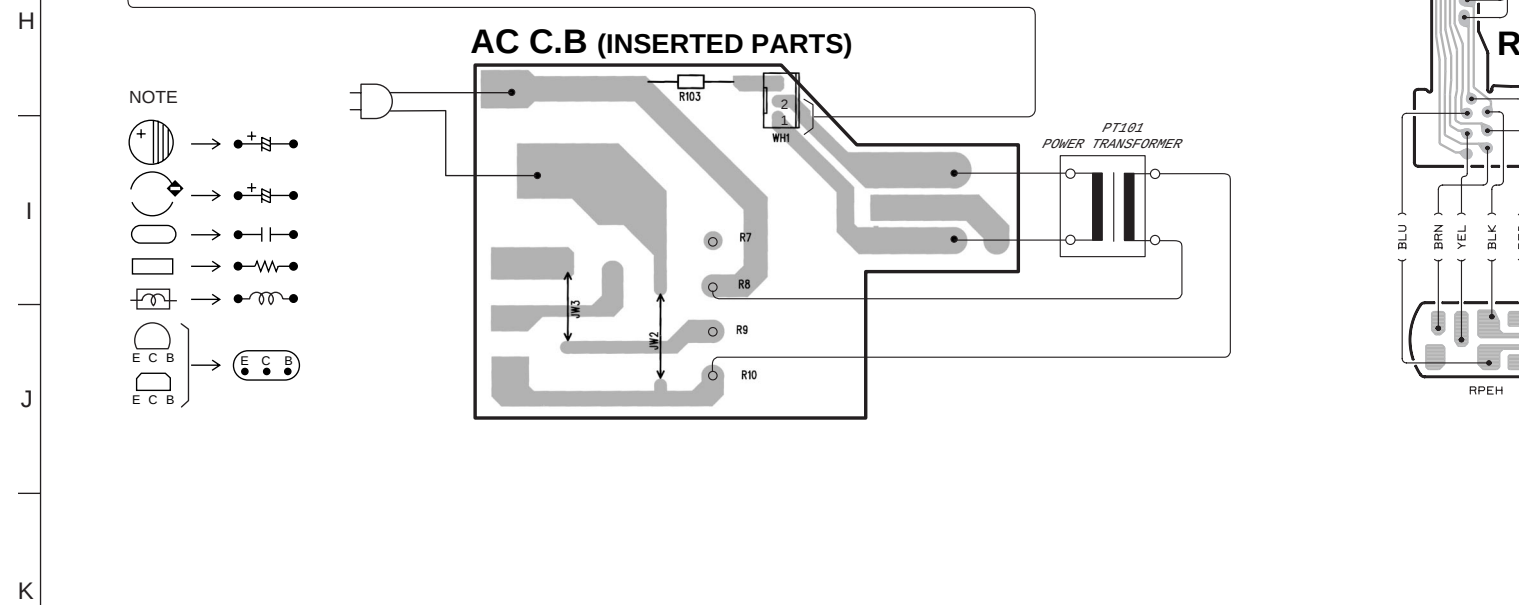
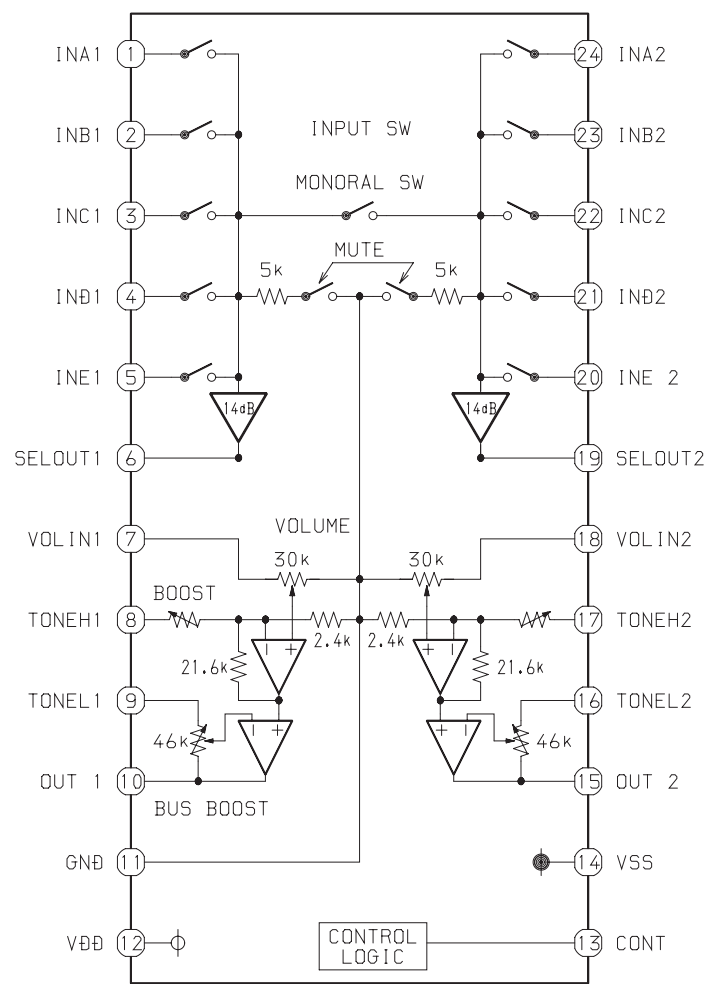
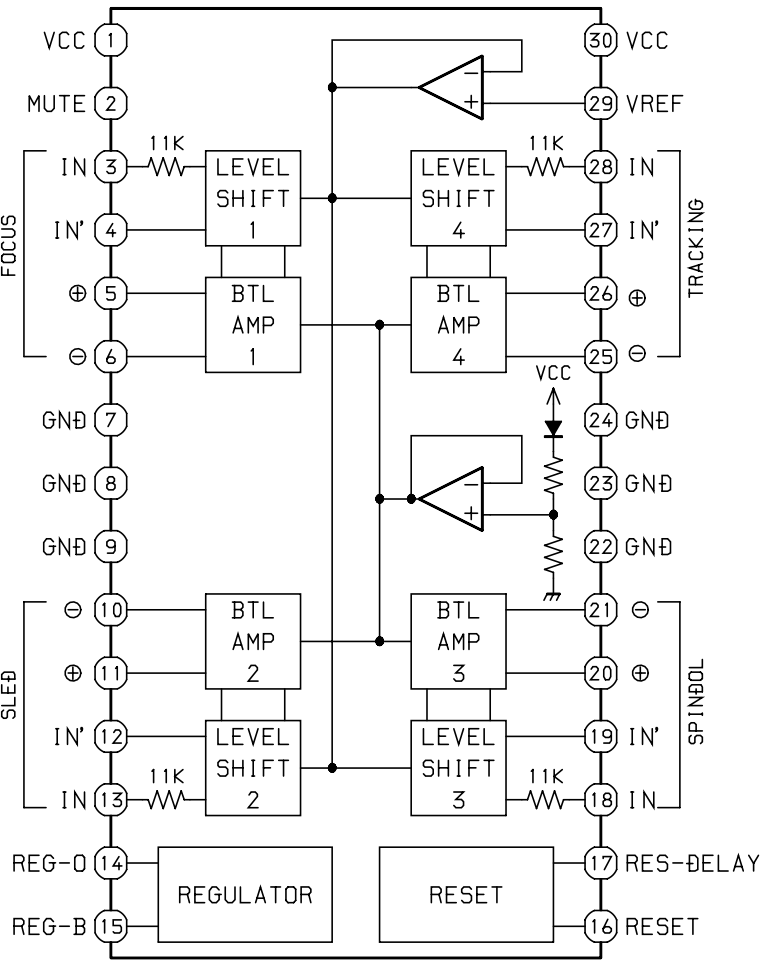


Figure 1 consists of two sub-images, (a) and (b), showing the PCB layout of the proposed system. Sub-image (a) is the top view, showing the layout of the antenna, feeding network, and rectifier circuit. It includes labels for the input ports: BLK, RED, and WHT. Sub-image (b) is the bottom view, showing the layout of the antenna, feeding network, and rectifier circuit. It includes labels for the output ports: PEH, C181, and C182.

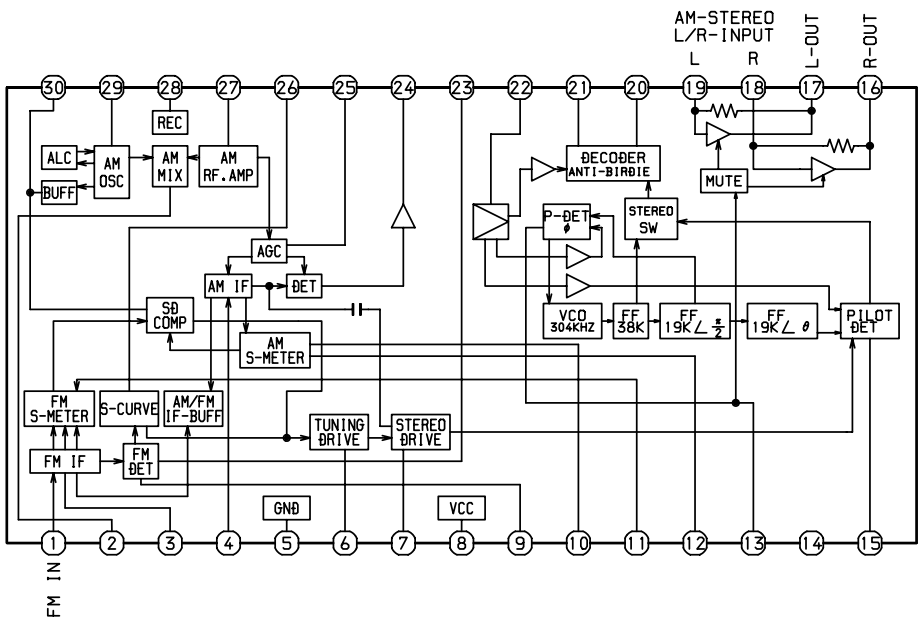
IC BLOCK DIAGRAM
IC, M62495AFP



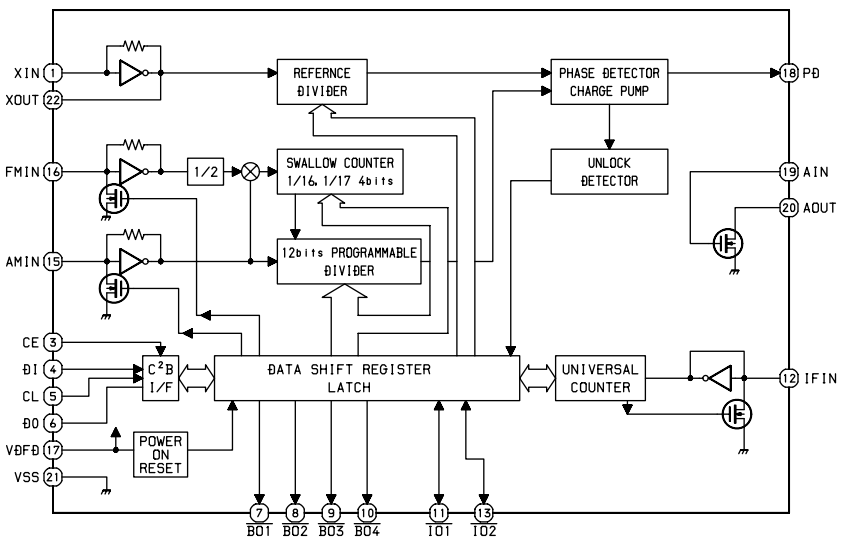
IC, LA6541D



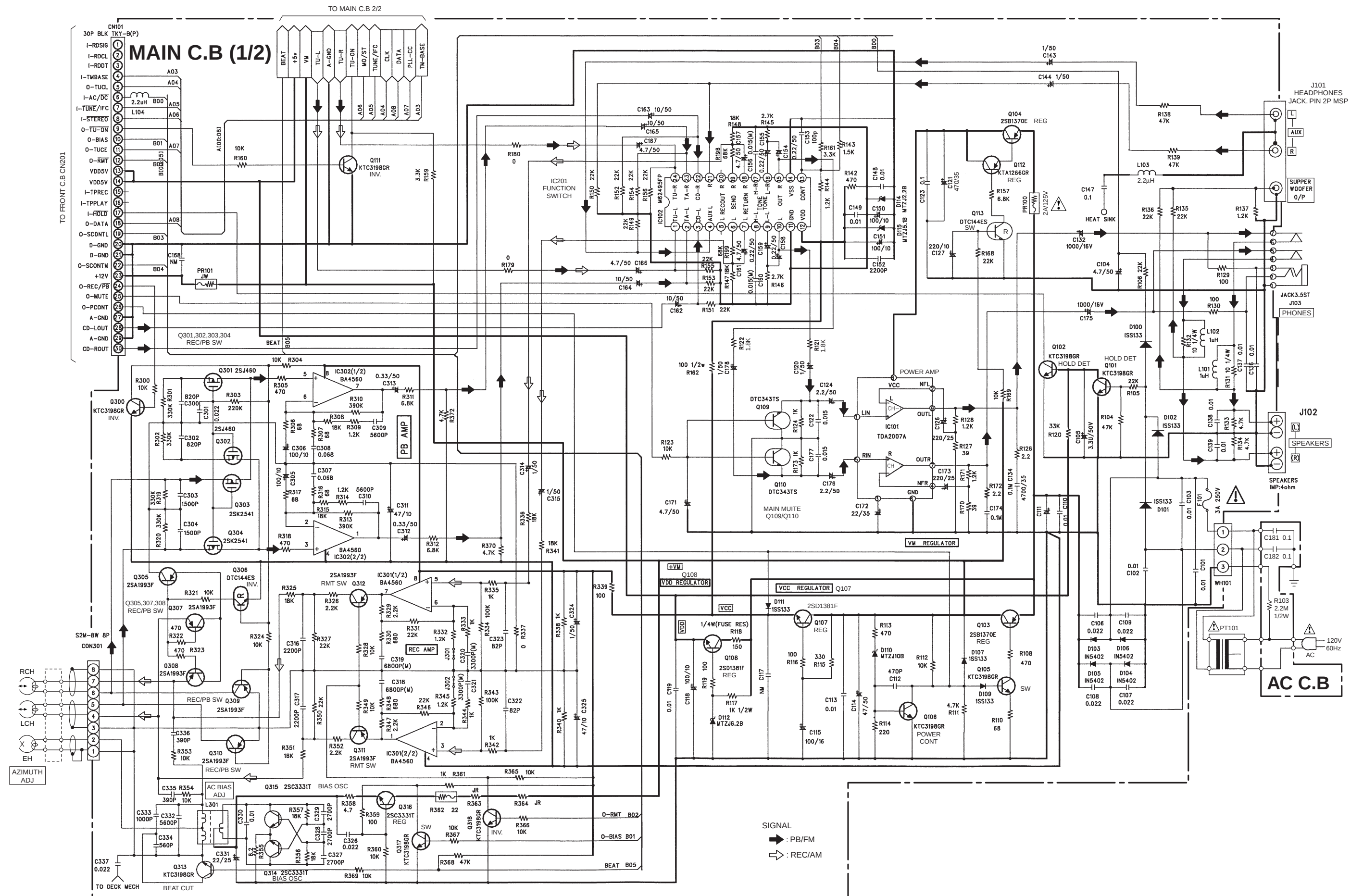
IC, LA1837NL

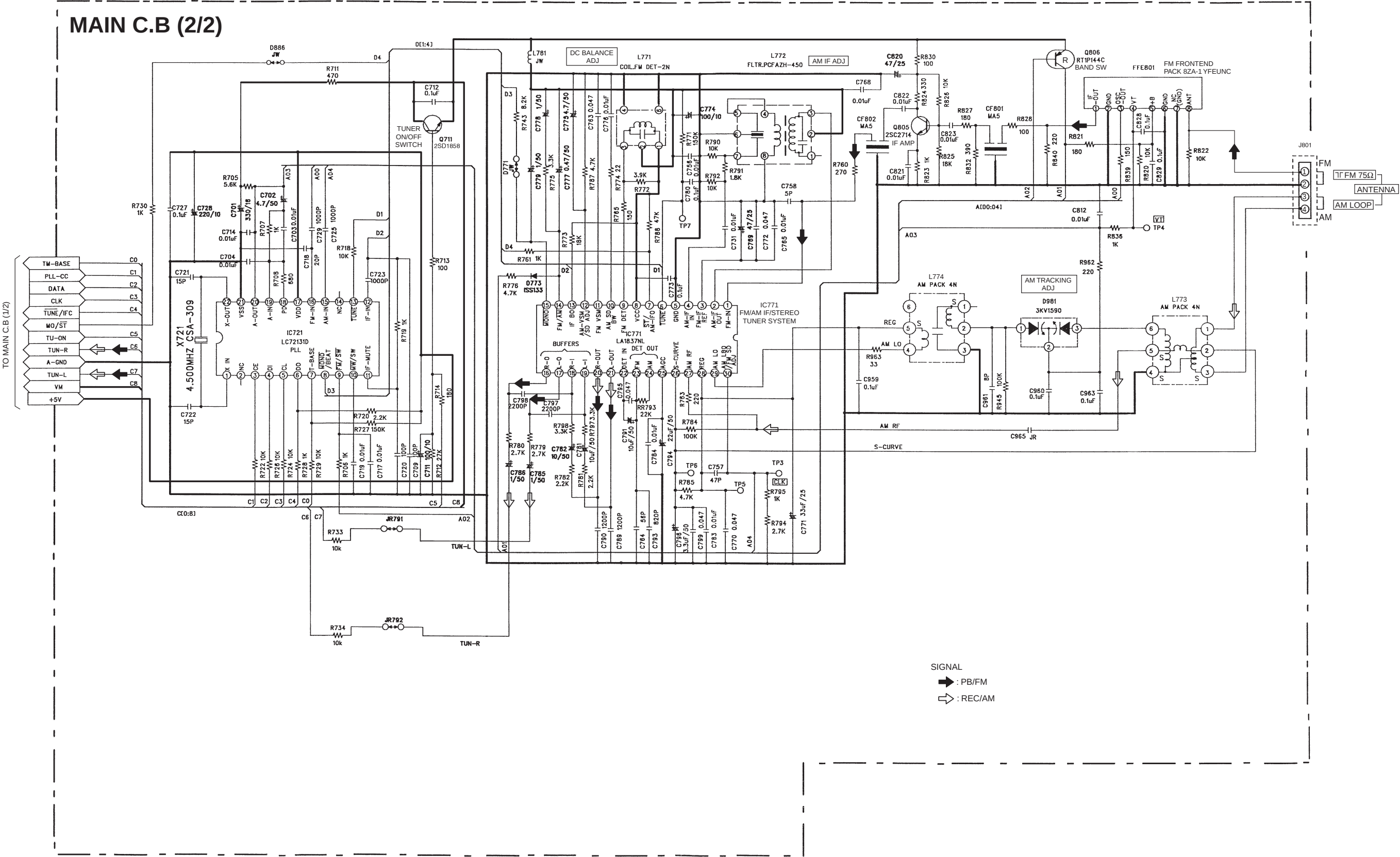


IC, LC72131D



SCHEMATIC DIAGRAM-1 (MAIN 1/2)





FRONT C.B (INSERTED PARTS)

CD C.B (INSERTED PARTS)

LED C.B (INSERTED PARTS)

DECK C.B

NOTE

CD C.B (CHIP PARTS)

LED C.B (CHIP PARTS)

OPTICAL PICK UP SF-P101NR

MOTOR C.B

M2 (SLED MOTOR)

SW1 (INSIDE LIMIT SW)

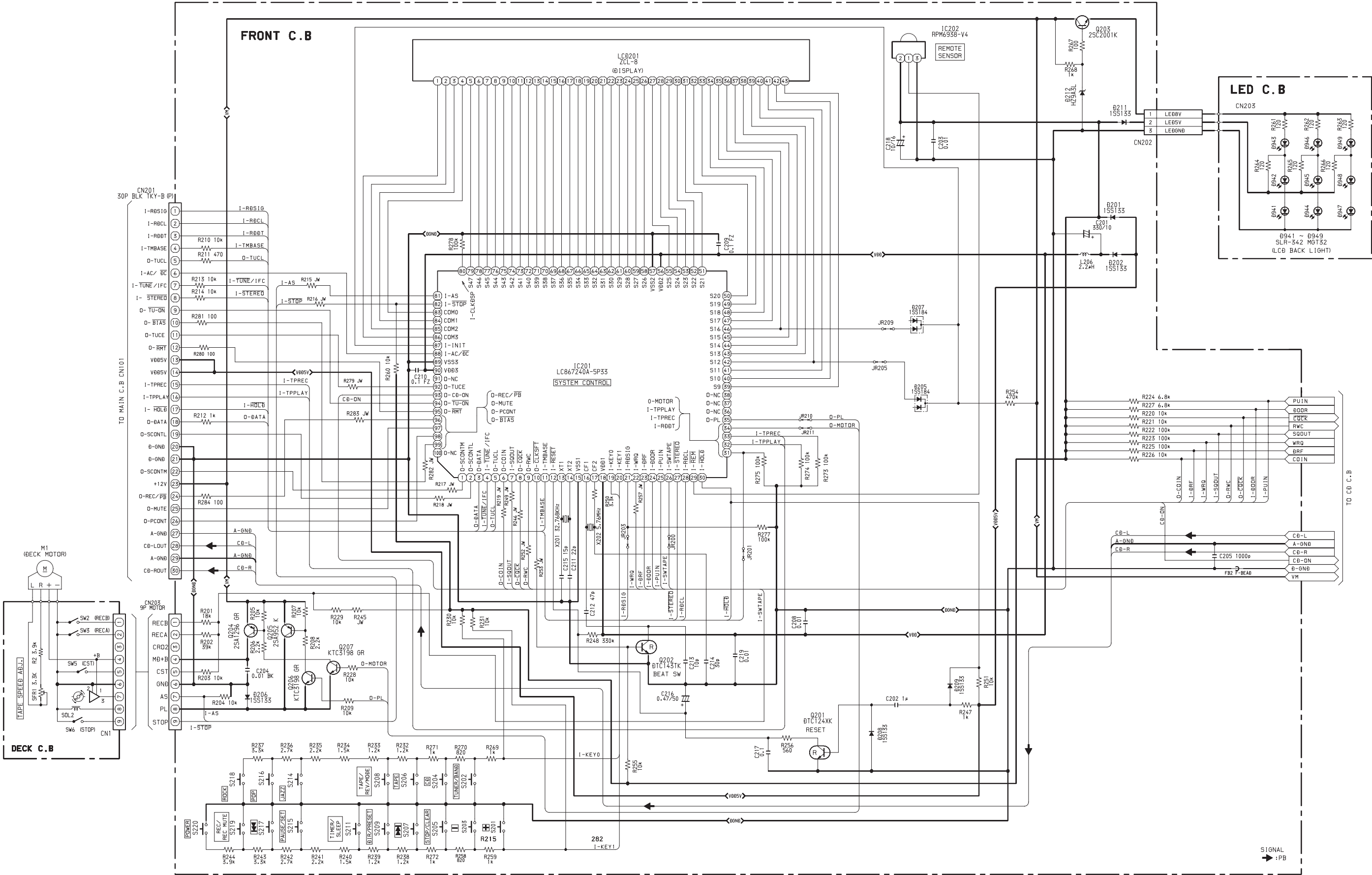
M3 (SPINDLE MOTOR)

PIN3



OPTICAL
PICK UP
SF-P101NR

SCHEMATIC DIAGRAM-3 (FRONT)



SCHEMATIC DIAGRAM-4 (CD)

