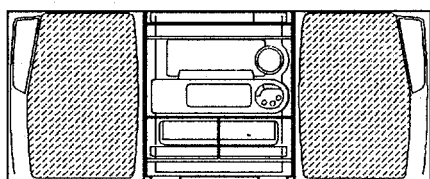


aiwa



NSX-S30 NSX-S32 NSX-S33 NSX-S35 NSX-S36



COMPACT DISC STEREO
CASSETTE RECEIVER

- BASIC TAPE MECHANISM : 6ZM-3 PR1NM<30EZ,35EZ,K,V>
6ZM-3 PR2NM<32HR>, 2ZM-3MK2 PR4NM<33EZ,36HR>
- BASIC CD MECHANISM : 4ZG-1 Z1DNM
- TYPE : HR,EZ,K,V

SYSTEM	SPEAKER	CD - CASSEIVER
NSX-S30 (TYPE : EZ,K,V)	SX - NS30	CX-NS30
NSX-S32 (TYPE : HR)		CX-NS32
NSX-S33 (TYPE : EZ)		CX-NS33
NSX-S35 (TYPE : EZ)	SX - NS30 SX - R270	CX-NS35
NSX-S36 (TYPE : HR)	SX - FNS36	CX-NS36

If requiring information about the CD mechanism, see Service Manual of 4ZG-1,
S/M Code No. 09-974-187-50T.

MANUAL
SERVICE

TRANSISTOR ILLUSTRATION



E C B

KTA1266GR
KTC3198GR



E C B

CSD1489B
CSB1058B



E C B

2SA933S



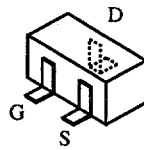
G D S

2SK2723

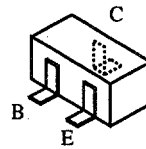


B C E

2SB1370
FN1016
FP1016



2SK2158



2SC2714
RN1410
2SC3052
2SA1235
RT1N141
CSA1362

RT1N144
CSD1306
CMBT5551
RT1P144
RT1P141



E B C

C2N5401



B C E

2SB1626
2SD2495

○ チップ抵抗部品コード/CHIP RESISTOR PART CODE

チップ抵抗部品コードの成り立ち
Chip Resistor Part Coding



A
抵抗部品コード
Resistor Code

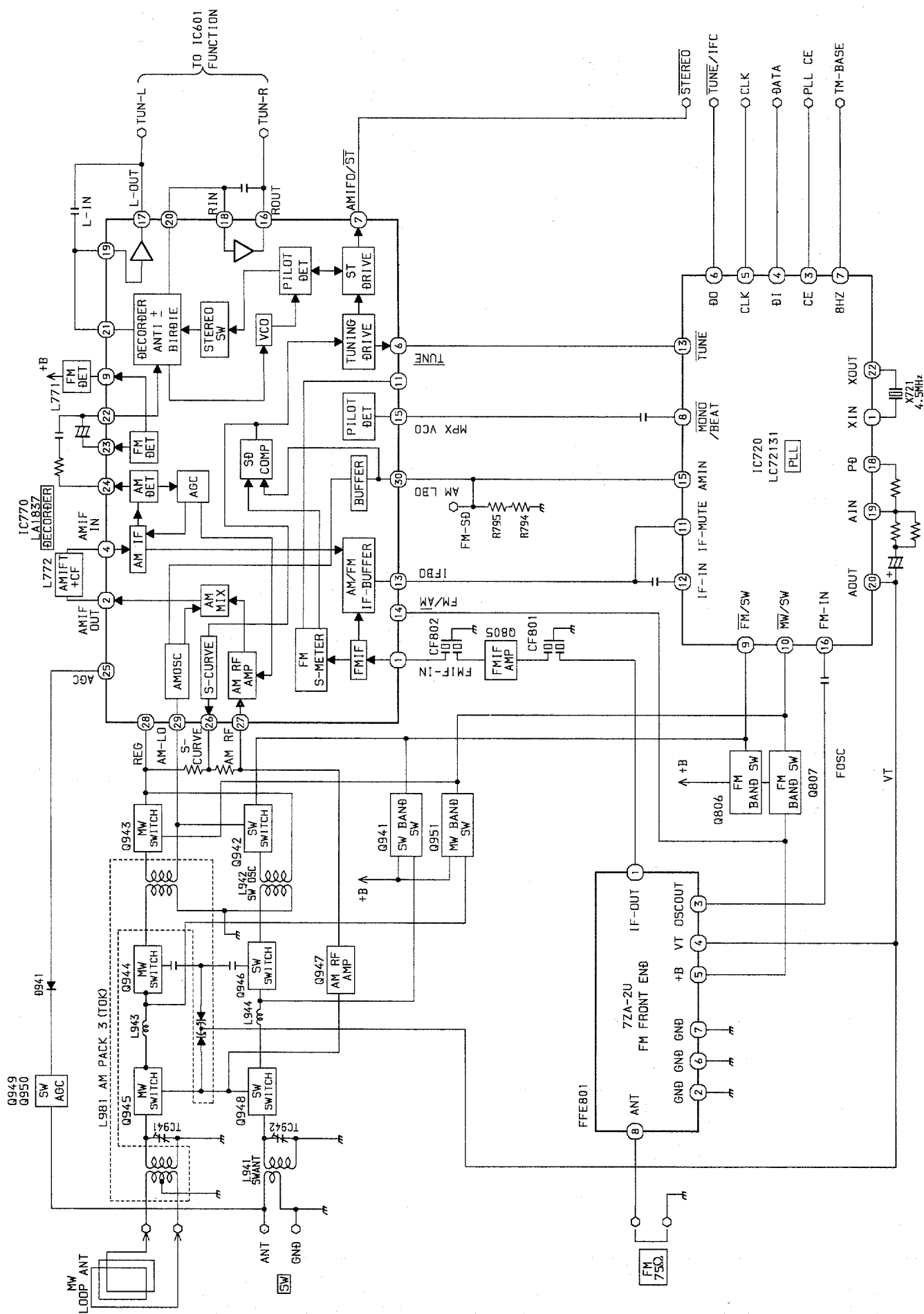
桁表示
Figure

抵抗値
Value of resistor

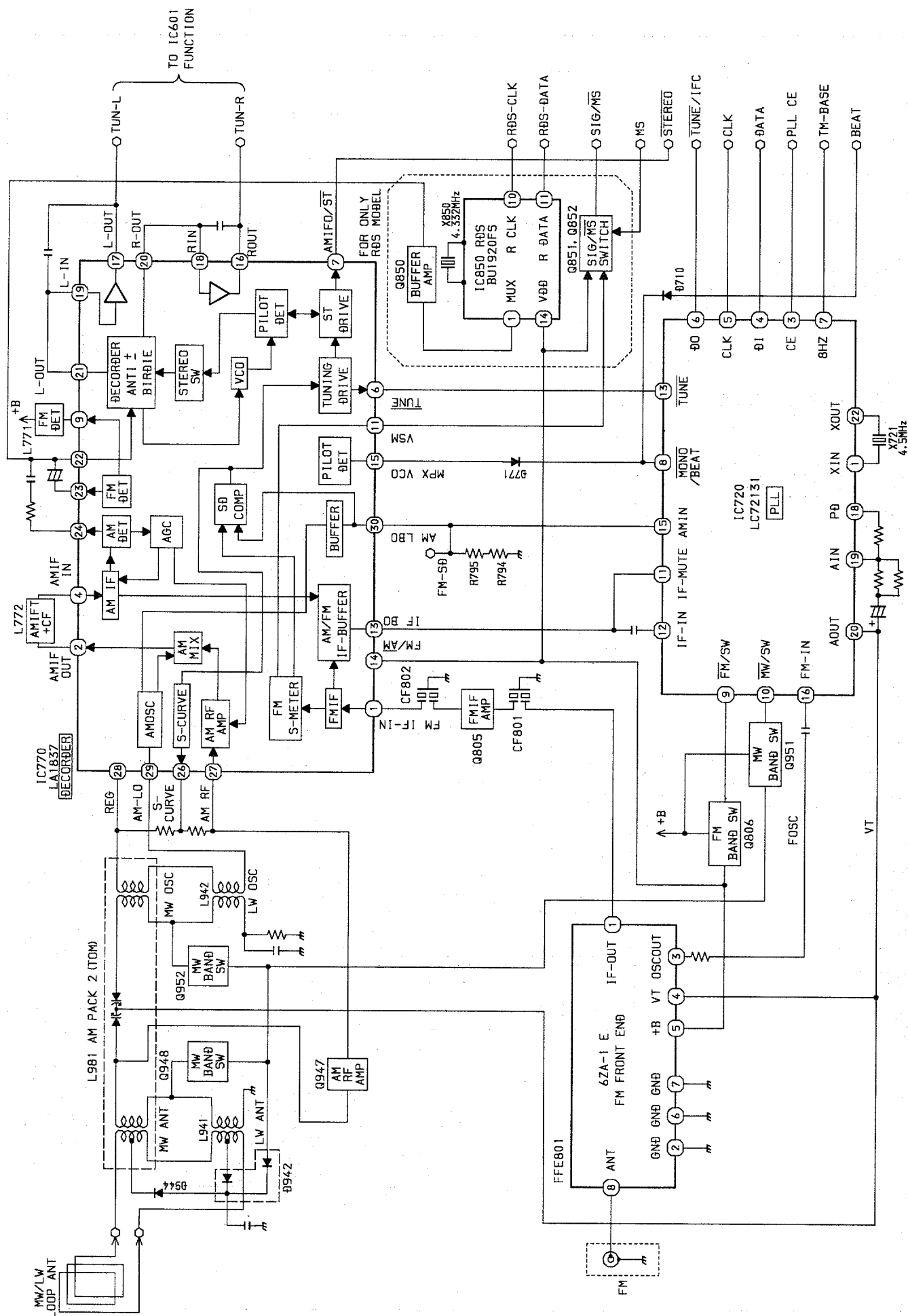
チップ抵抗
Chip resistor

容量 Wattage	種類 Type	許容誤差 Tolerance	記号 Symbol	寸法/Dimensions (mm)				抵抗コード : A Resistor Code: A
				外形/Form	L	W	t	
1/16W	1608	±5%	CJ		1.6	0.8	0.45	108
1/10W	2125	±5%	CJ		2	1.25	0.45	118
1/8W	3216	±5%	CJ		3.2	1.6	0.55	128

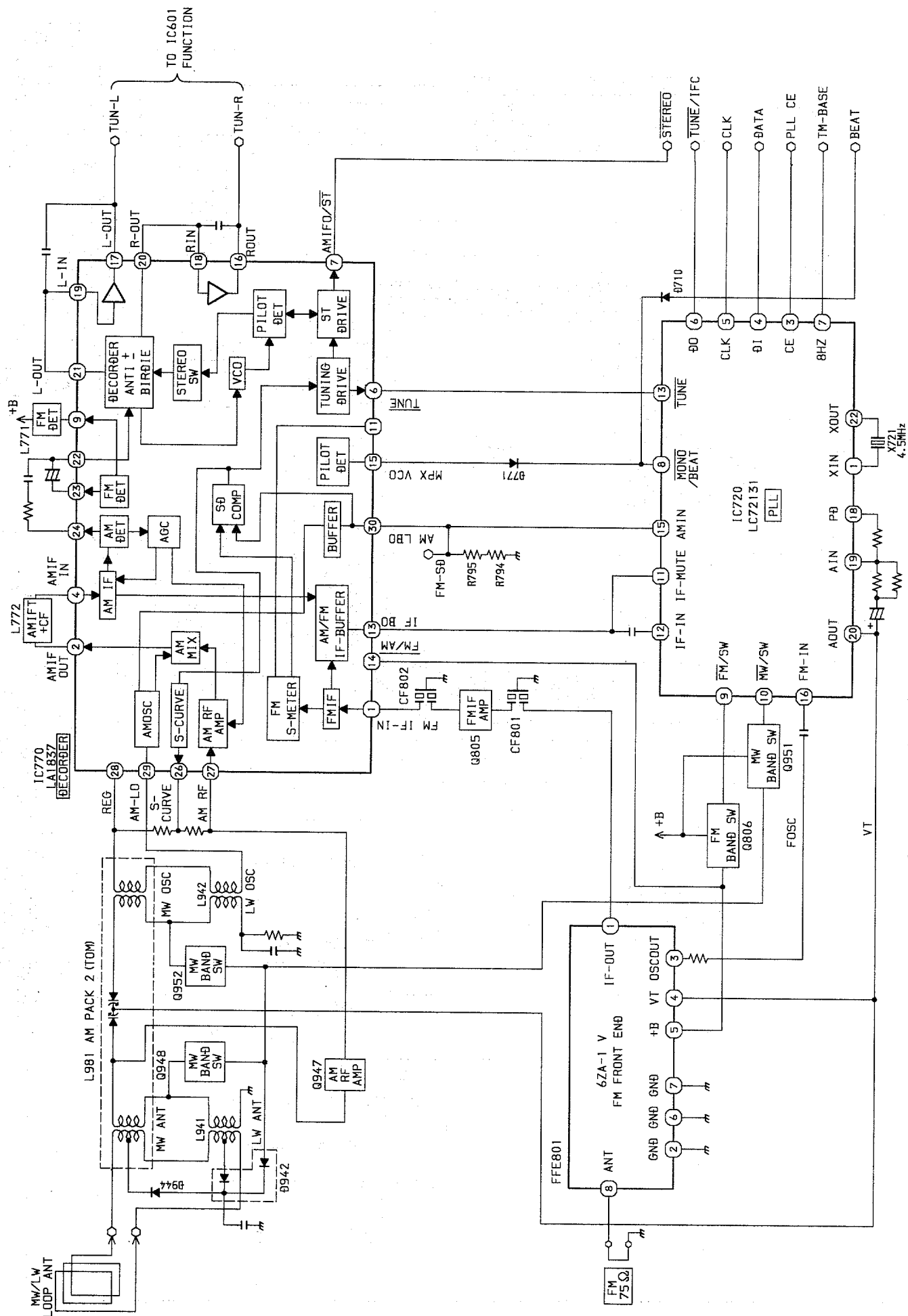
BLOCK DIAGRAM - 1 (TUNER : HR)

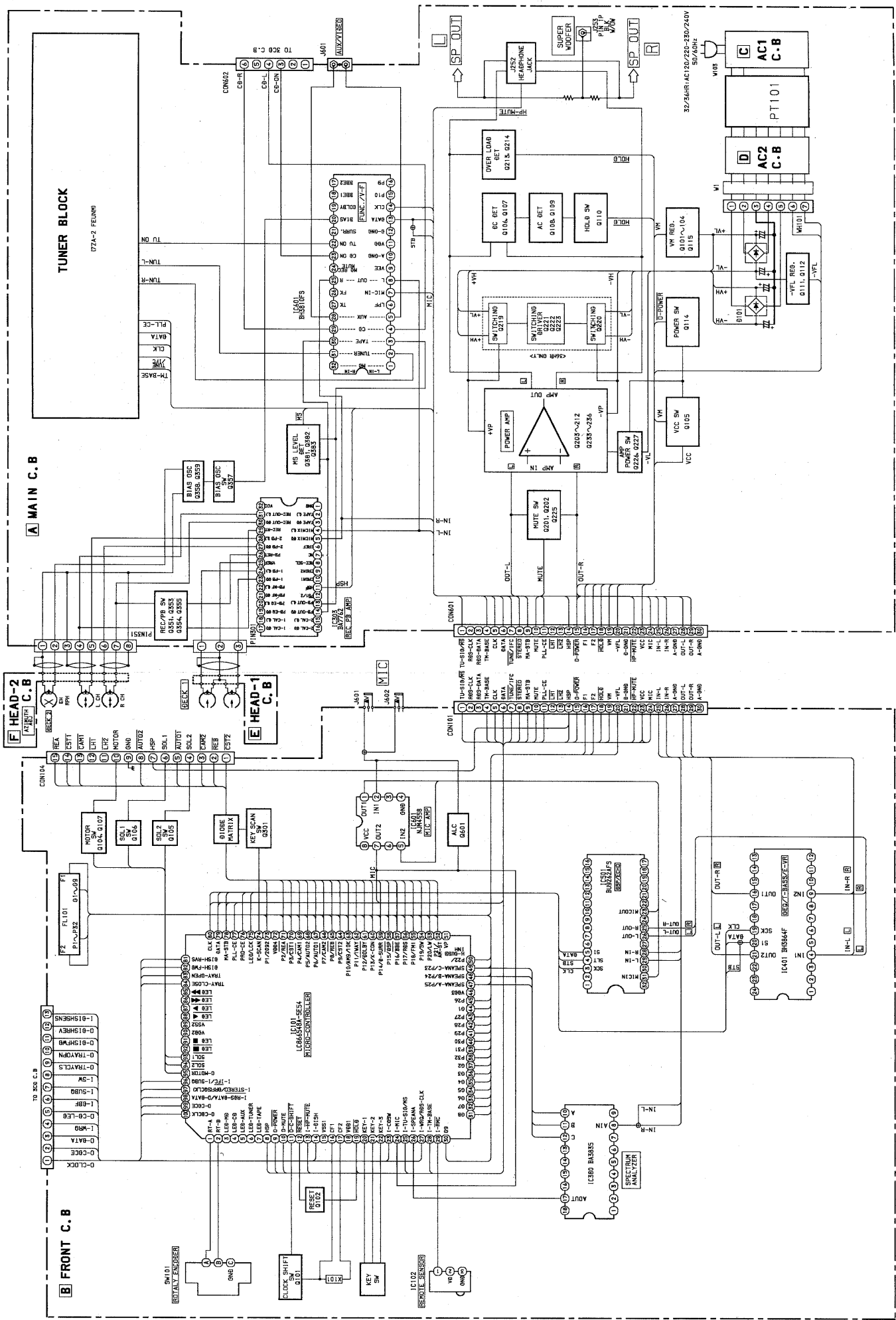


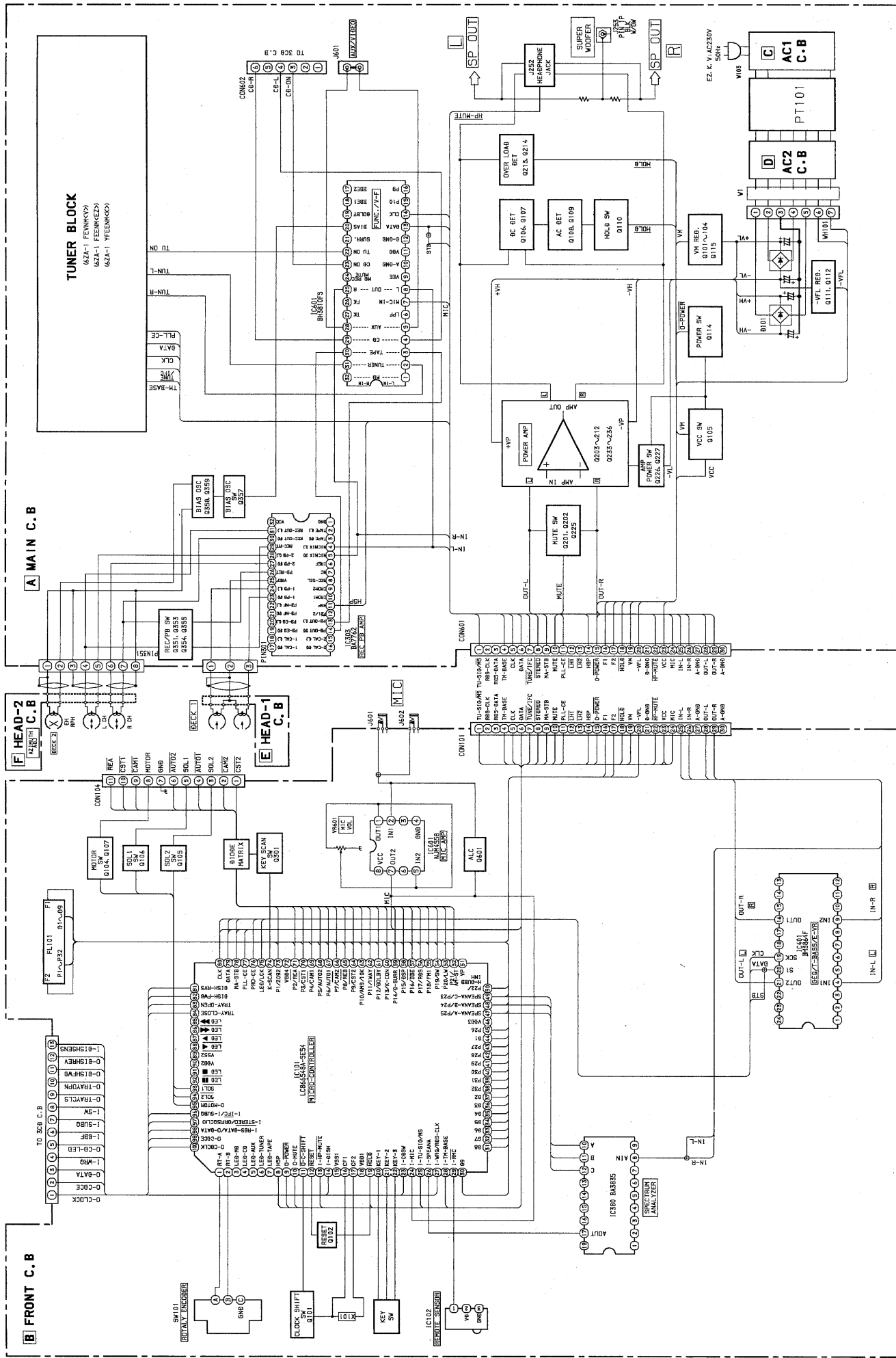
BLOCK DIAGRAM – 2 (TUNER : 30EZ, 33EZ, 35EZ, K)



BLOCK DIAGRAM - 3 (TUNER : V)







1 2 3 4 5 6 7 8 9 10 11 12 13 14

A

B

C

D

E

F

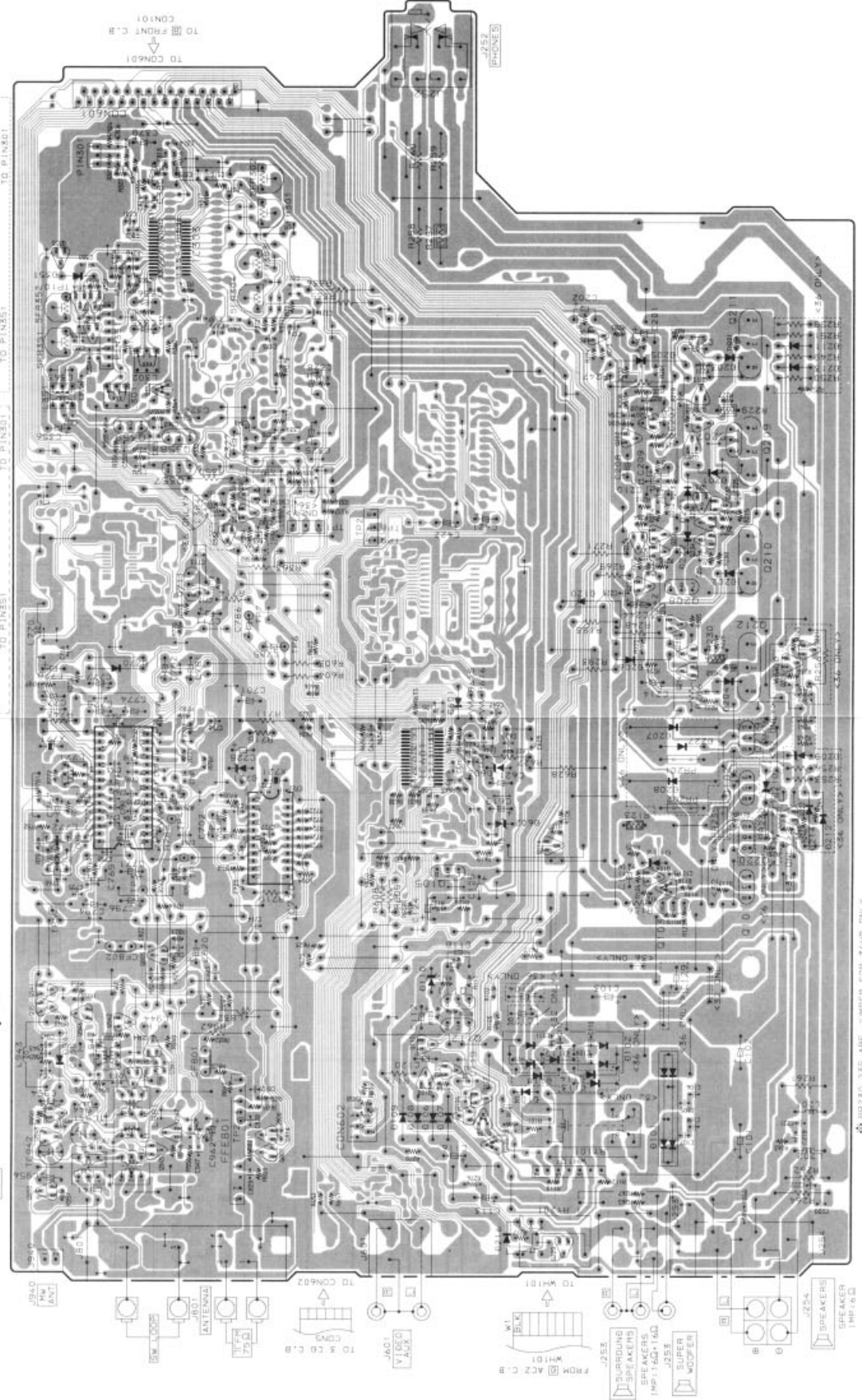
G

H

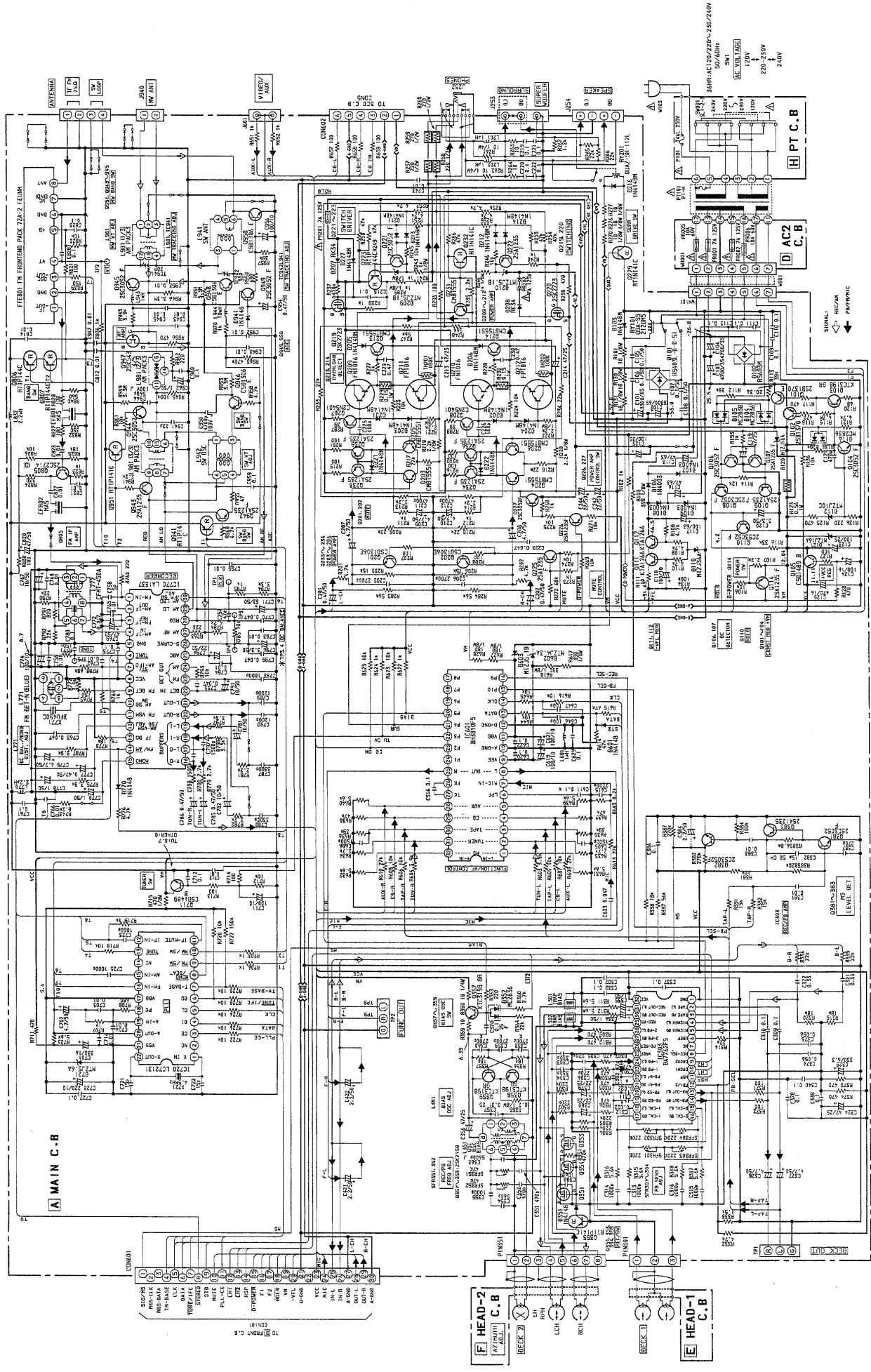
I

J

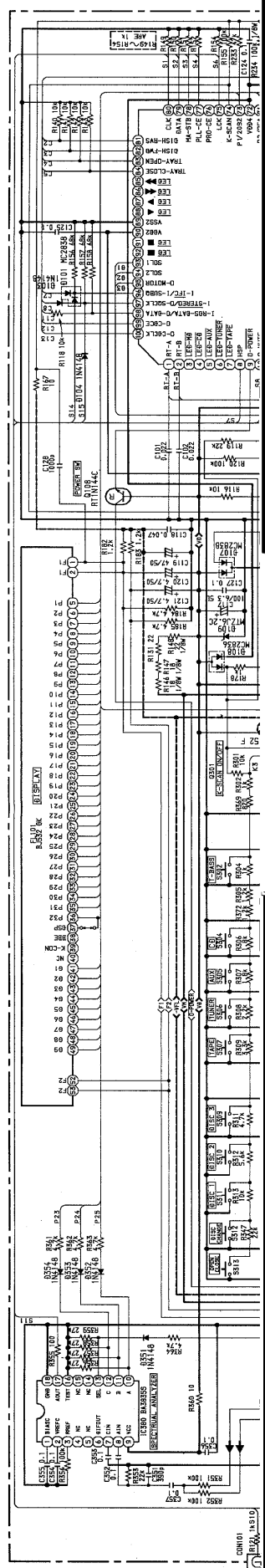
A MAIN C.B

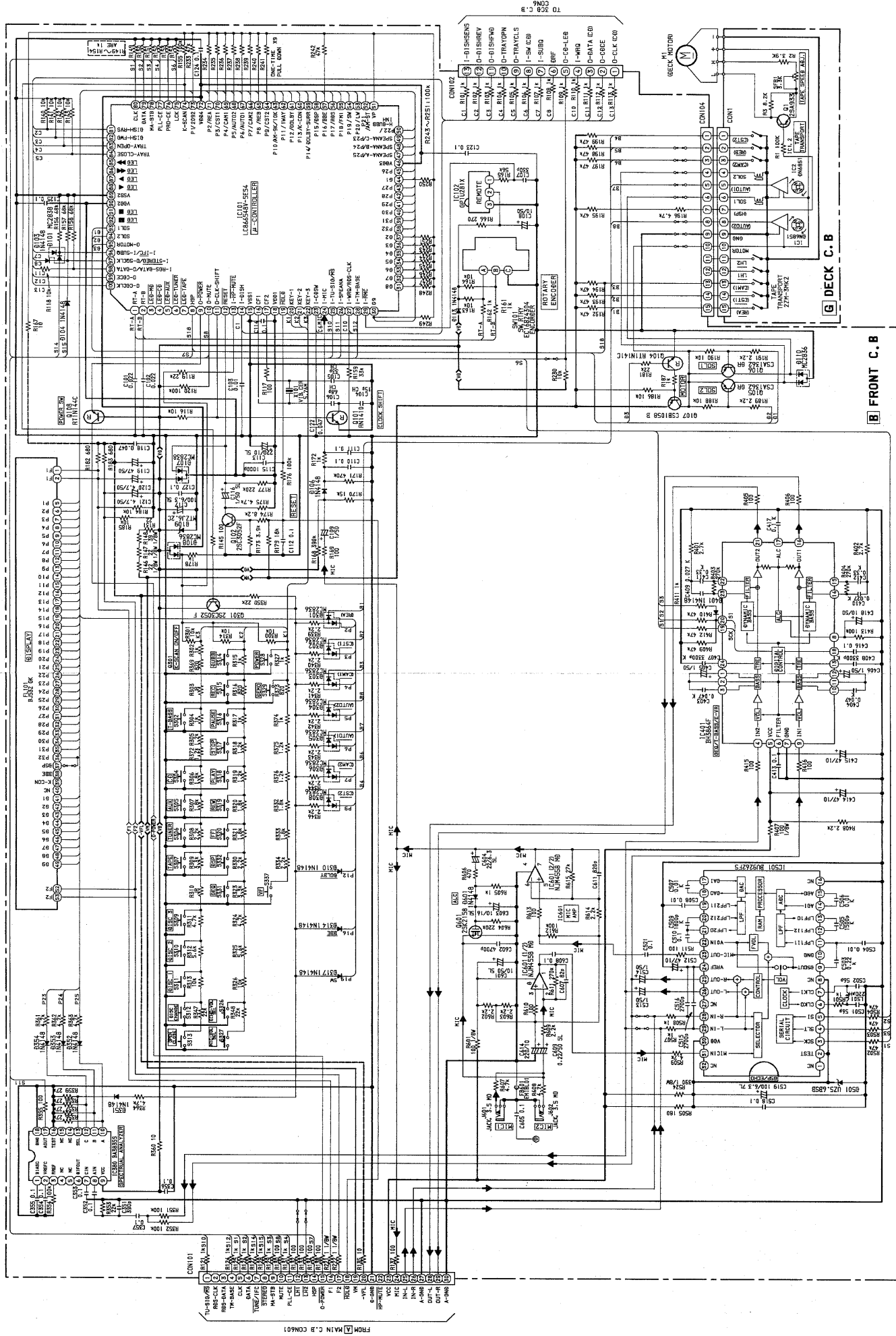


PH230, 235 ARE JUMPER FOR 34HR ONLY

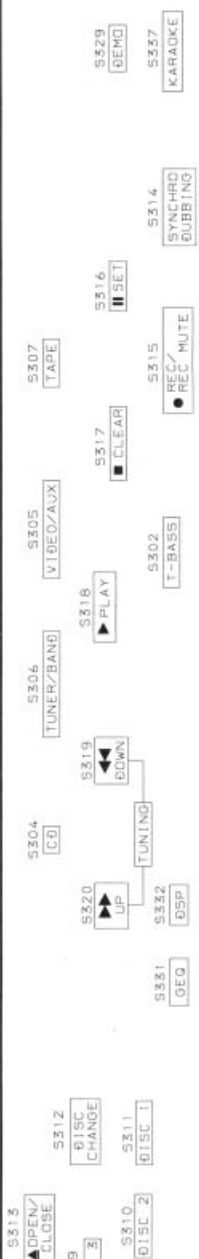


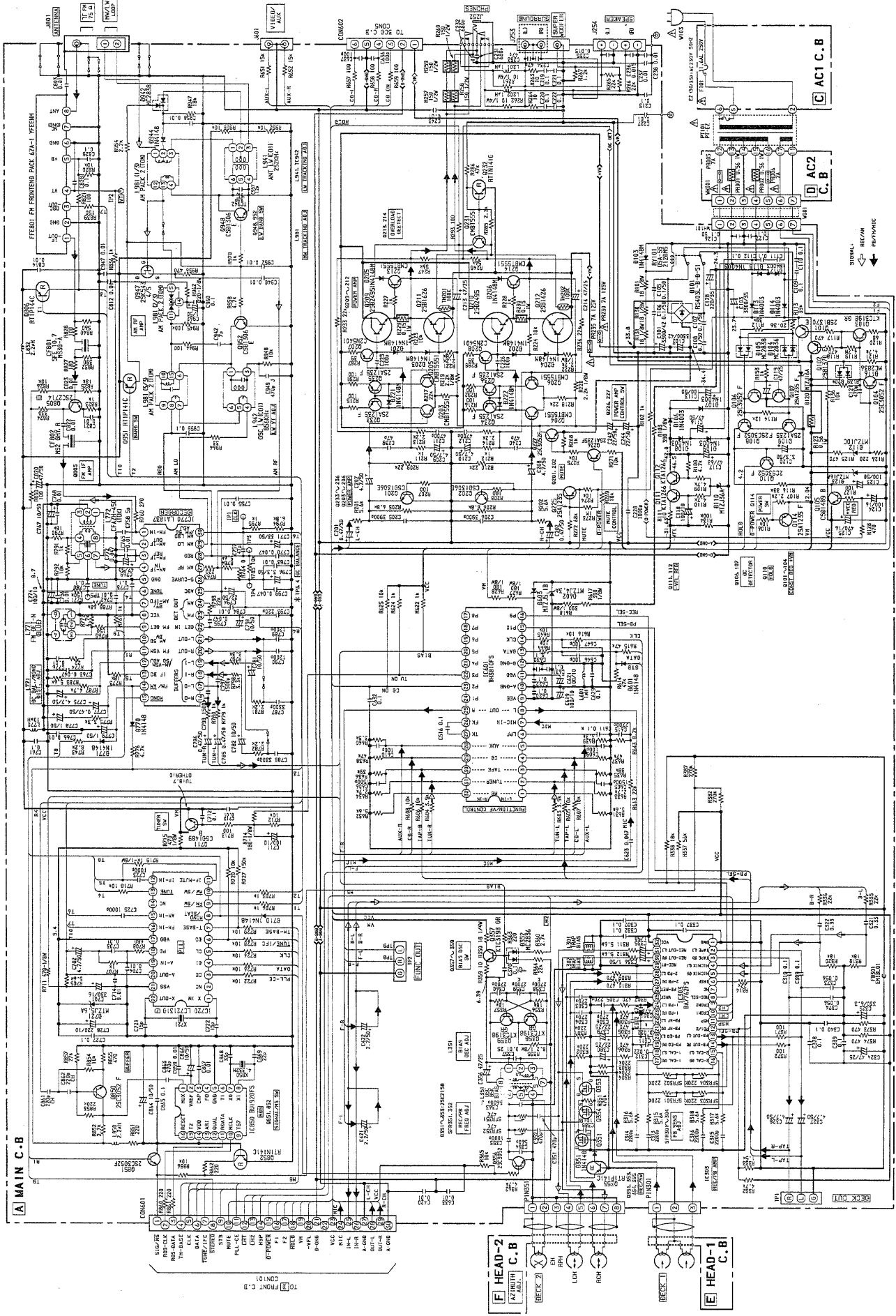
SCHEMATIC DIAGRAM - 3 (FRONT : 32HR)

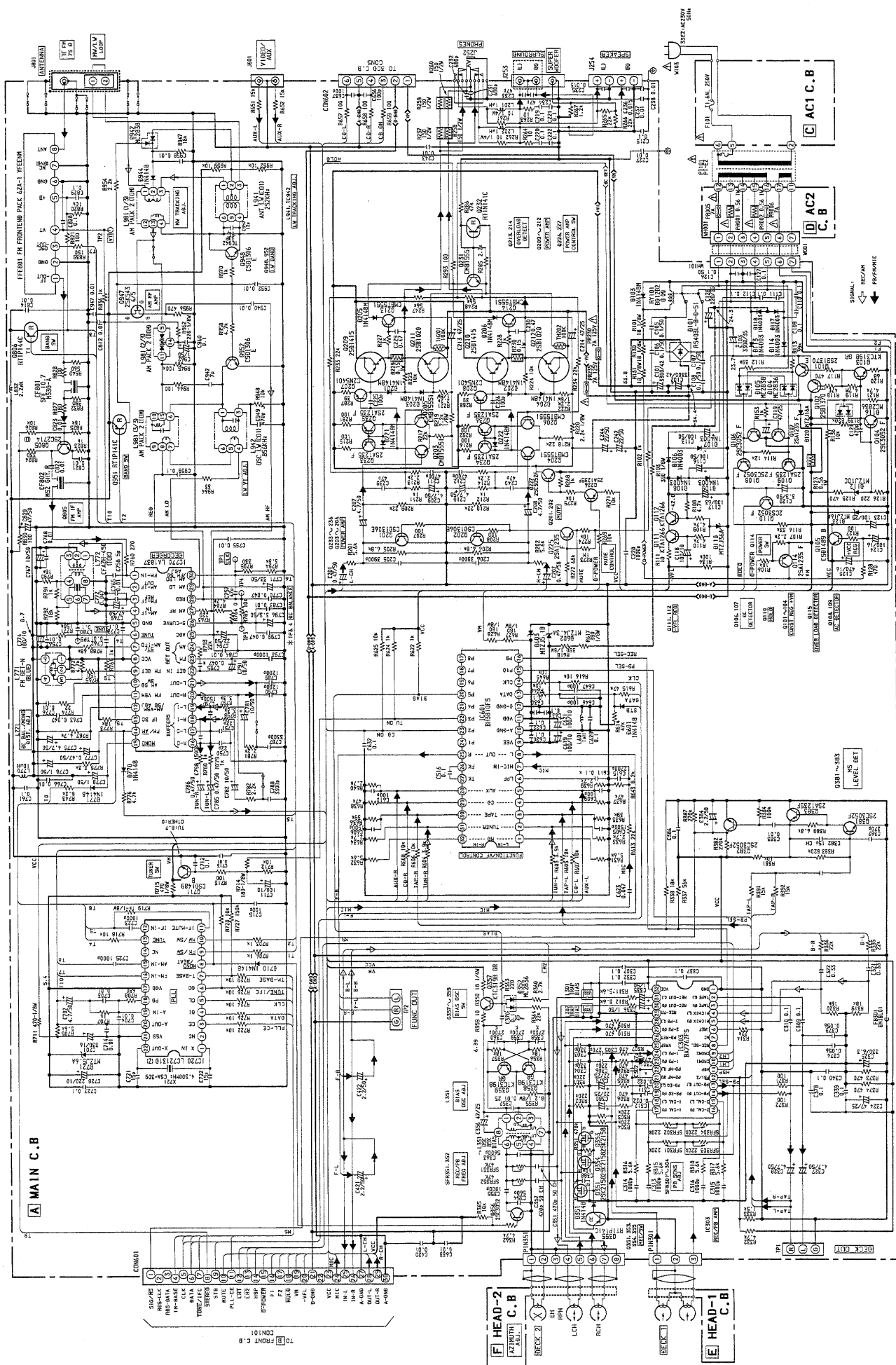




- 30 -

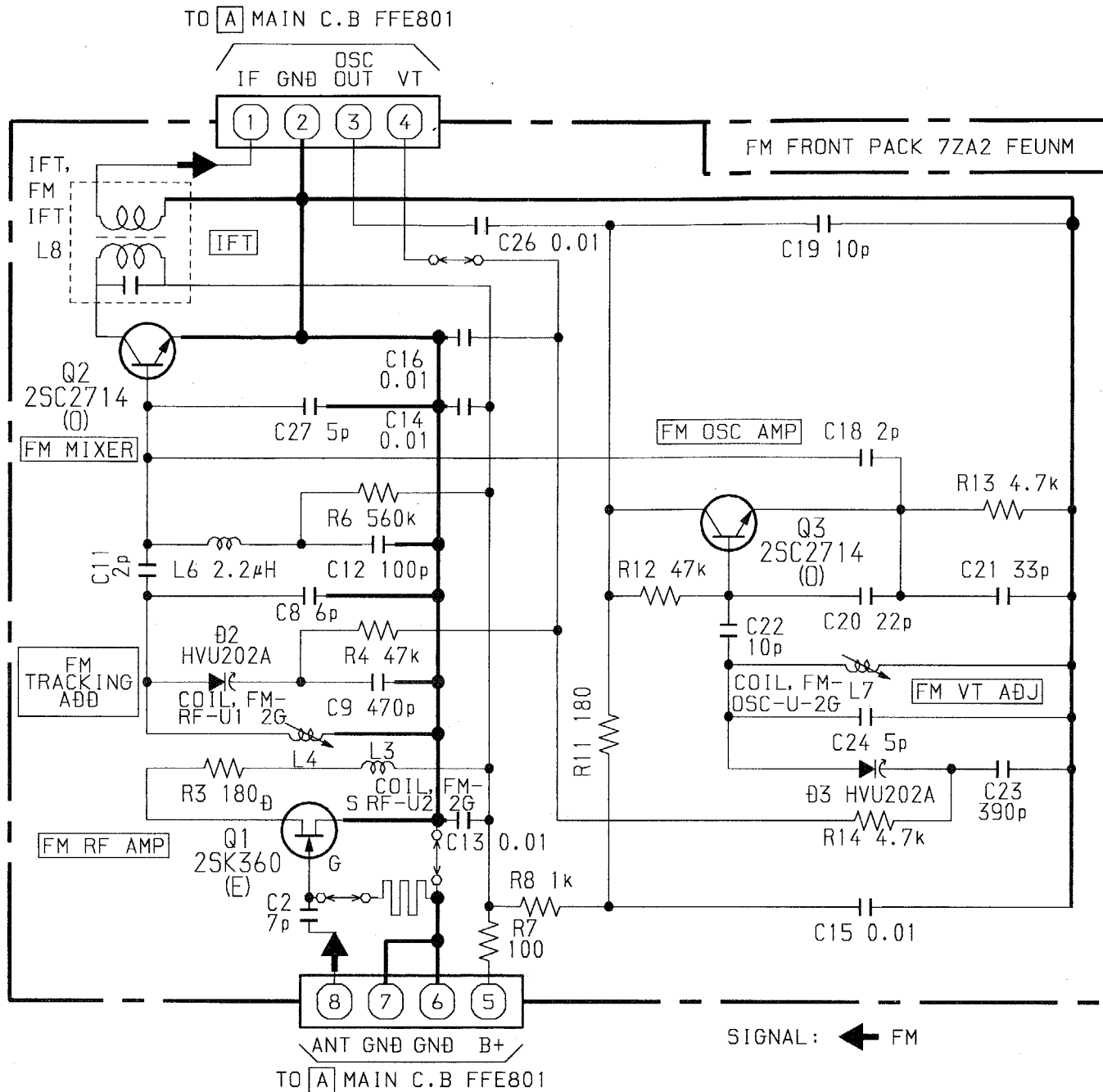


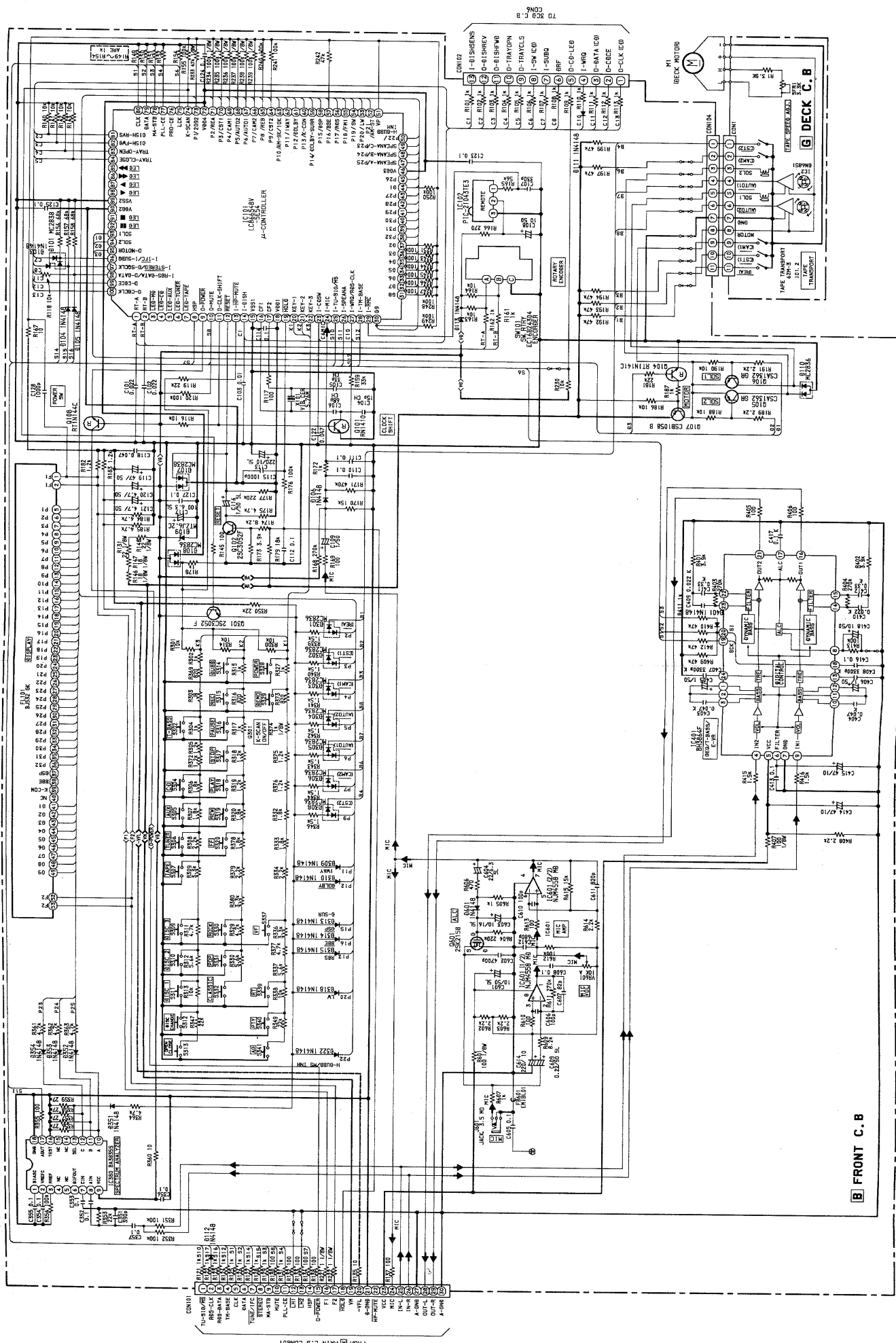






SCHEMATIC DIAGRAM - 8 (TUNER : 7ZA - 2)

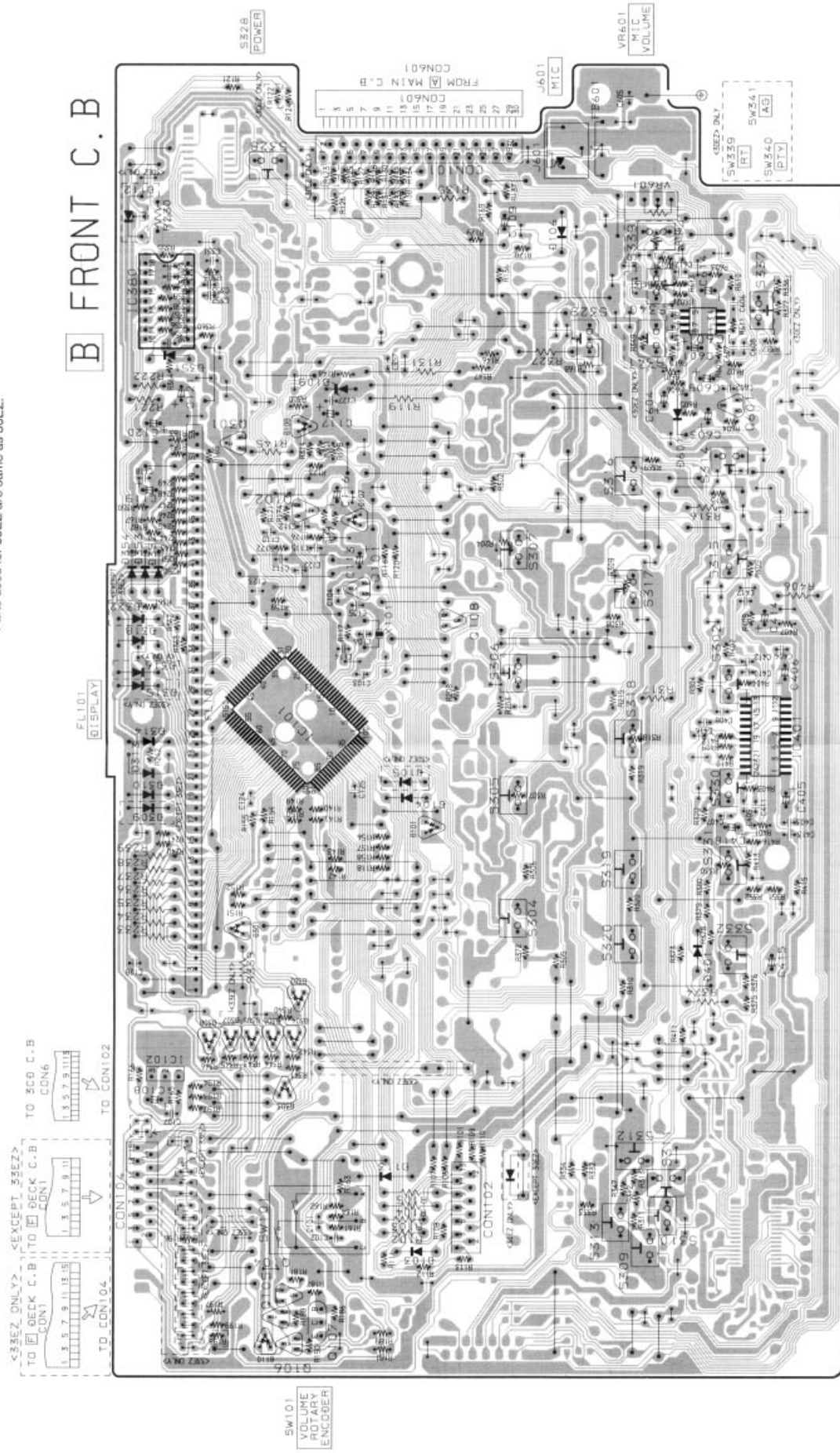


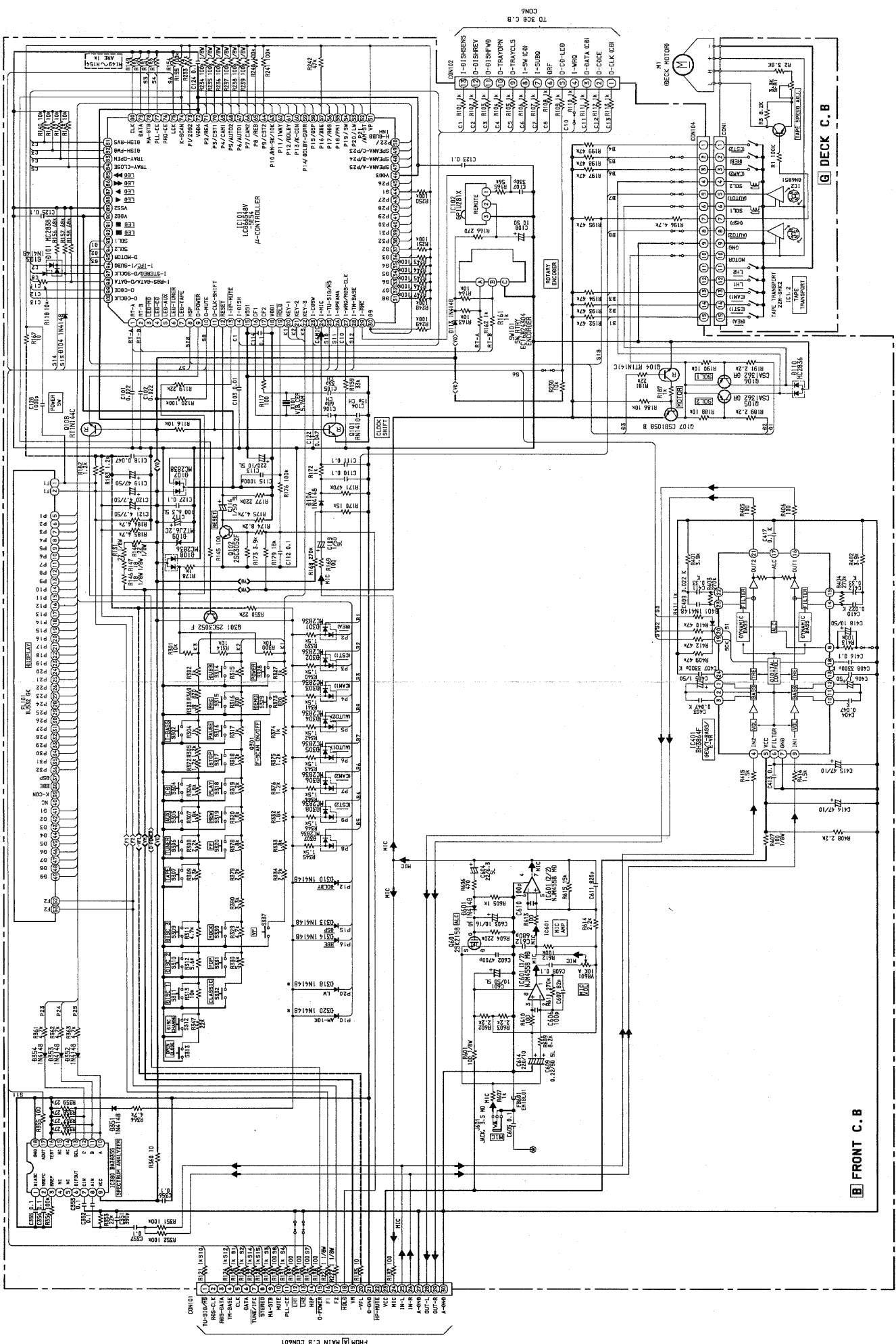


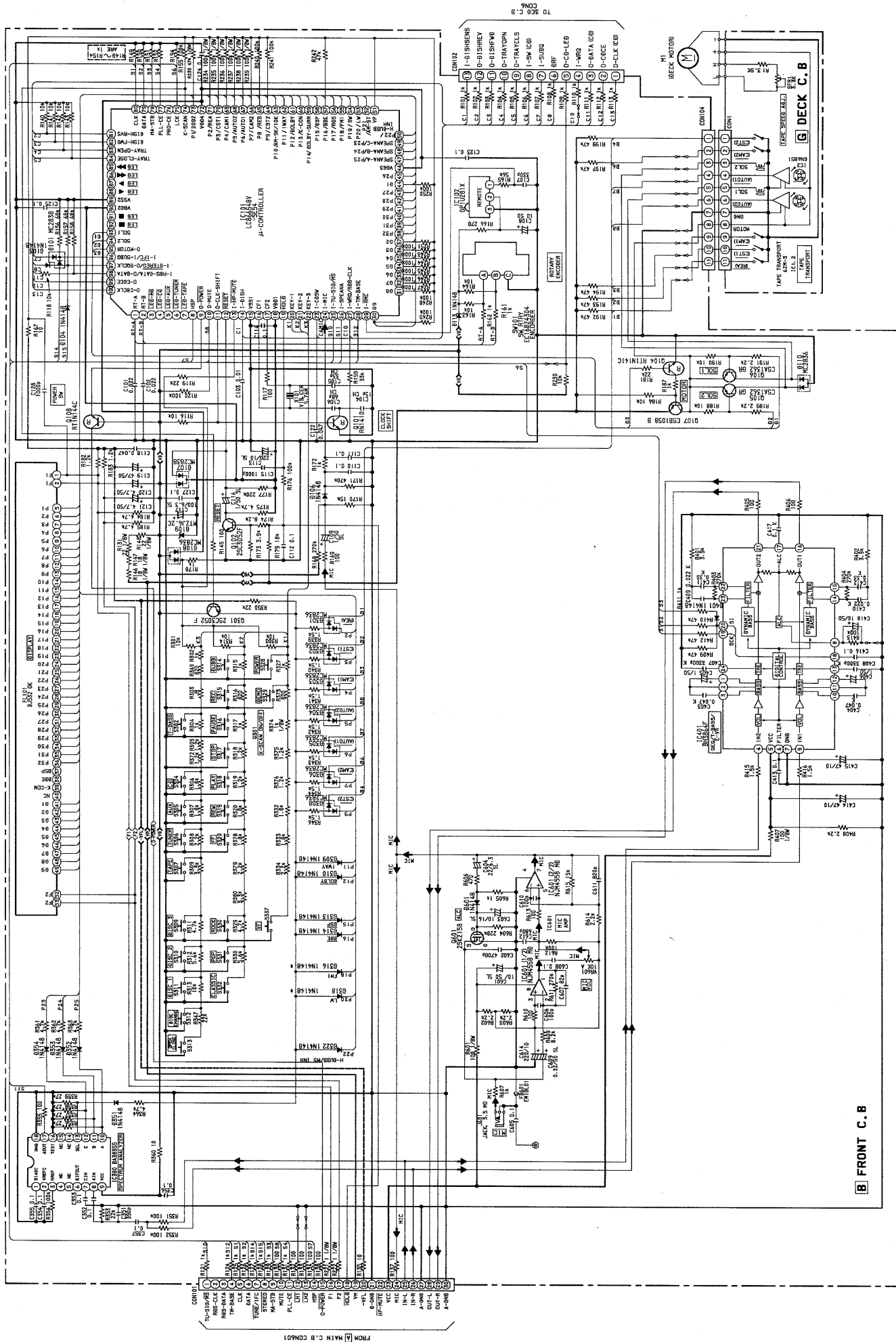
1 2 3 4 5 6 7 8 9 10 11 12 13 14

* Parts used for 35EZ are same as 30EZ.

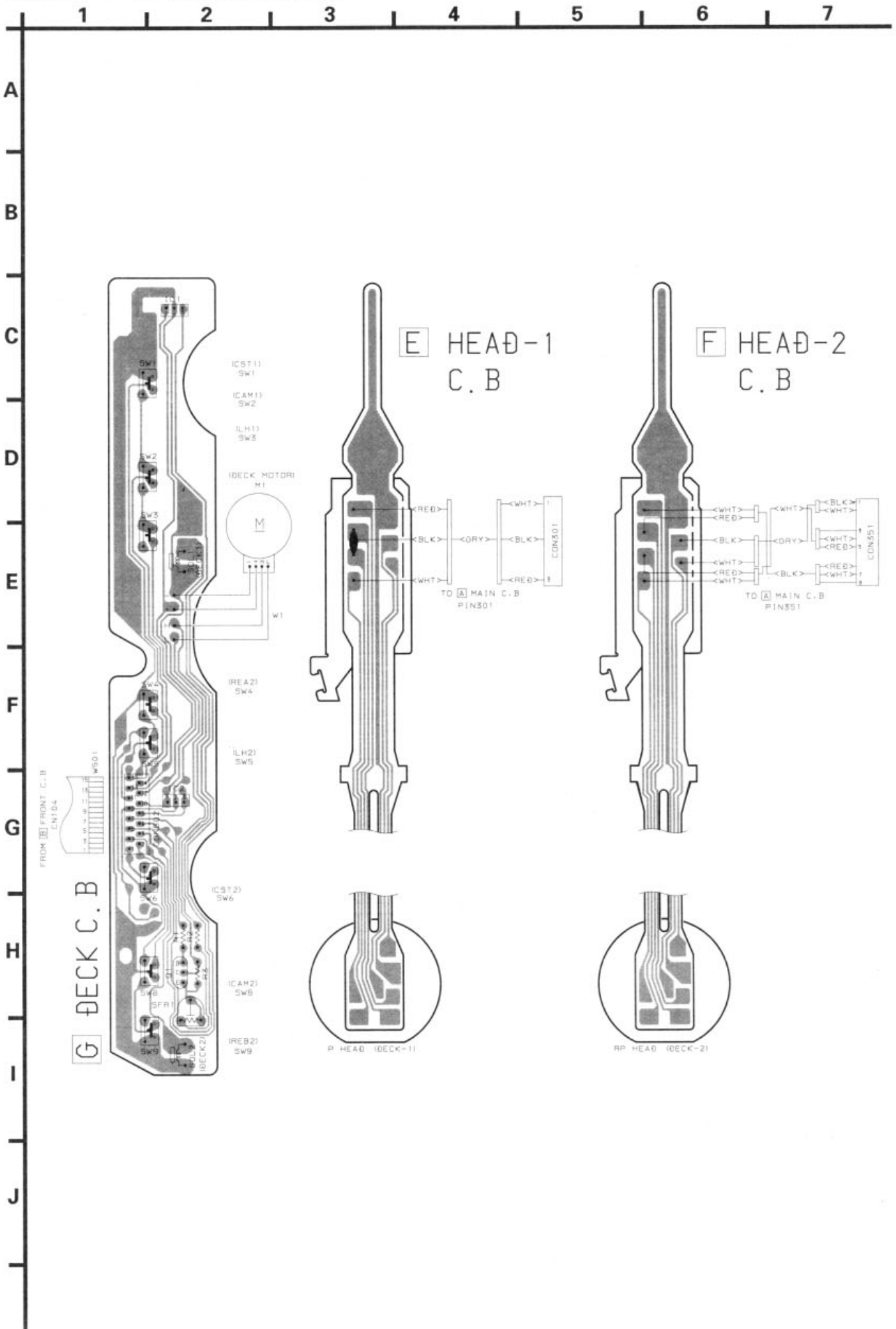
B FRONT C.B.

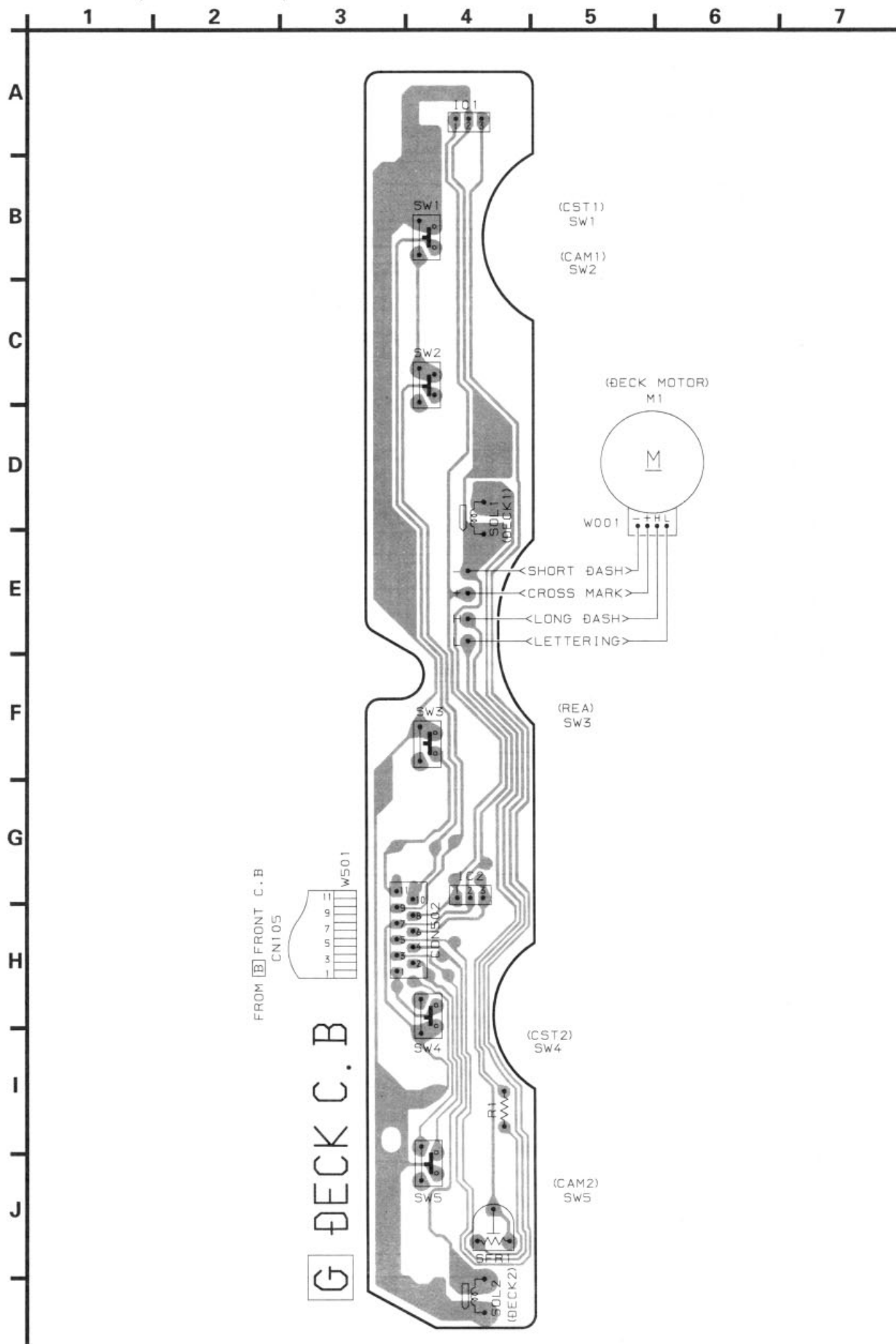


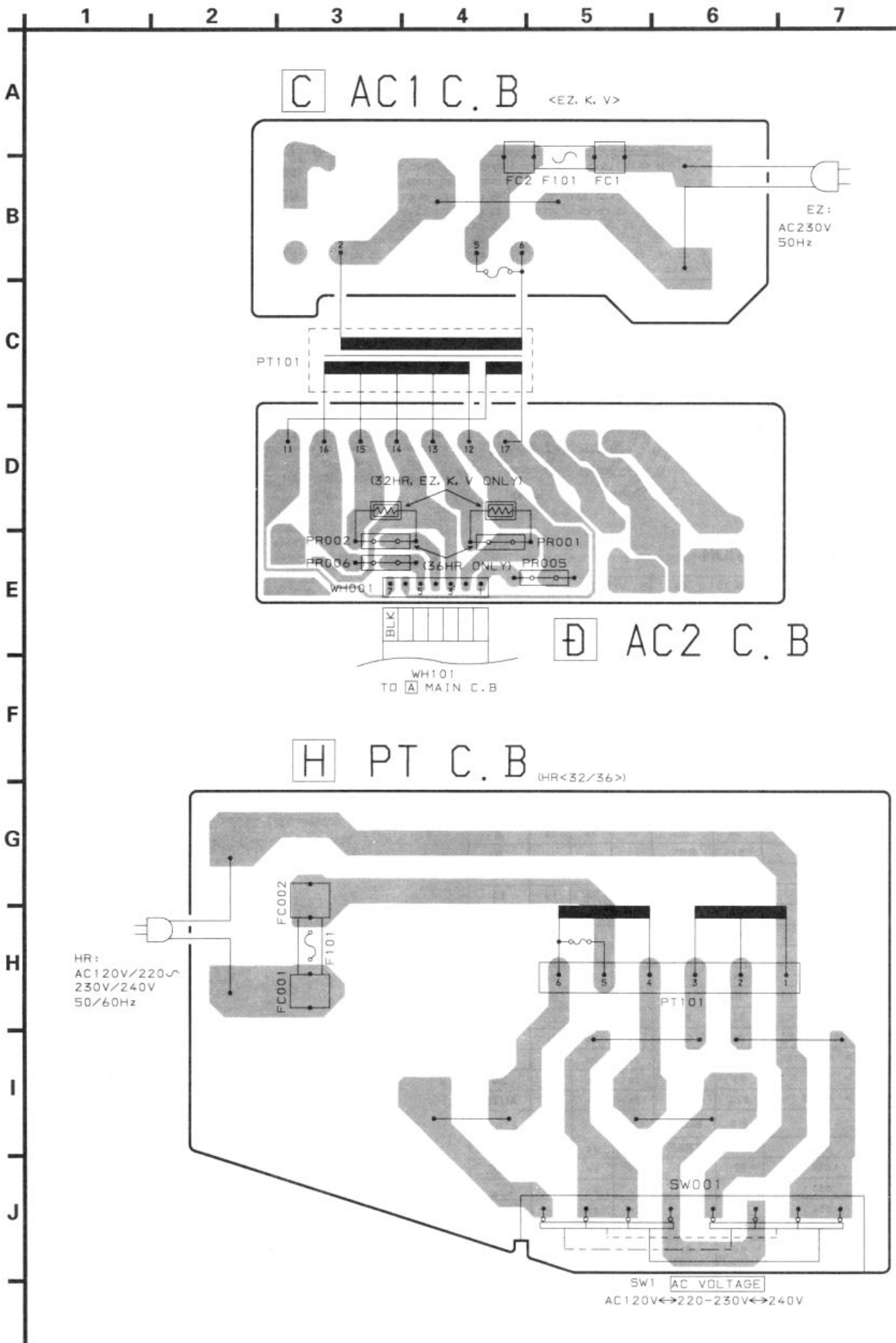




WIRING - 5 (DECK : 2ZM-3 MK2)



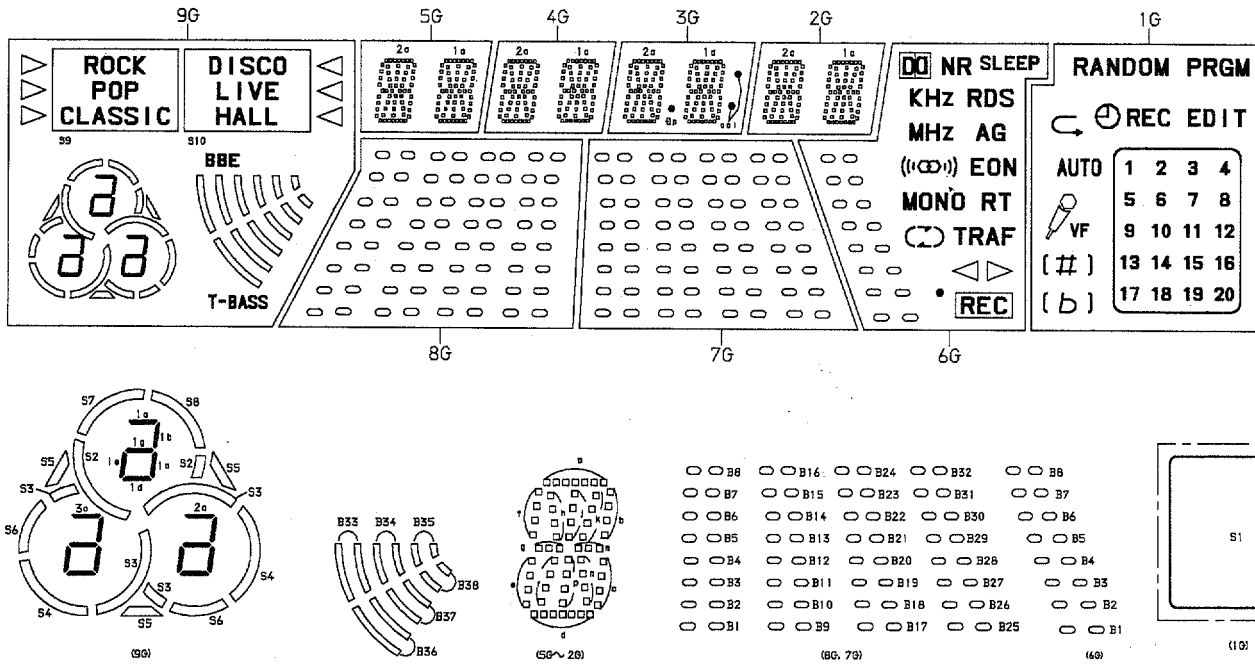




FL GRID ASSIGNMENT AND ANODE CONNECTION

FL, BJ532GK

GRID ASSIGNMENT

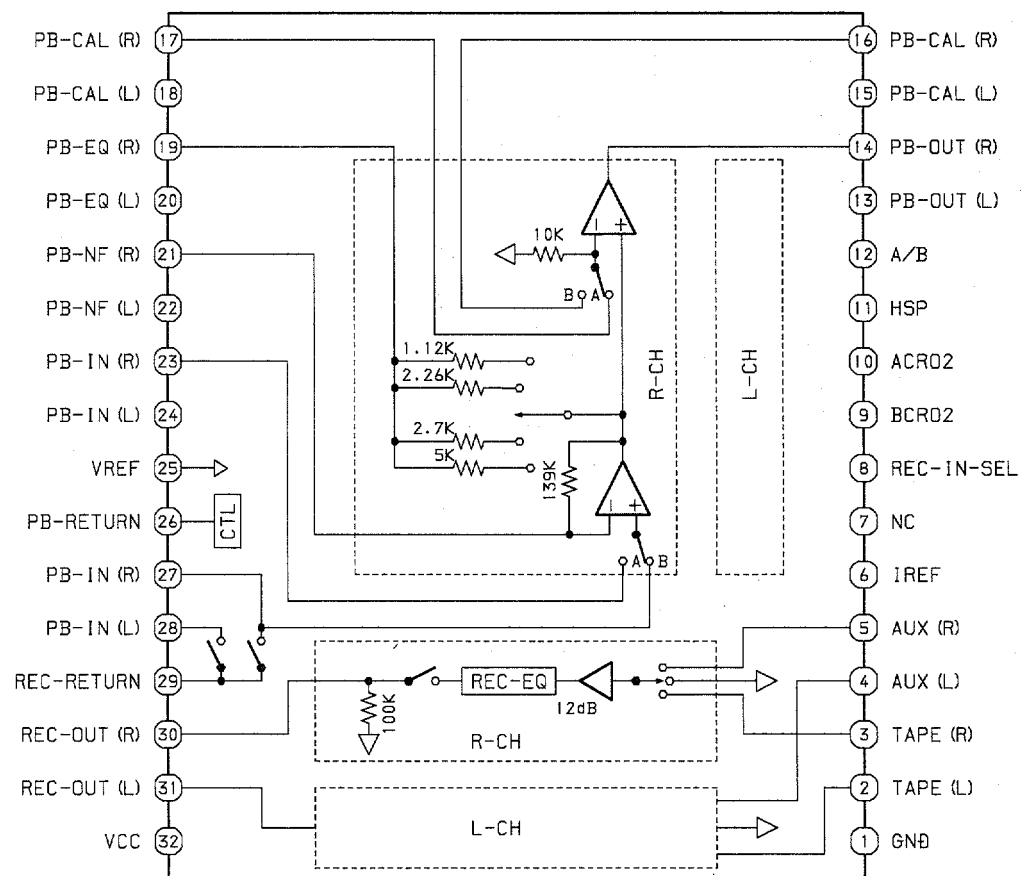


ANODE CONNECTION

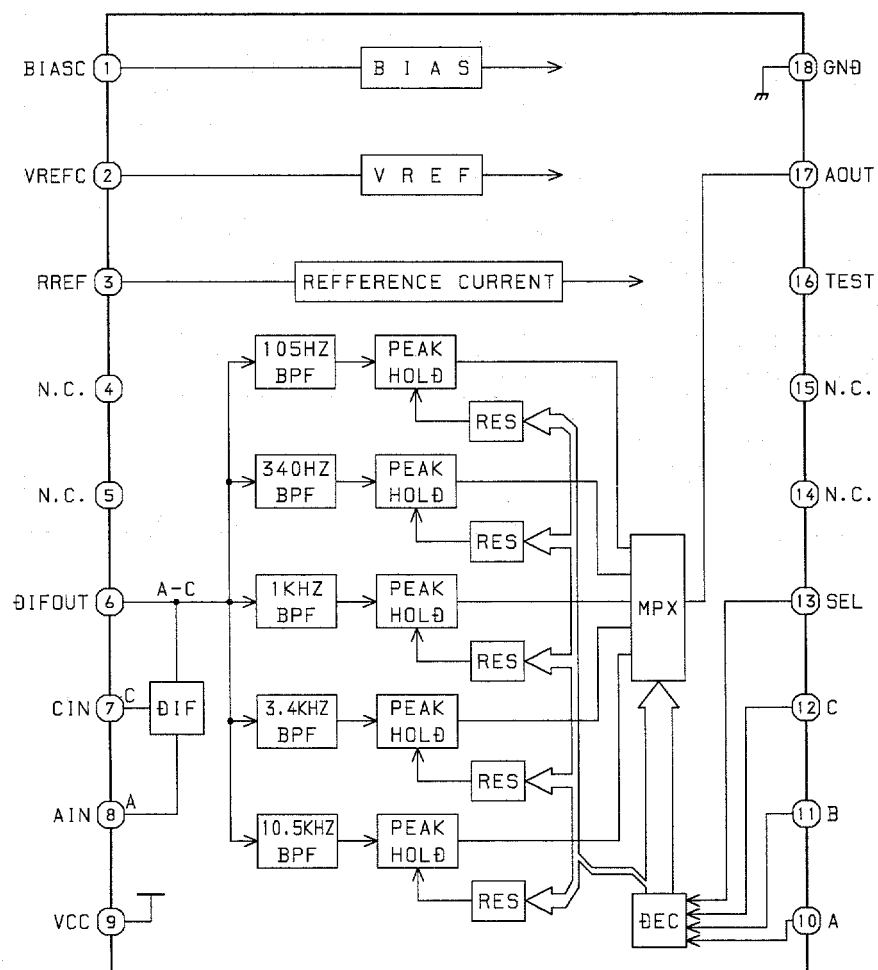
	9G	8G, 7G	6G	5G, 4G	3G	2G	1G
P1	S8	B32	▷	-	c o (DND)	-	RANDOM
P2	S2	B24	◁	1d	1d	1d	-
P3	1b	B16	SLEEP	1n	1n	1n	PRGM
P4	1c	B8	B8	1p	1p	1p	EDIT
P5	1e	B31	○	1r	1r	1r	1
P6	1a, 1d, 1g	B23	(REC)	1e	1e	1e	2
P7	2b	B15	KHz	1c	1c	1c	3
P8	2c	B7	B7	1q	1q	1q	4
P9	2e	B30	MHz	1a	1a	1a	5
P10	2a, 2d, 2g	B22	-	1t	1t	1t	6
P11	3b	B14	[DO] NR	1b	1b	1b	7
P12	3c	B6	B6	1k	1k	1k	8
P13	3e	B29	RDS	1j	1j	1j	9
P14	3a, 3d, 3g	B21	-	1h	1h	1h	10
P15	S3	B13	-	1o	1o	1o	11
P16	S5	B5	B5	-	c o (UP)	-	12
P17	S7	B28	-	-	8p	-	13
P18	S4	B20	-	2a	2a	2a	14
P19	S6	B12	-	2n	2n	2n	15
P20	(HALL) ◁	B4	B4	2p	2p	2p	16
P21	(LIVE) ◁	B27	AG	2r	2r	2r	17
P22	(DISCO) ◁	B19	((∞))	2e	2e	2e	18
P23	▷ (CLASSIC)	B11	EON	2c	2c	2c	19
P24	▷ (POP)	B3	B3	2q	2q	2q	20
P25	▷ (ROCK)	B26	RT	2m	2m	2m	AUTO
P26	B36	B18	MONO	2f	2f	2f	VF
P27	B37	B10	TRAF	2b	2b	2b	⊕
P28	B38	B2	B2	2k	2k	2k	REC
P29	B33	B25	)	2l	2l	2l	↻
P30	B34	B17	⌂	2h	2h	2h	((#))
P31	B35	B9	(	2o	2o	2o	((b))
P32	S9 T-BASS	B1	B1	-	-	-	S1
P33	S10	-	-	-	-	-	-
P34	BBE	-	-	-	-	-	-
P35	-	-	-	-	-	-	b #

IC BLOCK DIAGRAM

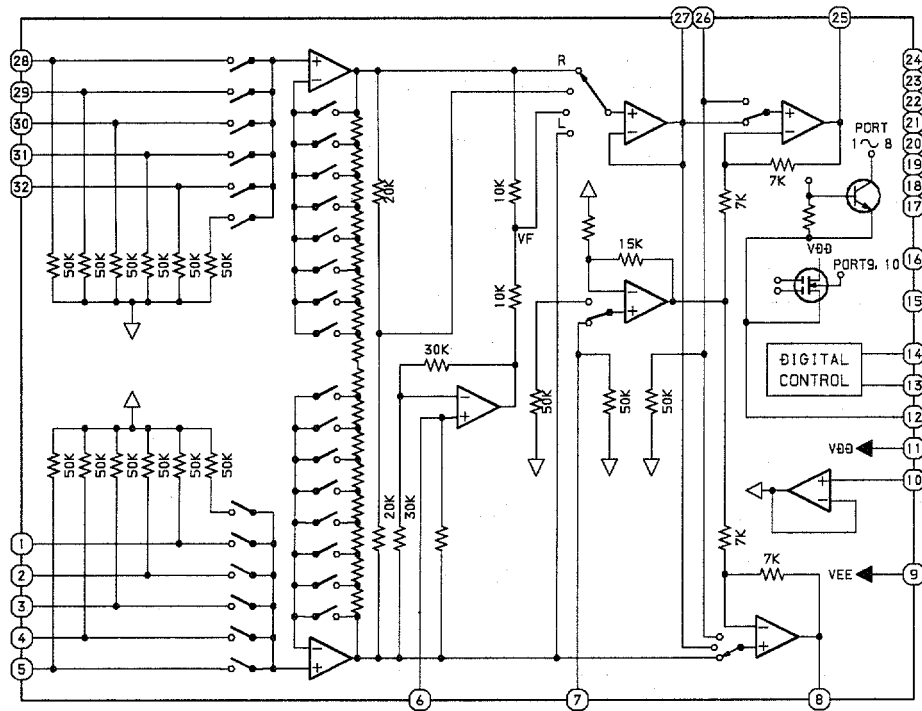
IC, BA7762FS



IC, BA3835S



IC, BH3810FS



IC, LA1837

