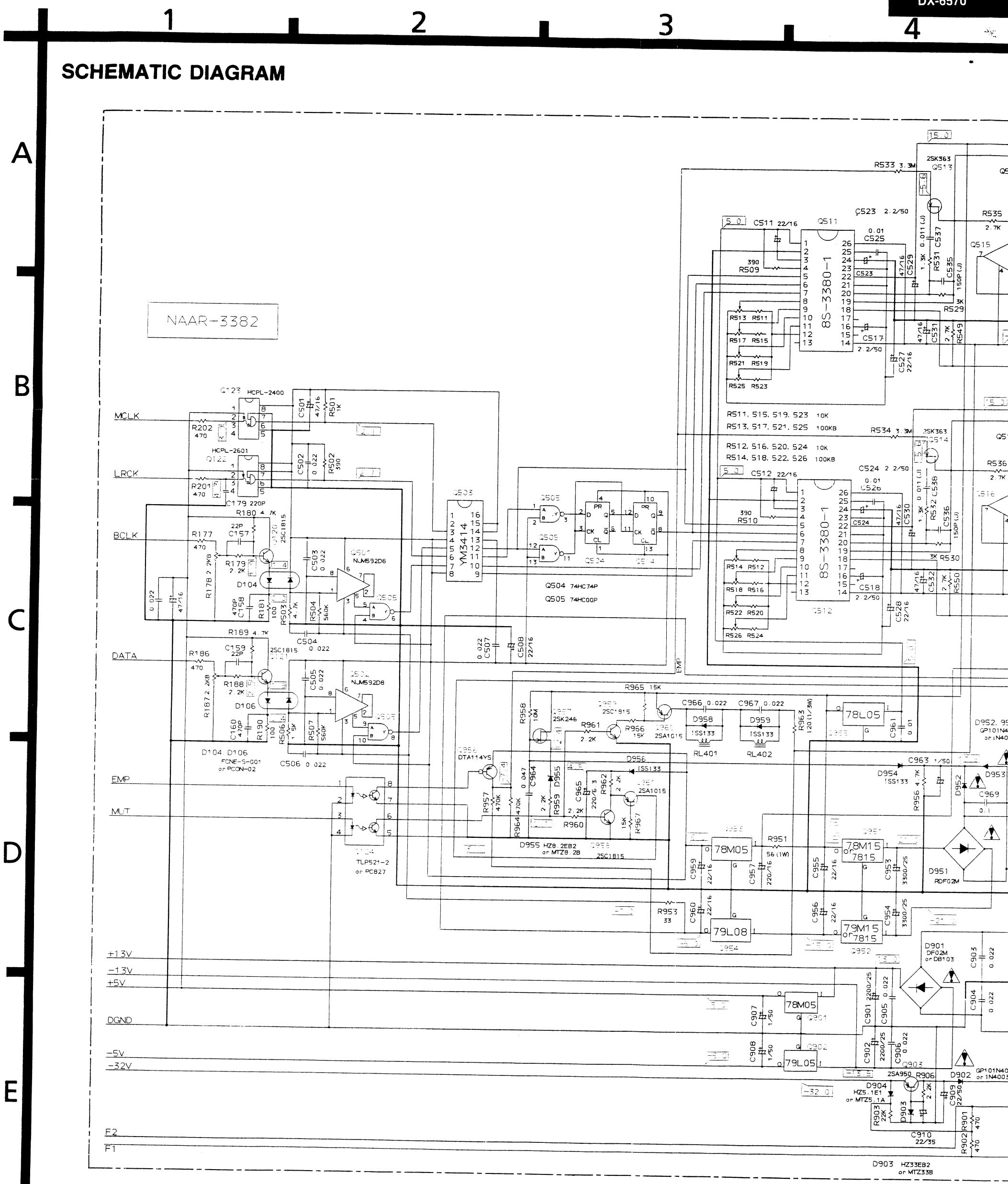
ONKYO

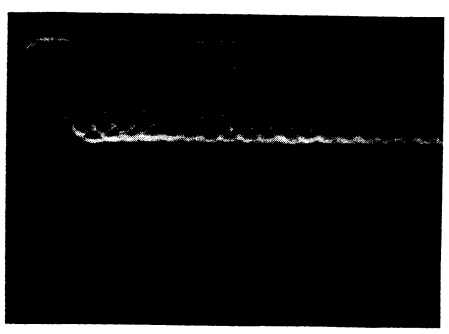
DX6570

MODEL

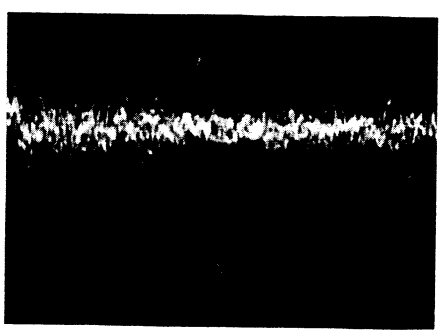
SERVICE MANUAL

SCHEMATIC DIAGRAM

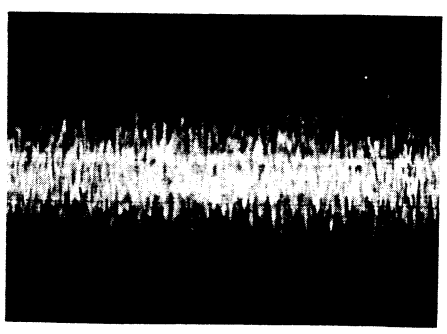




TP RF (RF signal)
Vertical : 1V/div.
Holizontal : 1 ms/div.
DC, Ground: Center

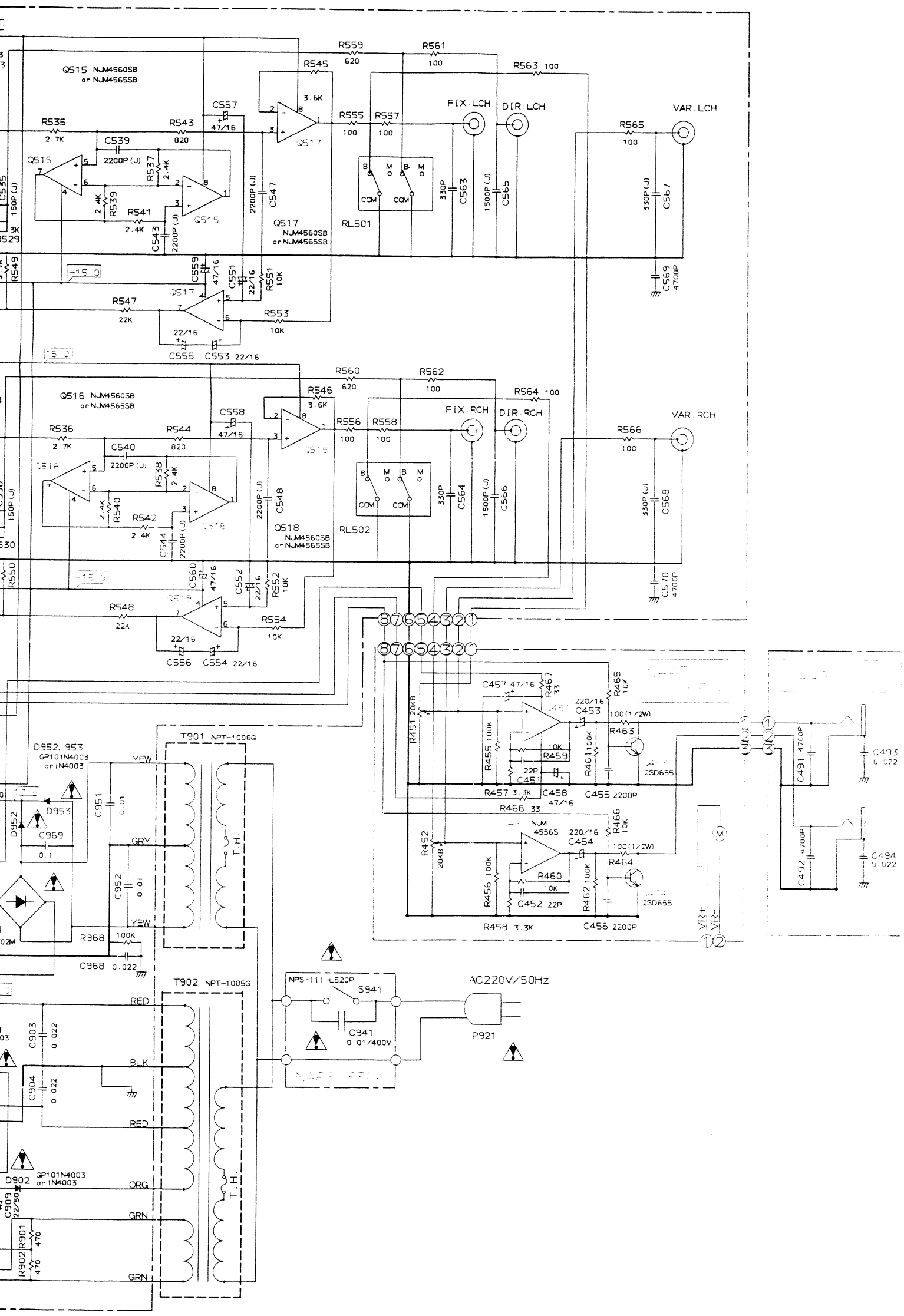


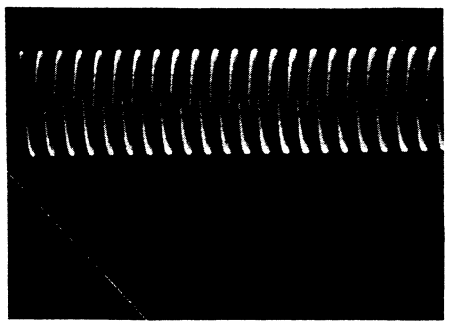
TP FO (Focus out)
Vertical : 0.5V/div.
Holizontal : 0.5 ms/div.
DC, Ground: Center



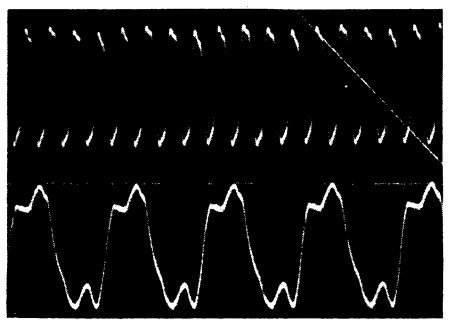
TP TO (Tracking out)
Vertical : 0.2V/div.
Holizontal : 0.5 ms/div.
DC, Ground: Center

5 6 7 8

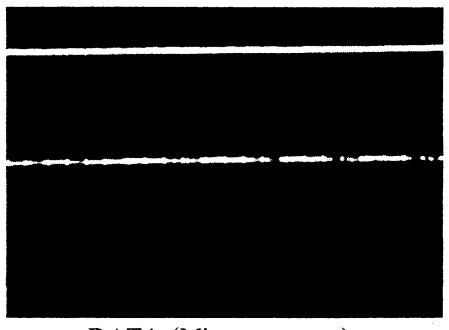




P110 PLCK
Vertical : 0.5V/div.
Holizontal : 0.2 μs/div.
DC, Ground: Center

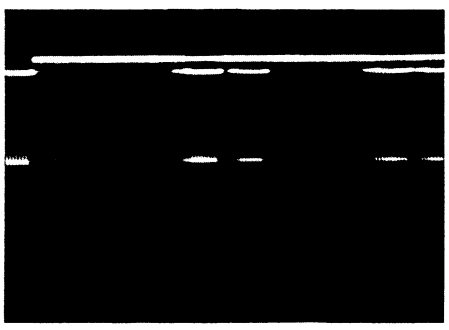


Vertical : 2V/div.
Holizontal: 0.1 μs/div.
X'tal (Q118 Pin 4)/C4M (R164)
AC

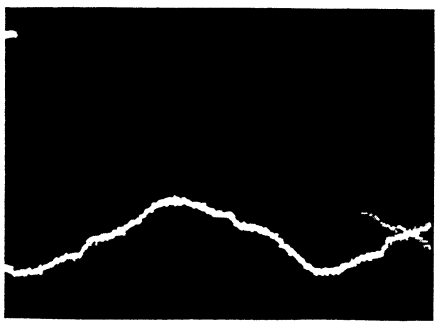


DATA (Microprocessor)
Vertical : 2V/div.
Holizontal : 0.5 ms/div.
DC, Ground: Center

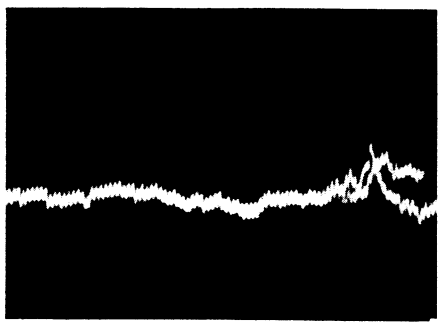
XLT
Vertical : 2V/div.
Holizontal: 0.5 ms/div.



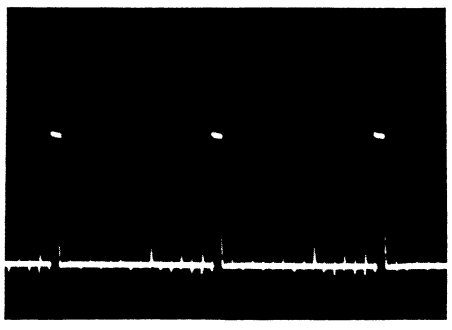
CLK
Vertical : 2V/div.
Holizontal : 50 μs/div.
DC, Ground: Center



TP SLD (Slide out)
Vertical :2V/div.
Holizontal :20 ms/div.
Top :Real
Bottom :Storage



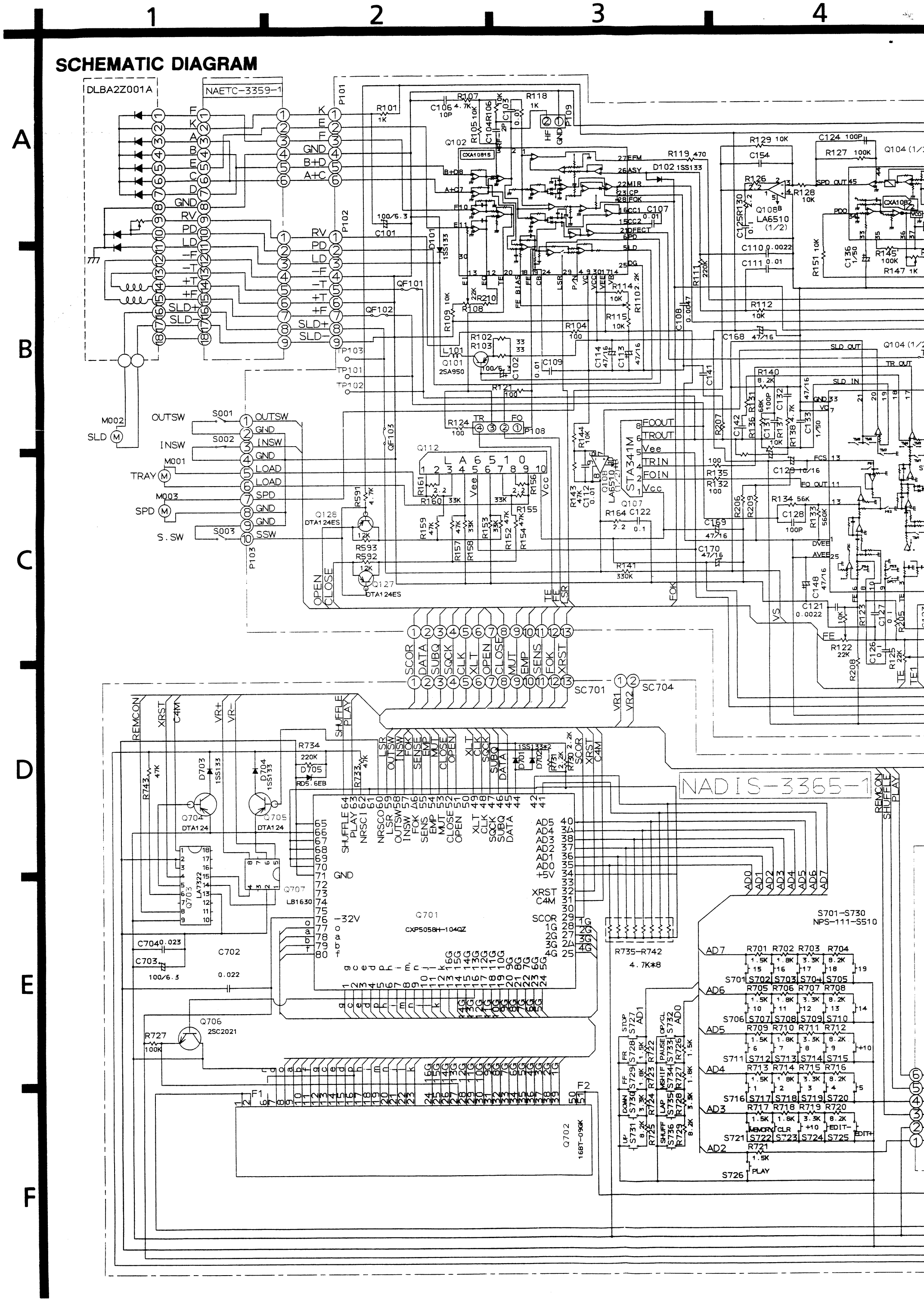
TP SPD (Spindle out)
Vertical : 1V/div.
Holizontal : 5 ms/div.
DC, Ground: Center

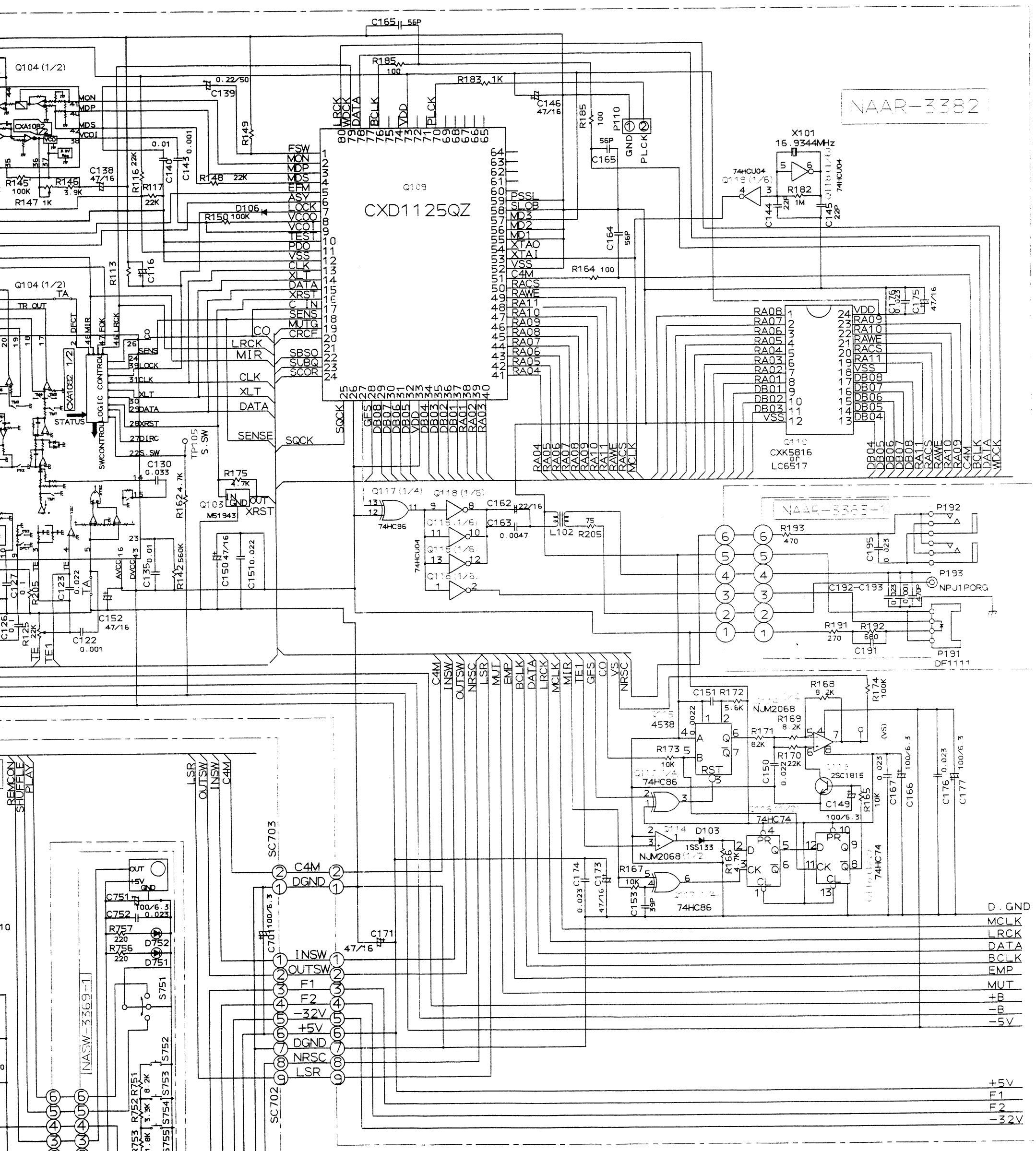


Grid
Vertical : 10V/div.
Holizontal : 1 ms/div.
DC, Ground: Center

V07007

SCHEMATIC DIAGRAM





- ALL RESISTORS ARE IN OHMS 1/4WATT UNLESS OTHERWISE NOTED.
- ALL CAPACITORS ARE IN μF/50wV UNLESS OTHERWISE NOTED.
- ELECTROLYTIC CAPACITORS (⊣⊢) ARE IN μF/WV.
- VOLTAGE (MEASURED WITH V.T.V.M) MEASURED WITH ▭ V IS DC VOLTAGE. (NO INPUT)
- THE CONPONENTS IDENTIFIER ⚠ ARE CRITICAL FOR SAFETY. REPLACE ONLY WITH PART NUMBER SPECIFIED.
- CIRCUIT IS SUBJECT TO CHANGE FOR IMPROVEMENT.