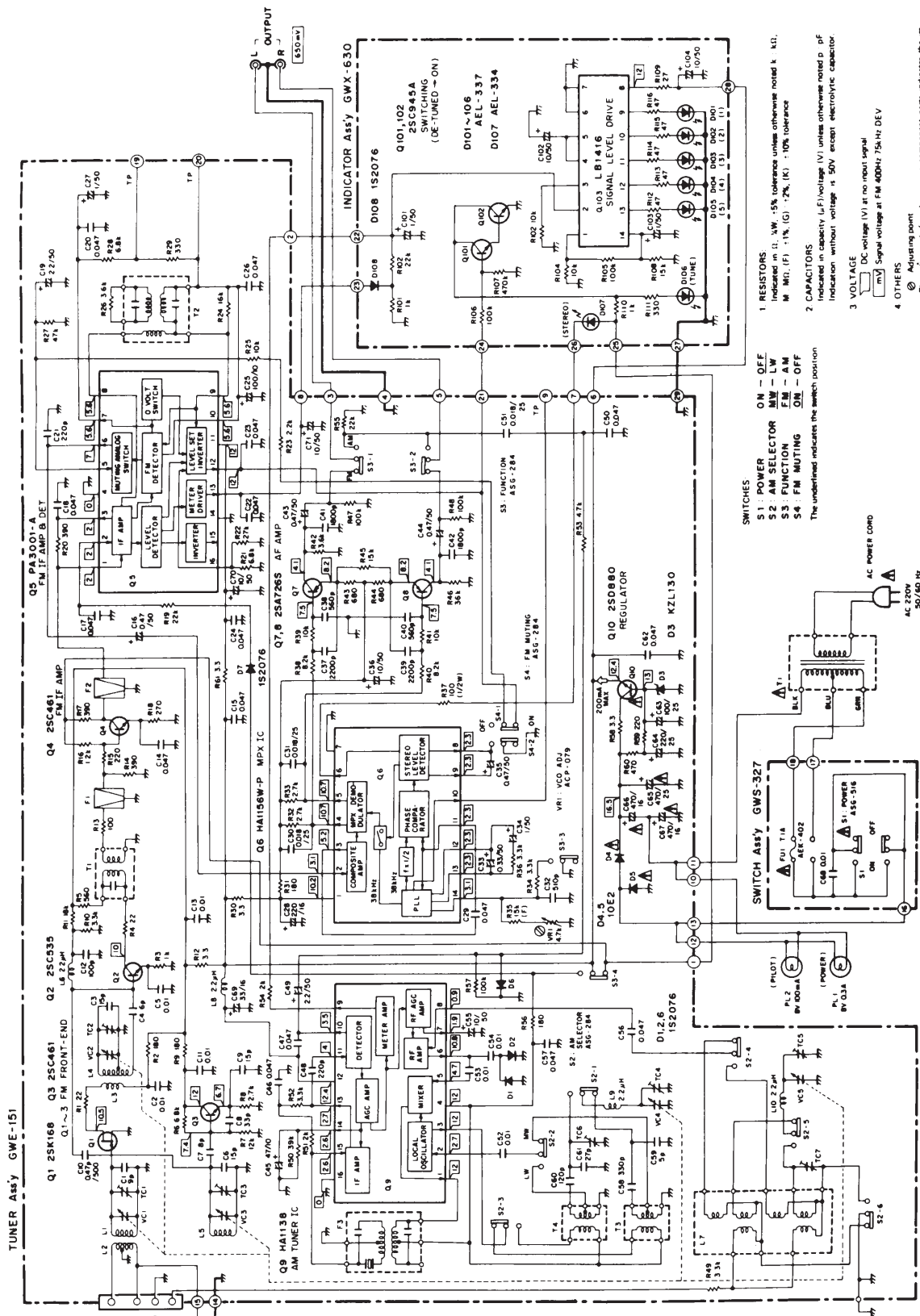


## SCHEMATIC DIAGRAM



**RESISTORS:**  
Indicated in  $\Omega$ ,  $\frac{1}{2}W$ , -5% tolerance unless otherwise noted k: k $\Omega$ ,  
M: M $\Omega$  (F)  $\pm 1\%$  (G)  $\pm 2\%$  (K)  $\pm 10\%$  tolerance

Indicated in capacity ( $\mu$ F)/voltage (V) unless otherwise noted p = pF  
Indication without voltage is 50V except electrolytic capacitor.

3 VOLTAGE

DC voltage (V) at no input signal

mV Signal voltage at FM 400Hz 75uV DE V

4. OTHERS

⊗ Adjusting point

The  $\frac{1}{4}$  mark found on some component parts indicates the amount of adjustment required.

This is the basic schematic diagram, but the actual circuit may vary due to improvements in design.

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