

AV RECEIVER/AV AMPLIFIER

RX-V461/HTR-6040/DSP-AX461

SERVICE MANUAL

For U, C, R, K, A, L and J models

This service manual is the RX-V461/HTR-6040/DSP-AX461 (U, C, R, K, A, L and J models).
For service manual of the RX-V461/RX-V461DAB (T, B, G and E models), please refer to the following publication number:

RX-V461/RX-V461DAB (T, B, G and E models): 101048

IMPORTANT NOTICE

This manual has been provided for the use of authorized YAMAHA Retailers and their service personnel.
It has been assumed that basic service procedures inherent to the industry, and more specifically YAMAHA Products, are already known and understood by the users, and have therefore not been restated.

WARNING: Failure to follow appropriate service and safety procedures when servicing this product may result in personal injury, destruction of expensive components, and failure of the product to perform as specified. For these reasons, we advise all YAMAHA product owners that any service required should be performed by an authorized YAMAHA Retailer or the appointed service representative.

IMPORTANT: The presentation or sale of this manual to any individual or firm does not constitute authorization, certification or recognition of any applicable technical capabilities, or establish a principle-agent relationship of any form.

The data provided is believed to be accurate and applicable to the unit(s) indicated on the cover. The research, engineering, and service departments of YAMAHA are continually striving to improve YAMAHA products. Modifications are, therefore, inevitable and specifications are subject to change without notice or obligation to retrofit. Should any discrepancy appear to exist, please contact the distributor's Service Division.

WARNING: Static discharges can destroy expensive components. Discharge any static electricity your body may have accumulated by grounding yourself to the ground buss in the unit (heavy gauge black wires connect to this buss).

IMPORTANT: Turn the unit OFF during disassembly and part replacement. Recheck all work before you apply power to the unit.

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このサービスマニュアルは、エコマーク認定の再生紙を使用しています。
This Service Manual uses recycled paper.



■ UPDATING FIRMWARE / ファームウェアの書き込み

After replacing the following parts with the replacement part, be sure to write the latest firmware.

- DSP P.C.B.
- IC201 (DSP P.C.B.)

● Required Tools

- DVD or CD player (with DIGITAL OUTPUT (OPTICAL or COAXIAL) terminal)
- Optical cable (when OPTICAL terminal is used)
- Digital audio pin cable (when COAXIAL terminal is used)
- Firmware CD
 - * To make the firmware CD, download the latest firmware from the specified download source to PC.

● Operation Procedures

1. Connect the main unit and DVD/CD player as shown below. (Fig. 1)

下記部品をサービス部品に交換した場合、最新のファームウェアの書き込みを行ってください。

- DSP P.C.B.
- IC201 (DSP P.C.B.)

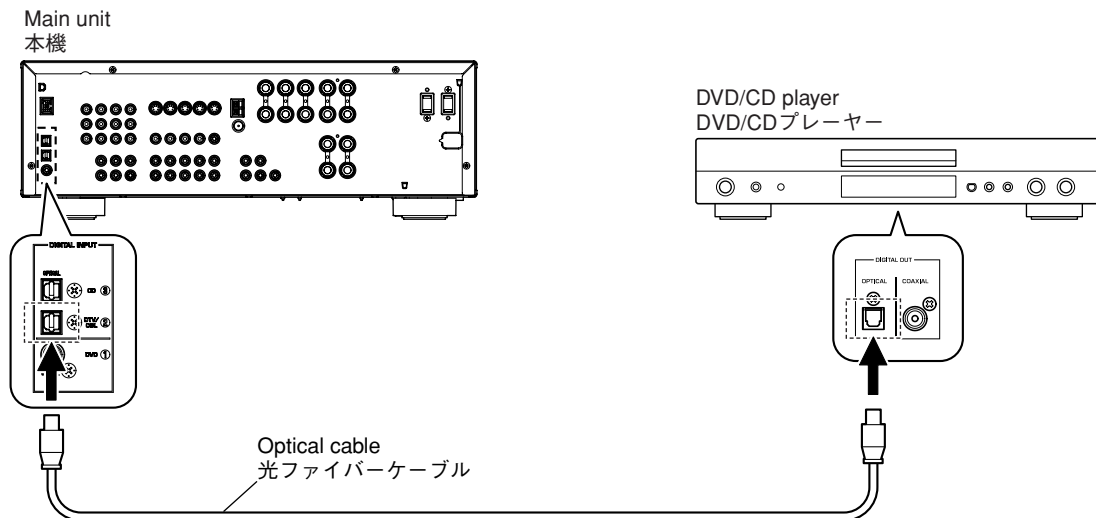
● 必要なツール

- DVDまたはCDプレーヤー (DIGITAL OUTPUT (OPTICALまたはCOAXIAL)端子付き)
- 光ファイバーケーブル (OPTICAL端子使用時)
- デジタル音声ピンケーブル (COAXIAL端子使用時)
- ファームウェアCD
 - ※ ファームウェアCDは、PCへ最新のファームウェアを指定のダウンロード先からダウンロードして制作してください。

● 操作方法

1. 本機とDVD/CDプレーヤーを下記のように接続します。(Fig. 1)

Example of OPTICAL terminal / OPTICAL端子使用例



Example of COAXIAL terminal / COAXIAL端子使用例

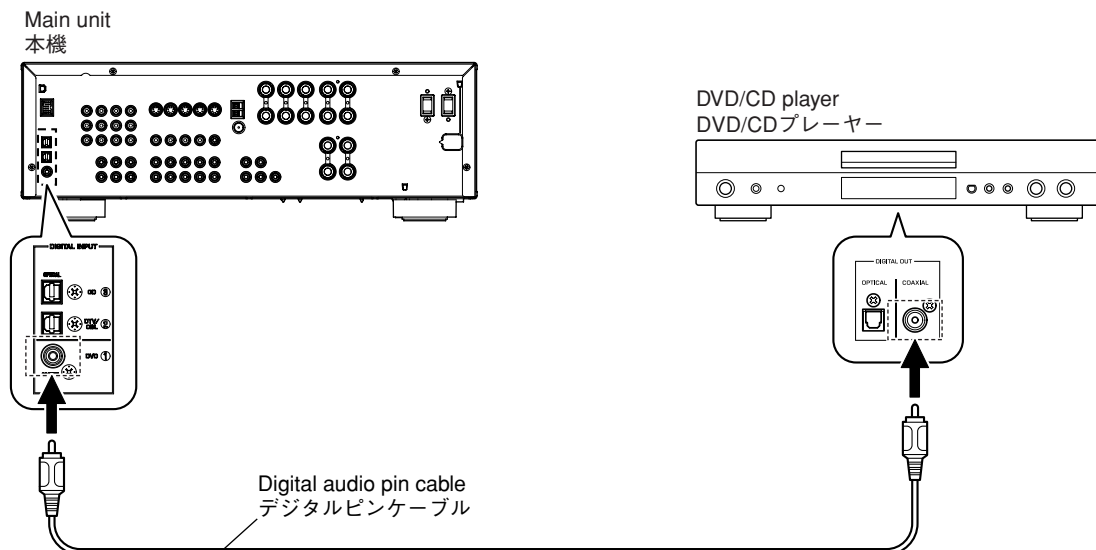


Fig. 1

- While pressing the "STANDBY/ON" key and "SPEAKERS A/B/OFF" key of the main unit simultaneously, connect the power cable of the main unit to the AC outlet. (Fig. 2)

The FIRMWARE UPDATE mode will then be activated and "SPDIF Upgrade" is displayed. (Fig. 2)

- 本機の"STANDBY/ON"キーと、"SPEAKERS A/B/OFF"キーを押しながら、本機の電源コードをACコンセントに接続します。(Fig. 2)
FIRMWARE UPDATEモードが起動し、"SPDIF Upgrade"が表示されます。(Fig. 2)

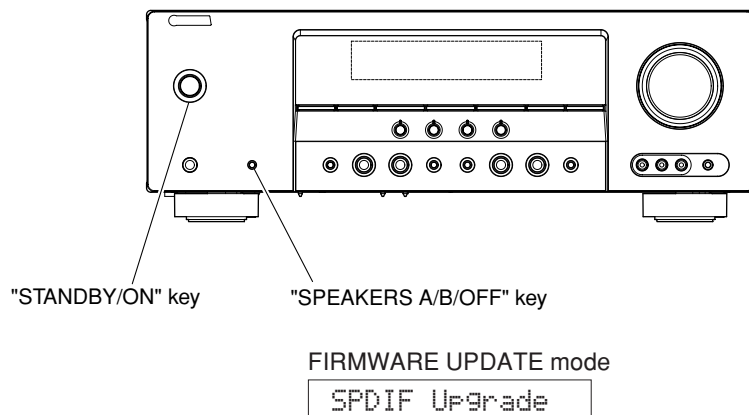


Fig. 2

- Connect the power cable of DVD/CD player to the AC outlet.
- Press the "STANDBY/ON" key of the DVD/CD player.
- Press the "EJECT" key of the DVD/CD player to open the tray.
- Put the firmware CD on the tray and close the tray.
- Press the "PLAY" key of the DVD/CD player. Then writing of the firmware is started. (Fig. 3)
- When writing of the firmware is completed, "Upgrade OK", "Please..." and "Turn off!!" are displayed repeatedly. (Fig. 3)

- DVD/CDプレーヤーの電源コードをACコンセントに接続します。
- DVD/CDプレーヤーの"STANDBY/ON"キーを押します。
- DVD/CDプレーヤーの"EJECT"キーを押し、トレイを開きます。
- ファームウェアCDをトレイに載せ、トレイを閉じます。
- DVD/CDプレーヤーの"PLAY"キーを押します。ファームウェアの書き込みが開始されます。(Fig. 3)
- ファームウェアの書き込み完了後、"Upgrade OK"、"Please..."、"Turn off!!"が繰り返し表示されます。(Fig. 3)

Writing is started. / 書き込み開始

Address:XXXXXX

XXXXXX: Address information of the received data /
受信データのアドレス情報



Writing is completed. / 書き込み完了

Upgrade OK

Please...

Turn off!!

Fig. 3

- * When the version of the firmware to be written is the same as the one existing in the main unit, "Same Version", "Please..." and "Turn off!!" are displayed repeatedly. (Upgrading is not necessary.)

If the display remains unchanged for more than 10 seconds after starting the firmware CD play procedure, perform the firmware CD play procedure again from the beginning.

If "FILE CORRUPTED" is displayed after "Address:XXXXXX", check to make sure that the written data is not corrupted and perform Steps 1 to 8 of "Operation Procedures" again.

If "Upgrade Failed" is displayed, perform Steps 1 to 8 of "Operation Procedures" again.

9. Press the "STOP" key of the DVD/CD player.
10. Press the "EJECT" key of the DVD/CD player to open the tray.
11. Remove the firmware CD from the tray and close the tray.
12. Turn off the power of the DVD/CD player and disconnect the power cable from the AC outlet.
13. Turn off the power by pressing the "STANDBY/ON" key of the main unit.

● Confirmation of firmware version and checksum

Confirm that the firmware version and checksum value is updated successfully with the DIAG function.

For more information, refer to "SELF DIAGNOSIS FUNCTION (DIAG)".

- * When the displayed firmware version and checksum are different from written firmware version and checksum, follow the steps from 1 to 13 of "Operation Procedures" again.

- ※ 本機に既存のファームウェアと、書き込もうとしているファームウェアのバージョンが同じ場合、"Same Version"、"Please..."、"Turn off!!"の表示が繰り返されます。(バージョンアップの必要はありません。)

ファームウェアCDの再生開始後、10秒以上経過してもディスプレイ表示が変わらない場合、ファームウェアCDの再生を最初からやり直してください。

"Address:XXXXXX"の後に、"FILE CORRUPTED"が表示された場合、書き込みデータが破損していないかを確認し、"操作方法"の1から8までをもう一度やり直してください。

"Upgrade Failed"が表示された場合、"操作方法"の1から8までをもう一度やり直してください。

9. DVD/CDプレーヤーの"STOP"キーを押します。
10. DVD/CDプレーヤーの"EJECT"キーを押し、トレイを開きます。
11. ファームウェアCDをトレイから外し、トレイを閉じます。
12. DVD/CDプレーヤーの電源を切り、電源コードをACコンセントから抜きます。
13. 本機の"STANDBY/ON"キーを押して電源を切ります。

● ファームウェアバージョンおよびチェックサムの確認

ダイアグメニューでファームウェアのバージョンおよびチェックサムが正しく更新されたことを確認します。

ダイアグメニューの詳細は「自己診断(ダイアグ)」を参照してください。

- ※ 表示されたファームウェアのバージョンおよびチェックサムが、書き込んだファームウェアのバージョンおよびチェックサムと異なる場合、"操作方法"の1から13までをもう一度やり直してください。

■ SELF DIAGNOSIS FUNCTION (DIAG)／自己診断機能 (ダイヤグ)

This unit has self diagnosis functions that are intended for inspection, measurement and location of faulty point.

There are 18 DIAG menu items, each of which has sub-menu items.

Listed in the table below are menu items and sub-menu items.

Note that not all menu items listed will apply to the models covered in this service manual.

本機には、検査、測定、不良個所の発見を目的にした自己診断機能 (ダイヤグ) があります。

ダイヤグメニューは18個あり、そのそれぞれにサブメニューがあります。

下表はダイヤグメニュー一覧です。

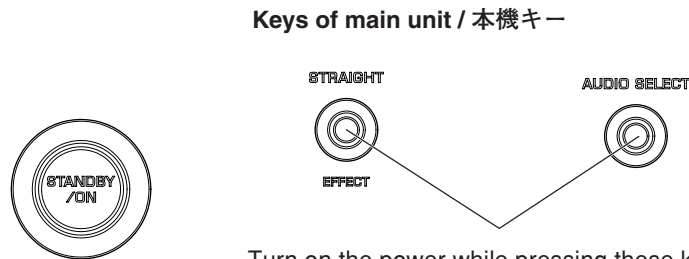
下表の全ダイヤグメニュー項目が、このサービスマニュアル記載のモデルに適用されるとは限りません。

No.	DIAG menu	Sub-menu
1	BYPASS	ANALOG BYPASS DSP BYPASS
2	AUDIO CHECK	AUDIO CHECK
3	SPEAKERS SET	FRNT : SML 0dB CENTER : NONE LFE/B : FRNT TONE : MAX TONE : MIN
4	6CH-INPUT	6ch INPUT 6-ohm 6ch INPUT 8-ohm LIM : , PLDET : , THM :
5	MIC CHECK	MIC CHECK
6	FL/OSD CHECK	VFD CHECK VFD DISP OFF VFD DISP ALL VFD DIMMER CHECK PATTERN
7	TEST TONE	TEST ALL TEST FRNT L TEST CENTER TEST FRNT R TEST SURR R TEST SURR L TEST LFE
8	FACTORY PRESET	PRESET INHI PRESET RSRV
9	AD DATA CHECK	PD : , PV : TH : , PL : PI : , DE : K0 : , K1 :
10	XM STATUS (U, C models)	1k -1dB/44 1k -61dB/44 Mute/44 XM Tone/44 ISO Tone/44 1k -1dB/32 1k -61dB/32 Mute/32 XM Tone/32 ISO Tone/32 Bus Power : OFF
11	DOCK	DOCK : DOCK ignore
12	USB (Not applied to these models.)	USB 1 USB 2
13	DAB (Not applied to these models.)	DAB 1 DAB 2 DAB 3

No.	DIAG menu	Sub-menu
14	IF STATUS (Not applied to these models.)	IF 1
		IF 2
		IF 3
		IF 4
		IF 5
		IF 6
		IF 7
		IF 8
		IF 9
		IF 10
		IF 11
		IF 12
		IF 13
		IF 14
		IF 15
		IF 16
		IF 17
15	PROTECTION	PRD L :
		PRD H :
		PRV L :
		PRV H :
		THM :
		PLD8H :
		PLD8L :
		PLD6H :
		PLD6L :
		PRI :
		PDET :
16	PROTECTION HISTORY	History 1
		History 2
		History 3
		History 4
17	SOFT SWITCH	SW MODE
		MODEL
		DESTINATION
		TUNER DESTINATION
		VIDEO FORMAT
		AAC
		OSD
		YPAO
		RDS
		XM
		DAB
		USB
		DOCK (iPod)
18	ROM VER/SUM	VERSION
		ALL CHECKSUM
		PROGRAM CHECKSUM
		SPI CHECKSUM
		SPD CHECKSUM
		XM VERSION
		DAB VERSION
		FlashROM TEST
		SDRAM TEST
		EEPROM TEST

● Starting DIAG

Press the “STANDBY/ON” key while simultaneously pressing those two keys of the main unit as indicated in the figure below.



Turn on the power while pressing these keys. /
これらのキーを同時に押しながら、電源オンする。

● Starting DIAG in the protection cancel mode

If the protection function works and causes hindrance to trouble diagnosis, cancel the protection function as described below, and it will be possible to enter the DIAG mode. (The protection functions other than the excess current detect function will be disabled.)

Press the “STANDBY/ON” key while simultaneously pressing those two keys indicated in the figure above. At this time, keep pressing those two keys for 3 seconds or longer.

In this mode, the [SLEEP] segment of the FL display of the main unit flashes to indicate that the mode is DIAG mode with the protection functions disabled.

CAUTION!

Using this product with the protection function disabled may cause damage to the main unit. Use special care for this point when using this mode.

● ダイアグの起動

本体の下図に示すキーを同時に押しながら“STANDBY/ON”キーを押すと、ダイアグが起動します。

● プロテクション解除モードでの起動

プロテクションが動作することにより、故障箇所の診断に支障をきたすような場合は、次の方法によりプロテクションを解除した状態でダイアグモードに入ることができます。(過電流検出以外のプロテクション動作を解除する)

上図のキーを同時に押しながら“STANDBY/ON”キーを押します。このとき、上図のキーを3秒以上押し続けてください。

このモードでは本体FLの「SLEEP」セグメントが点滅し、プロテクションを解除した状態でのダイアグモードであることを知らせます。

注意！

プロテクション解除モードでの起動は、危険な状態でもプロテクションが作動しないため、動作させると本機を破壊することがあります。
このモードを使用する場合は十分注意してください。

● Canceling DIAG

1. Before canceling DIAG, execute setting for FACTORY PRESET of DIAG menu No.8 (Memory initialization inhibited or Memory initialized).

* In order to keep the user memory stored, be sure to select PRESET INHIBITED (Memory initialization inhibited).

2. Turn off the power by pressing the “STANDBY/ON” key of the main unit.

● Display provided when DIAG started

On the FL display of the main unit, an opening message (including the protection history) appears for a few seconds followed by the DIAG menu display (1. ANALOG BYPASS).

● ダイアグの解除

1. ダイアグを解除する前に、ダイアグメニューNo. 8 FACTORY PRESET (メモリーの初期化禁止／またはメモリーの初期化)の設定をします。

※ ユーザーメモリーを保持したい場合は、必ず PRESET INHIBITED (メモリー初期化禁止)を選択してください。

2. 本機の“STANDBY/ON”キーを押し、電源オフします。

● ダイアグ起動時の表示

本機のFLディスプレイには、オープニング(プロテクション履歴)が表示され、数秒後にダイアグメニュー表示(1. ANALOG BYPASS)となります。

When there is no history of protection function:

Opening message / オープニング表示

プロテクション履歴が無い場合：

DIAG menu display / ダイアグメニュー表示

When there is no protection history
プロテクション履歴が無い場合

After a few seconds / 数秒後

NO PROTECTION

1. ANALOG BYPASS

When there is a history of protection function:**When there is a history of protection function due to excess current****プロテクション履歴がある場合：**

過電流によるプロテクション履歴がある場合

PRI PRT:xxx

AD value when the protection function is working
プロテクション動作時のA/D値**Cause:** An excessive current flowed through the power amplifier.**Supplementary information:**

As current of the power amplifier is detected, the abnormal channel can be identified by checking the current detect transistor.

Turning on the power without correcting the abnormality will cause the protection function to work immediately and the power supply will instantly be shut off.

原因： パワーアンプに過電流が流れた。**補足：** パワーアンプの電流を検出していますので、電流検出トランジスタをチェックすれば異常チャンネルが特定できます。異常状態のまま電源オンすると、瞬時にプロテクションがかかり、すぐに電源が切れます。**Note)**

- Applying the power to the main unit without correcting the abnormality can be dangerous and cause additional circuit damage. To avoid this, if "PRI" and "PRD" protection function has been activated 3 times continuously, the power will not turn on even when the "STANDBY/ON" key is pressed. In order to turn on the power again, disconnect the power cable of the main unit from the AC outlet once and then reconnect it again.
- The output transistors in each amplifier channel should be checked for damage before applying power of the main unit.
- Amplifier current should be monitored by measuring across the emitter resistors for each channel.

注意！

- 異常状態のまま本機の電源を入れると、危険な状態になり、さらに回路が損傷を受ける原因になります。そのため連続して"PRI"および"PRD"プロテクションが働いた場合、3回目から"STANDBY/ON"キーを押しても電源が入らなくなります。再度電源を入れる場合、一度本機の電源コードをAC電源コンセントから抜いて接続し直してください。
- 本機の電源をいれる前に、各アンプのチャンネル内の出力トランジスタに損傷がないかチェックしてください。
- アンプの電流は、各チャンネルのエミッターの抵抗器間で測定することによりモニターしてください。

When there is a history of protection function due to abnormal DC output**DC出力異常によるプロテクション履歴がある場合**

PRD PRT:xxx

AD value when the protection function is working
プロテクション動作時のA/D値**Cause:** DC output of the power amplifier is abnormal.**Supplementary information:**

The protection function worked due to a DC voltage appearing at the speaker terminal.

A cause could be a defect in the amplifier.

If the power is turned on with the abnormality unsolved, the protection function works in about 3 seconds to turn off the power.

原因： パワーアンプのDC出力が異常。**補足：** アンプの故障でスピーカー端子に直流電圧が掛かるなどが原因で、プロテクションが働いたことを示します。異常状態のままパワーオンすると、約3秒後にプロテクションが掛かり、電源が切れます。

When there is a history of protection function due to abnormal voltage in the power supply section

電源部の電圧異常によるプロテクション履歴がある場合

PRV PRT:xxxx

AD value when the protection function is working
プロテクション動作時のA/D値

Cause: The voltage in the power supply section is abnormal.

Supplementary information:

The protection function worked due to a defect or overload in the power supply.
If the power is turned on with the abnormality unsolved, the protection function works in about 1 second to turn off the power.

原因: 電源部の電圧が異常。

補足: 電源電圧による原因で、プロテクションが働いたことを示します。
異常状態のままパワーオンすると、約1秒後にプロテクションが掛かり、電源が切れます。

When there is a history of protection function due to excessive heat sink temperature

ヒートシンクの異常温度によるプロテクション履歴がある場合

THM PRT:xxxx

AD value when the protection function is working
プロテクション動作時のA/D値

Cause: The temperature of the heat sink is excessive.

Supplementary information:

The protection function worked due to the temperature limit being exceeded.
Causes could be poor ventilation or a defect related to the thermal sensor.

原因: ヒートシンクの温度が異常。

補足: 温度制限を越えた原因で、プロテクションが働いたことを示します。

If the power is turned on with the abnormality unsolved, the protection function works in about 1 second to turn off the power.

For detection of each protection function, refer to DIAG menu described later.

異常状態のままパワーオンすると、約1秒後にプロテクションが掛かり、電源が切れます。

各プロテクションの検出に関しては、後述のダイアグメニューを参照してください。

History of protection function

When the protection function has worked, its history is stored in memory with a backup.

Even if no abnormality is noted while servicing the unit, an abnormality which has occurred previously can be defined as long as the backup data has been stored.

The history of the protection function is cleared when DIAG is cancelled by selecting PRESET RESERVED (Memory initialized) of DIAG menu No. 8 or when the backup data is erased.

プロテクションの履歴

プロテクションが働いた場合、履歴をバックアップして記憶しています。

サービスのときに異常が認められなくても、バックアップが残っていれば、お客様のところで起きた異常を区別できます。

ダイアグメニューNo. 8 PRESET RESERVED(メモリーの初期化)を選んでダイアグを解除した場合または、バックアップが消えた場合にプロテクションの履歴はクリアされます。

● Operation procedure of DIAG menu and Sub-menu

There are 18 menu items, each of having sub-menu items.

DIAG menu selection:

Select the menu using “>” (forward) and “<” (reverse) keys of PROGRAM.

Sub-menu selection:

Select the sub-menu using “SCENE 2” (forward) and “SCENE 1” (Reverse) keys.

● ダイアグメニューとサブメニューの操作

ダイアグにはNo. 1～18のメニューがあり、そのそれぞれにサブメニューがあります。

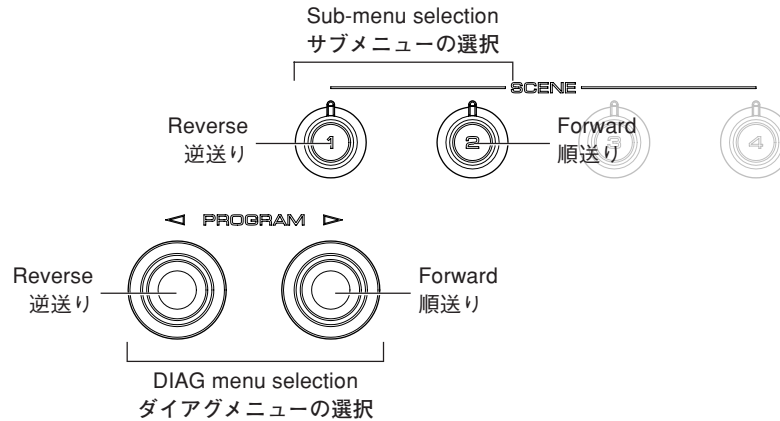
ダイアグメニューの選択:

“PROGRAM >”(順送り)、“PROGRAM <”(逆送り)キーで選択します。

サブメニューの選択:

“SCENE 2”(順送り)、“SCENE 1”(逆送り)キーで選択します。

Keys of main unit / 本機キー



● Functions in DIAG mode

In addition to the DIAG menu items, functions as listed below are available.

- Power on/off
- Master volume
- Muting
- Speakers A/B/OFF
- Input selection
- Audio select
- Tone control

* Functions related to the tuner and the set menu are not available.

● ダイアグ中の機能

ダイアグメニューの他に、以下の機能が動作します。

- パワーオン/オフ
- マスターボリューム
- ミューティング
- スピーカーA/B/OFF
- インプット切り換え
- オーディオセレクト
- トーンコントロール

※ チューナー関連、セットメニュー関連は機能しません。

● Initial settings used to start DIAG

The following initial settings are used when starting DIAG.

When DIAG is canceled, these settings are restored to those before starting DIAG.

- Master volume: -20 dB
- Input: DVD (MULTI CHANNEL INPUT OFF)
- Effect level: 0 dB
- DIAG menu: 1. ANALOG BYPASS

● ダイアグ開始時の初期設定

ダイアグ開始時に以下のような設定になります。ダイアグ解除時にはダイアグ開始前の状態に戻ります。

- マスターボリューム: -20 dB
- インプット: DVD(マルチチャンネルインプットオフ)
- エフェクトレベル: 0 dB
- ダイアグメニュー: 1. ANALOG BYPASS

• Details of DIAG menu

1. BYPASS

Using the sub-menu, it is possible to select ANALOG BYPASS output or DSP BYPASS output.

ANALOG BYPASS

The analog input sound signal is output to FRONT L/R with EFFECT OFF.

1. ANALOG BYPASS

INPUT: DVD ANALOG

SPEAKER OUT: 1 kHz, SUBWOOFER OUTPUT: 50 Hz

Input level	Volume	SPEAKER OUT			SUBWOOFER OUTPUT
		FRONT	CENTER	SURROUND	
Both ch, -20 dBm	+6.0 dB	+11.5 dBm	-∞	-∞	-∞

DSP BYPASS

The digital input sound signal is output to FRONT L/R with EFFECT OFF.

1. DSP BYPASS

INPUT: DVD ANALOG

SPEAKER OUT: 1 kHz, SUBWOOFER OUTPUT: 50 Hz

Input level	Volume	SPEAKER OUT			SUBWOOFER OUTPUT
		FRONT	CENTER	SURROUND	
Both ch, -20 dBm	+6.0 dB	+11.5 dBm	-∞	-∞	-∞

2. AUDIO CHECK

The input sound signal is output.

* When the inputted sound signal is 2 ch L/R, it is distributed as follows when output.

L ch: FRONT L, CENTER, SURROUND L, LFE (L ch +10 dB)

R ch: SURROUND R

2. AUDIO CHECK

2. AUDIO CHECK

入力された音声信号を出力します。

※ 入力された音声信号が2 ch L/Rの場合は、下記のように振り分け出力します。

L ch: FRONT L、CENTER、SURROUND L、LFE (L ch +10 dB)

R ch: SURROUND R

INPUT: DVD ANALOG

SPEAKER OUT: 1 kHz, SUBWOOFER OUTPUT: 50 Hz

Input level	Volume	SPEAKER OUT			SUBWOOFER OUTPUT
		FRONT	CENTER	SURROUND	
Both ch, -20 dBm	+6.0 dB	+11.5 dBm	+11.5 dBm	+11.5 dBm	0 dBm

3. SPEAKER SET

The analog switch settings for each sub-menu are as shown in the table below.

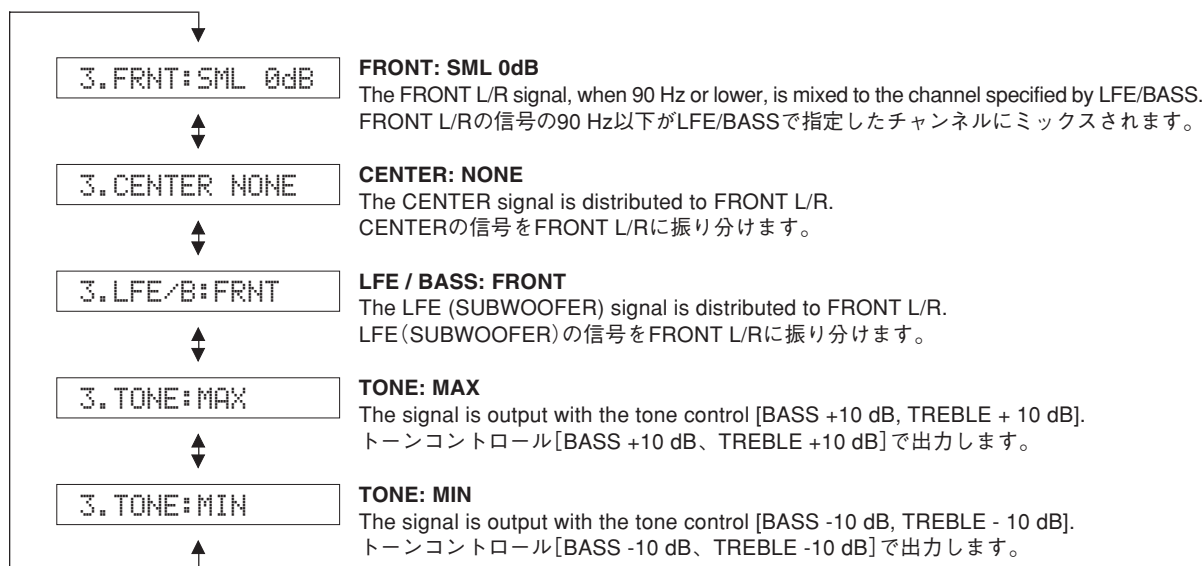
FRONT : SML 0dB	SMALL	LARGE	LARGE	SWFR
CENTER : NONE	LARGE	NONE	LARGE	SWFR
LFE/B : FRNT	LARGE	SMALL	SMALL	FRONT
TONE : MAX	LARGE	LARGE	LARGE	SWFR
TONE : MIN	LARGE	LARGE	LARGE	SWFR

- LARGE:** This mode is used for a speaker with high bass reproduction performance (a large unit). Full bandwidth signals are output.
- SMALL:** This mode is used for a speaker with low bass reproduction performance (a small unit). The signals of 90 Hz or less are mixed into the channel specified by LFE/BASS.
- NONE:** This mode is used for no center speaker. The center content is reduced by 3 dB and distributed to FRONT L/R.
- SWFR:** LFE of 5.1 ch signal or LFE/BASS lower than 90 Hz is output through SUBWOOFER OUT.
- FRONT:** LFE of 5.1 ch signal or LFE/BASS lower than 90 Hz is distributed to FRONT L/R.

3. SPEAKER SET

各サブメニューにおけるアナログスイッチの設定は以下の通りです。

- LARGE :** 低音再生能力の高い(ユニットの大きい)スピーカーを使用するモードです。全帯域が出力されます。
- SMALL :** 低音再生能力の低い(ユニットの小さい)スピーカーを使用するモードです。90 Hz以下がLFE/BASSで指定したチャンネルにミックスされます。
- NONE :** センタースピーカーを使用しないモードです。センター成分は-3dBされて、FRONT L/Rに振り分けられます。
- SWFR :** 5.1 ch信号のLFEまたは90 Hz以下のLFE/BASSがSUBWOOFER OUTに出力されます。
- FRONT :** 5.1 ch信号のLFEまたは90 Hz以下のLFE/BASSをFRONT L/Rに振り分けます。



INPUT: DVD ANALOG

SPEAKER OUT: 1 kHz, SUBWOOFER OUTPUT: 50 Hz

Sub-menu	Input level	Volume	SPEAKER OUT			SUBWOOFER OUTPUT
			FRONT	CENTER	SURROUND	
FRONT : SML 0dB	Both ch, -20 dBm	+6.0 dB	+11.5 dBm	-∞	-∞	-3.5 dBm
CENTER : NONE	Both ch, -20 dBm	+6.0 dB	+11.5 dBm	-∞	-∞	-∞
LFE/B : FRNT	Both ch, -20 dBm	+6.0 dB	+11.5 dBm	-∞	-∞	-∞
TONE : MAX	Both ch, -20 dBm	+6.0 dB	+14.5 dBm	-∞	-∞	-∞
TONE : MIN	Both ch, -20 dBm	+6.0 dB	+8.5 dBm	-∞	-∞	-∞

RX-V461/HTR-6040/DSP-AX461

4. 6CH INPUT

The input source [MULTI CHANNEL INPUT] is selected.

It is possible to select the 6-ohm/8-ohm by using the sub-menu.

6 ch INPUT 6-ohm

4.6ch INPUT 6Ω

INPUT: MULTI CH INPUT

SPEAKER OUT: 1 kHz, SUBWOOFER OUTPUT: 50 Hz

Sub-menu	Input level	Volume	SPEAKER OUT			SUBWOOFER OUTPUT
			FRONT	CENTER	SURROUND	
6 ch INPUT 6-ohm	Both ch, -20 dBm	+6.0 dB	+11.5 dBm	+11.5 dBm	+11.5 dBm	-3.5 dBm

6 ch INPUT 8-ohm

4.6ch INPUT 8Ω

INPUT: MULTI CH INPUT

SPEAKER OUT: 1 kHz, SUBWOOFER OUTPUT: 50 Hz

Sub-menu	Input level	Volume	SPEAKER OUT			SUBWOOFER OUTPUT
			FRONT	CENTER	SURROUND	
6 ch INPUT 8-ohm	Both ch, -20 dBm	+6.0 dB	+11.5 dBm	+11.5 dBm	+11.5 dBm	-3.5 dBm

LIM/PLDET/THM

LIM: Setting value of LIM (Limiter control)

* As this is a development menu, do not change the setting value.

PLDET: Power limiter detection

The A/D conversion value during operation is displayed.

THM: Thermo protection detection

The A/D conversion value during operation is displayed.
(Reference voltage: 3.3 V=255)

LIM/PLDET/THM

LIM: LIM(リミッター制御)の設定値

※ 開発用メニューのため、設定値の変更は行わないでください。

PLDET: パワーリミッターの検出

動作時のA/D変換値が表示されます。

THM: 温度プロテクションの検出

動作時のA/D変換値が表示されます。
(基準電圧: 3.3 V=255)

4.255:255: 83

THM (Thermo protection detection / 温度プロテクションの検出)
PLDET (Power limiter detection / パワーリミッターの検出)
LIM (Limiter control / リミッター制御)

5. MIC CHECK

The signals input through the microphone are output of FRONT L/R via A/D and D/A.

5.MIC CHECK

5. MIC CHECK

マイク入力された信号をA/D→D/A経由でFRONT L/Rに出力します。

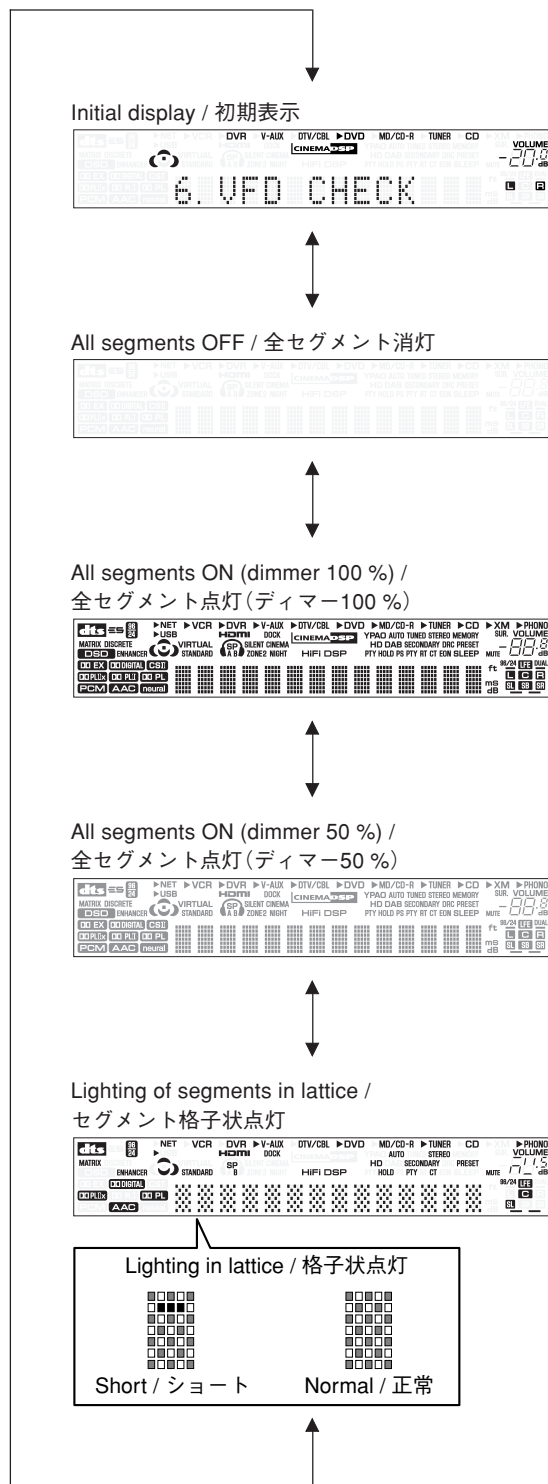
6. FL/OSD CHECK

Use this program to check the FL display section and video control section. When checking the video control section, prepare a monitor, S video cable and video pin cable and connect them.

Using the sub-menu operation, selection items of the FL display section and video display section vary as shown below.

For audio signal processing, use STRAIGHT.

Checking FL display section / FL表示部のチェック



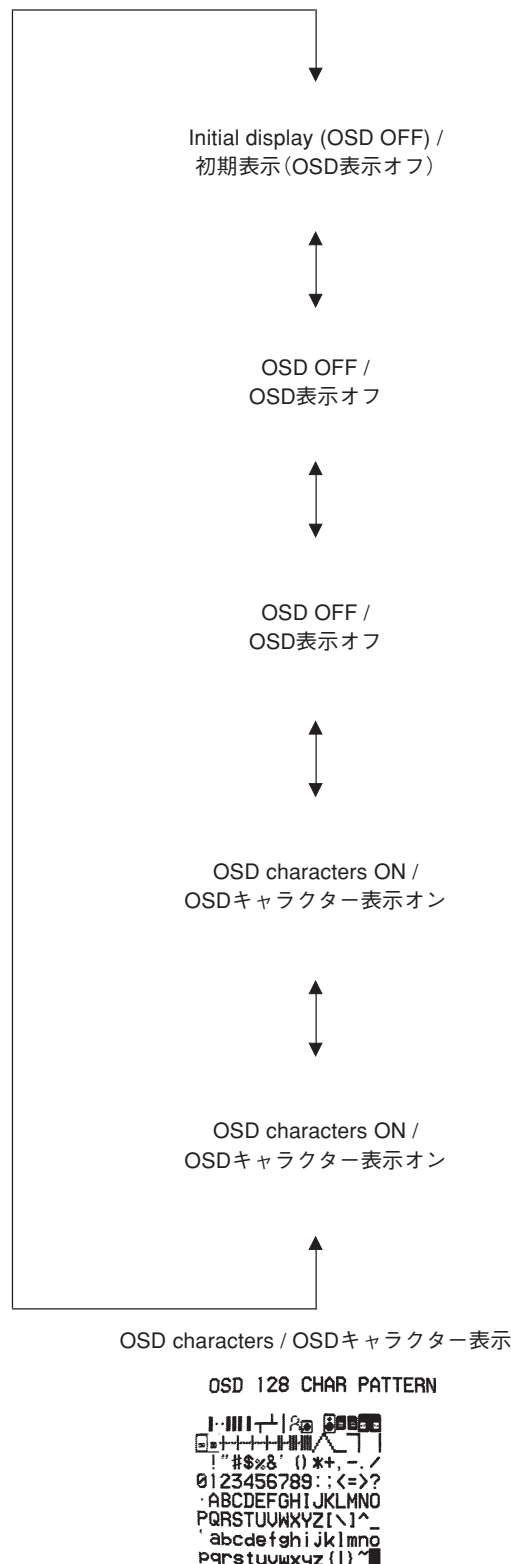
6. FL/OSD CHECK

FL表示部および映像表示部のチェックプログラムです。映像制御部をチェックする場合には、モニター、Sビデオケーブル、ビデオ用ピンケーブルを準備し接続します。

サブメニュー操作により、FL表示部と映像表示部の選択が以下のように連動して変わります。

オーディオ信号処理はSTRAIGHTです。

Check of the Video control section. (Monitor out) / 映像表示部のチェック (モニター出力)

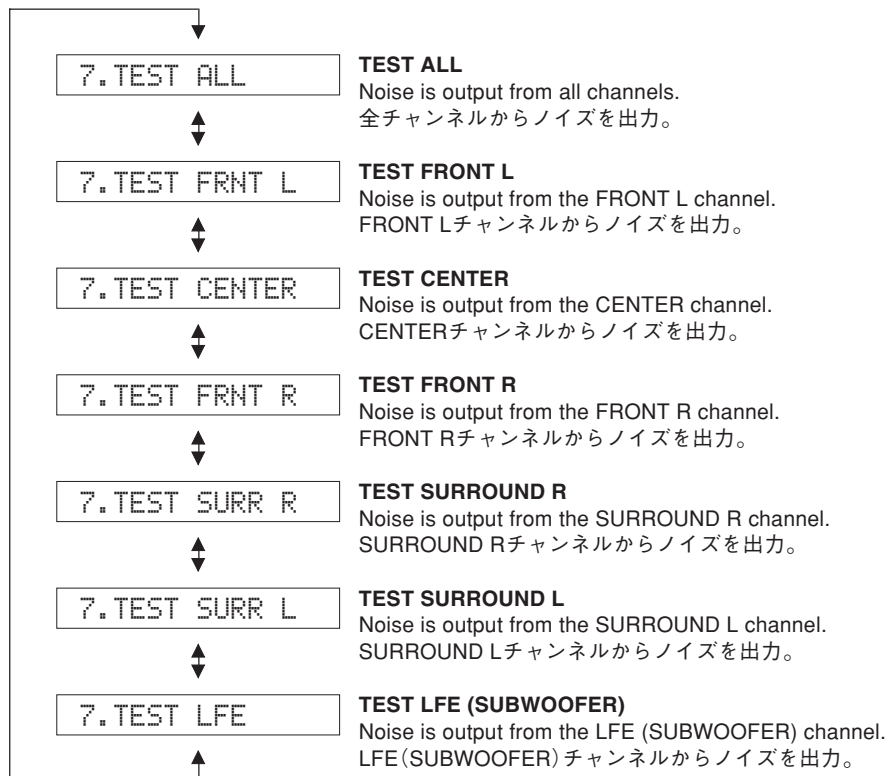


7. TEST TONE

The noise generator with a built-in microprocessor outputs the noise through the channels specified by the submenu.

The noise frequency for LFE (SUBWOOFER) is 35 to 80 Hz.

Other than that, the noise frequency is 500 to 2 kHz.



7. TEST TONE

マイコンに内蔵されているノイズ発生回路によって、サブメニューで指定したチャンネルへノイズを出力します。

LFE (SUBWOOFER) 用のノイズ周波数は35～80 Hz、それ以外はノイズ周波数500～2 kHzとなります。

8. FACTORY PRESET

This menu is used to reserve and inhibit initialization of the back-up RAM.

The signals are processed using EFFECT OFF (The L/R signal is output using ANALOG BYPASS).



8. FACTORY PRESET

バックアップ用RAM(音場プログラムのパラメーターやセットメニュー内容等)の初期化を予約／禁止します。

信号処理はEFFECT OFF (ANALOG BYPASSでL/Rを出力)です。

CAUTION: Before setting to the PRESET RESERVED, write down the existing preset memory content of the tuner in a table as shown below. (This is because setting to the PRESET RESERVED will cause the user memory content of the tuner to be erased.)

注意： PRESET RESERVEDを選んで初期化をする前に、チューナーのユーザーメモリー内容を下表に書き写してください。(初期化をすると、チューナーのユーザーメモリーの内容は消えてしまいます。)

Preset Group	P1	P2	P3	P4	P5	P6	P7	P8
A								
B								
C								
D								
E								

9. A/D DATA CHECK

This menu is used to display the A/D conversion value of the microprocessor which detects panel keys of the main unit and protection functions in using the sub-menu.

When K0/K1 menu is selected, keys become non-operable due to detection of the values of all keys. However, it is possible to advance to the next sub-menu by turning the VOLUME of the main unit. When using this function, note that turning the VOLUME more than 1 click would cause the volume value to change. During signal processing, the condition before execution is maintained.

* The figures in the diagram are given as reference only.

PD/PV

PD: PRD (Power amplifier DC protection detection)
The output of power amplifier DC (DC voltage) is detected.
Normal value: 35 to 81 (Reference voltage: 3.3 V=255)

PV: PRV (Voltage protection detection)
Voltage detects: ACL, AC2, 10V, S9, +12, -12, +5V and VP
Normal value: 84 to 153 (Reference voltage: 3.3 V=255)

* If PRD and PRV are out of the normal value range, the protection function works to turn off the power.

9. A/D DATA CHECK

本機パネルキー、プロテクションなどを検出しているマイコンのA/D変換値を、サブメニューで表示します。

K0/K1のメニューにすると、全キーの値を検出するためキー操作はできなくなりますが、本機のVOLUMEを回すことにより、次のサブメニューに進めることができます。このとき1クリック以上回すと、ボリューム値が変化するので注意してください。

信号処理は実行前の状態を維持します。

※ 図中の数値は参考例です。

PD/PV

PD: PRD (パワーアンプDCプロテクションの検出)
パワーアンプDC(直流電圧)出力の検出。
正常値: 35~81(基準電圧: 3.3 V=255)

PV: PRV (電圧プロテクションの検出)
検出電圧: ACL、AC2、10V、S9、+12、-12、+5V、VP
正常値: 84~153(基準電圧: 3.3 V=255)

※ PRDおよびPRVは正常値を外れるとプロテクションが働き、電源オフされます。

PD: 57 PV:116

RX-V461/HTR-6040/DSP-AX461

TH/PL

TH: THM (Thermo protection detection)
The temperature of the heat sink is detected.
Normal value: 0 to 124 (Reference voltage: 3.3 V=255)

* If THM is out of the normal value range, the protection function works to turn off the power.

PL: PLDET (Power limiter detection)
The output voltage of power amplifier is detected.

TH/PL

TH: THM(温度プロテクションの検出)
ヒートシンク温度の検出。
正常値: 0~124(基準電圧: 3.3 V=255)

※ THMは正常値を外れるとプロテクションが働き、電源オフされます。

PL: PLDET(パワーリミッターの検出)
パワーアンプ出力電圧の検出。

TH: 83 PL:255

U, C models (Reference voltage: 3.3 V=255)

	During normal operation	Value for starting limiter operation	Value for canceling limiter operation
PLDET	255	77	100
LIM H: 255 / L: 102	H	L	H

(LIM: Limiter control)

R, K, A, L models (Reference voltage: 3.3V=255)

	During normal operation	Value for starting limiter operation	Value for canceling limiter operation
PLDET	255	100	131
LIM H: 255 / L: 90	H	L	H

(LIM: Limiter control)

J model(基準電圧: 3.3 V=255)

	通常値	リミッター動作開始値	リミッター動作解除値
PLDET	255	100	131
LIM H: 255 / L: 90	H	L	H

(LIM: リミッター制御)

PI/DE

PI: PRI (Current protection detection)
The current of the power amplifier is detected.
Normal value: 0 to 100 (Reference voltage: 3.3 V=255)

DE: PDET (Sub-trans power detection)
Normal value: 209 to 255 (Reference voltage: 3.3 V=255)

* If PRI and PDET are out of the normal value range, the protection function works to turn off the power.

PI/DE

PI: PRI(電流プロテクションの検出)
パワーアンプ電流の検出。
正常値: 0~100(基準電圧: 3.3 V=255)

DE: PDET(サブトランス電源電圧の検出)
正常値: 209~255(基準電圧: 3.3 V=255)

※ PRIおよびPDETは正常値を外れるとプロテクションが働き、電源オフされます。

PI: 33 DE:255

K0/K1

K0/K1: KEY0/KEY1 (Panel key of main unit)
A/D value of the key fails to function properly when the standard value is deviated by ± 4 .
In this case, check the constant of partial pressure resistor, solder condition, etc.
Refer to table.
(Reference voltage: 3.3 V=255)

K0/K1

K0/K1: KEY0/KEY1(本機パネルキー)
キーのA/D値は基準値から ± 4 を外れると、正常な動きをしません。
下表をご覧になり、各キーの分圧抵抗の定数、ハンダ不良等の確認をしてください。
(基準電圧: 3.3 V=255)

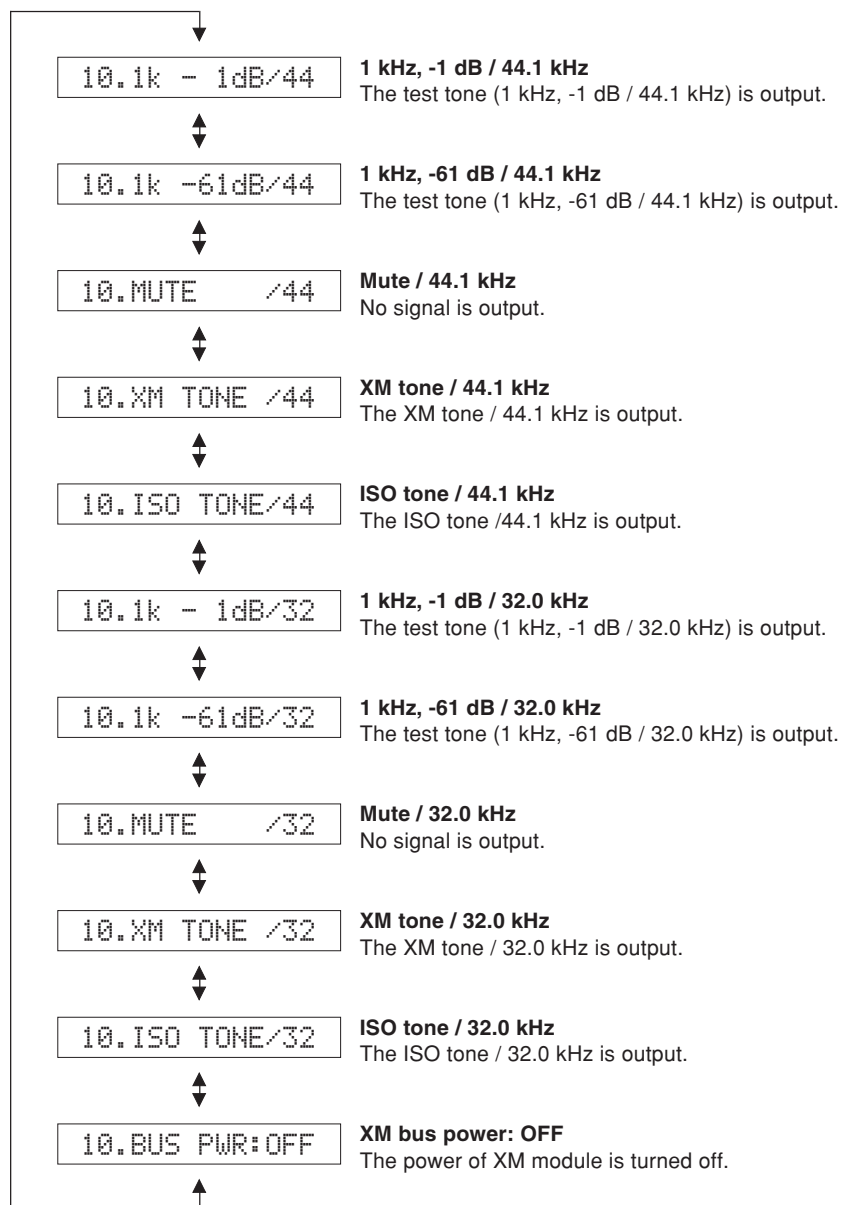
K0:251 K1:252

Display / 表示	KEY0	KEY1
23 ± 4	SCENE 1	SCENE 3
42 ± 4	SCENE 2	SCENE 4
66 ± 4	PROGRAM <	DIRECT
92 ± 4	PROGRAM >	AUDIO SELECT
120 ± 4	STRAIGHT	INPUT <
147 ± 4	TONE CONTROL	INPUT >
165 ± 4	SEARCH MODE	PRESET/TUNING <
182 ± 4	FM/AM	PRESET/TUNING >
198 ± 4	A/B/C/D/E	MEMORY
217 ± 4	SPEAKERS	TUNING
255	(KEY OFF)	(KEY OFF)

10. XM STATUS (U, C models)

The output check of XM radio antenna module is executed.

10. XM STATUS (U, C models)



11. DOCK

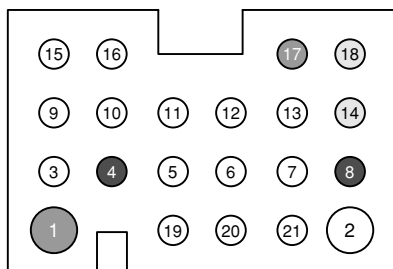
This menu is used to test the DOCK connector without the iPod itself.

After turning off the power, short between pins No. 14 (TX) and No. 18 (RX), between pins No. 1 (PWR) and No. 17 (ACCPW) and between pins No. 4 (iPDET) and No. 8 (DGND).

Start the DIAG function and select the menu.

The check result is displayed according to the following display specifications.

Note) Be sure to return the shorted locations to their original state.



DOCK

Check item / チェック項目	Result / 結果		Display / 表示
UART loop back test / UARTループバックテスト	OK		Y
	NG		N
iPAP (iPod accessory power) detection / iPAP (iPod accessory power) 検出	IC402 pin No. 1	High	Y
		Low	N
iPDET (iPod installation to DOCK) detection / iPDET (iPod installation to DOCK) 検出	IC402 pin No. 12	Low	Y
		High	N

11. DOCK

iPod本体無しで、DOCKコネクタの検査を行うメニューです。

電源オフの状態にしてから、DOCKコネクタの14ピン (TX) と18ピン (RX)、1ピン (PWR) と17ピン (ACCPW)、4ピン (iPDET) と8ピン (DGND) をショートさせます。

ダイグを起動してメニューを選択します。

下記表示仕様に従って、チェック結果が表示されます。

注) ショート箇所は、必ず元に戻してください。

11.DOCK=NG NNN

All Y / すべてY = "OK"
Others / その他 = "NG"

DOCK ignore

When DOCK and iPod are connected, the input source [DOCK (iPod)] is made invalid and [V-AUX] is selected.

DOCK 無効

DOCKおよびiPodを接続している時、入力ソース [DOCK (iPod)] を無効にして [V-AUX] に切り替えます。

11.DOCK IGNORE

12. USB CHECK

Not applied to these models.

12.USB 1



12. USB CHECK

このモデルには適用されません。

12.USB 2

13. DAB CHECK

Not applied to these models.

13.DAB 1



13.DAB 3

13. DAB CHECK

このモデルには適用されません。

14. IF STATUS (Input function status)

Not applied to these models.

14. IF STATUS

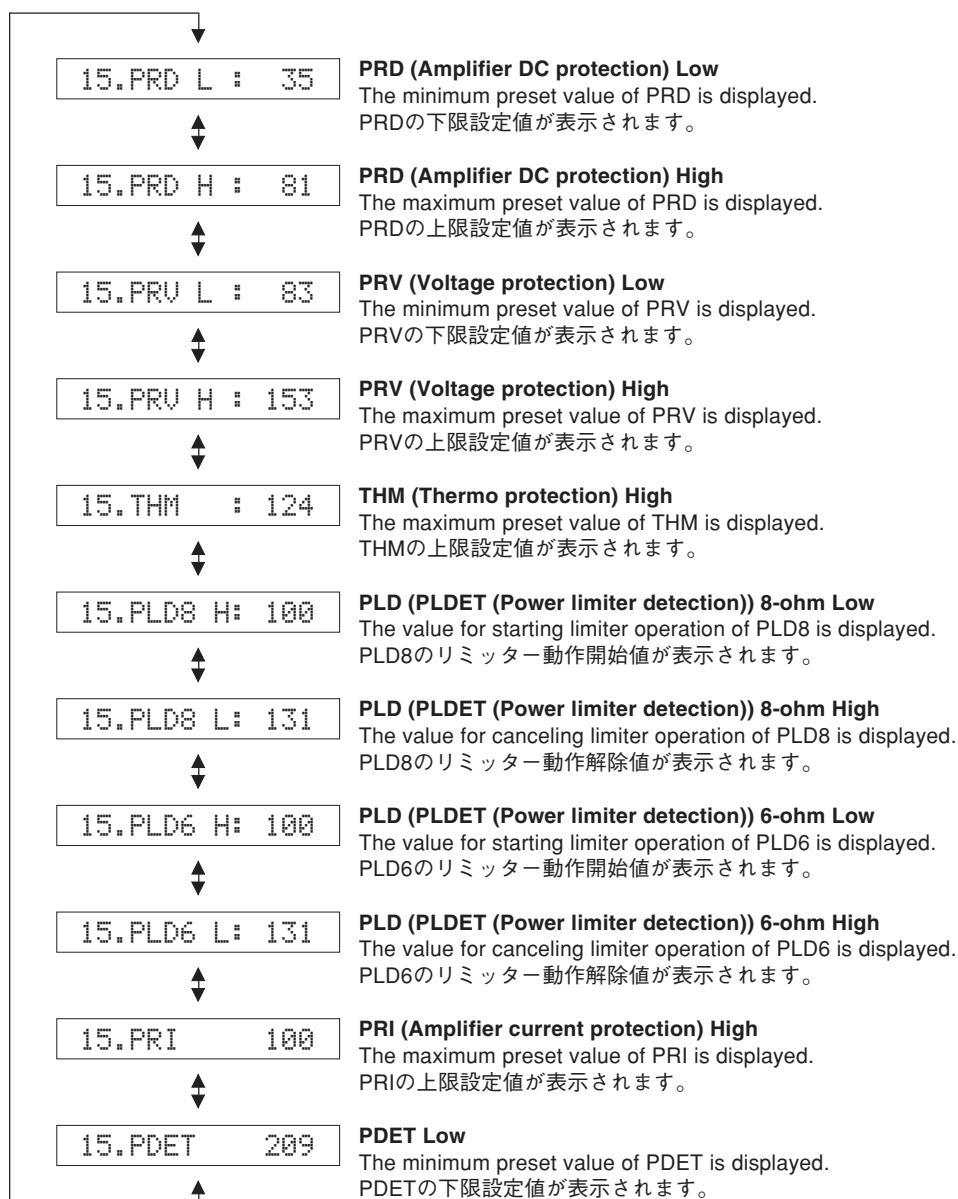
このモデルには適用されません。

**15. PROTECTION SETTING**

The A/D setting value of each protection is displayed.
(Reference voltage: 3.3 V=255)

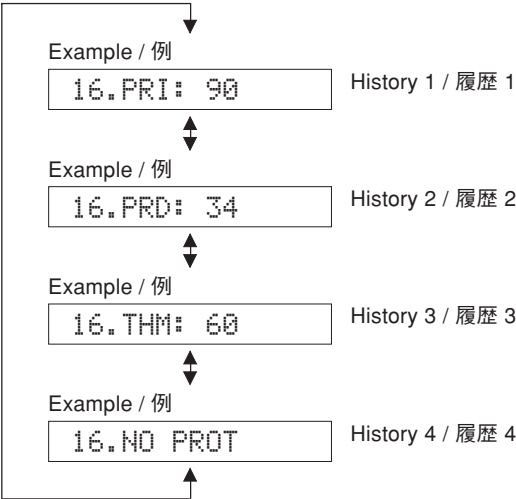
15. PROTECTION SETTING

各プロテクションのA/D設定値が表示されます。(基準
電圧：3.3 V=255)



16. PROTECTION HISTORY

Four protection histories are displayed.



16. PROTECTION HISTORY

過去のプロテクション履歴が4つまで表示されます。

17. SOFT SWITCH

**Note) As this is a development menu, do not change the function setting.
Changing the function setting may hinder the proper operation.**

This menu is used to switch the function settings on P.C.B. through the software to activate the main unit. The protection function follows the P.C.B. settings.

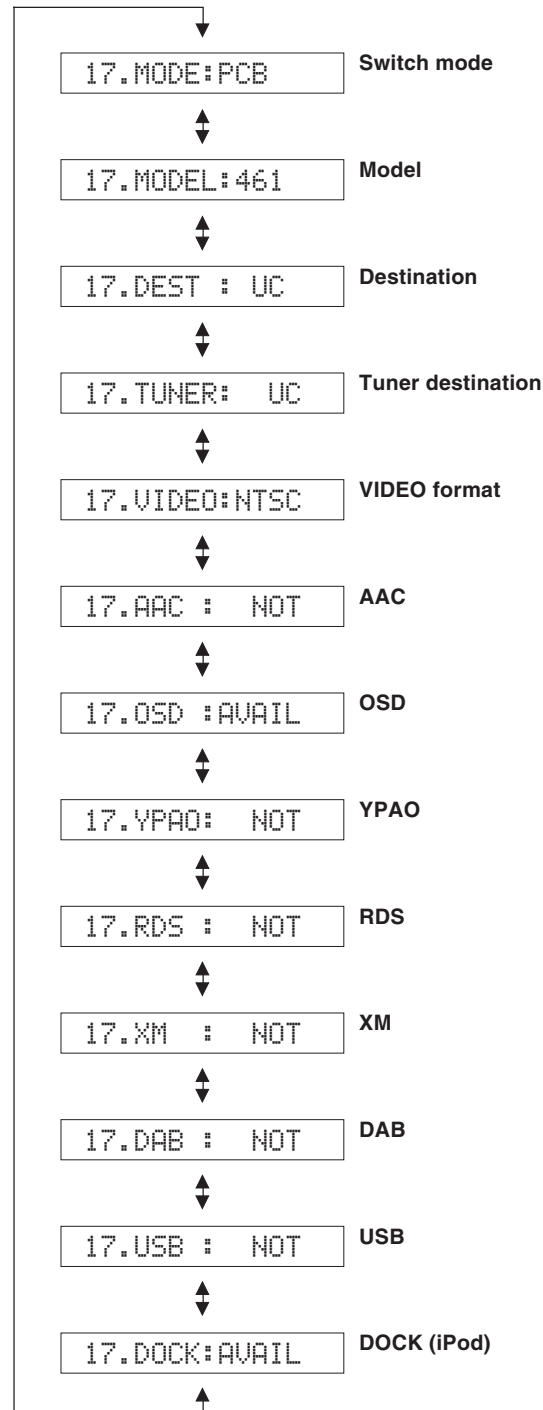
* As this is a development menu, it is not possible to describe the details.

17. SOFT SWITCH

注) 開発用メニューのため、機能設定の変更は行わないでください。
機能設定を変更した場合、正常に動作しないことがあります。

P.C.B.上の機能設定をソフト的に切り替えて、本機を動作させる機能です。
プロテクション機能は、P.C.B.の設定に従います。

※ 開発用メニューのため、詳細に関しては記載できません。



18. ROM VER/SUM

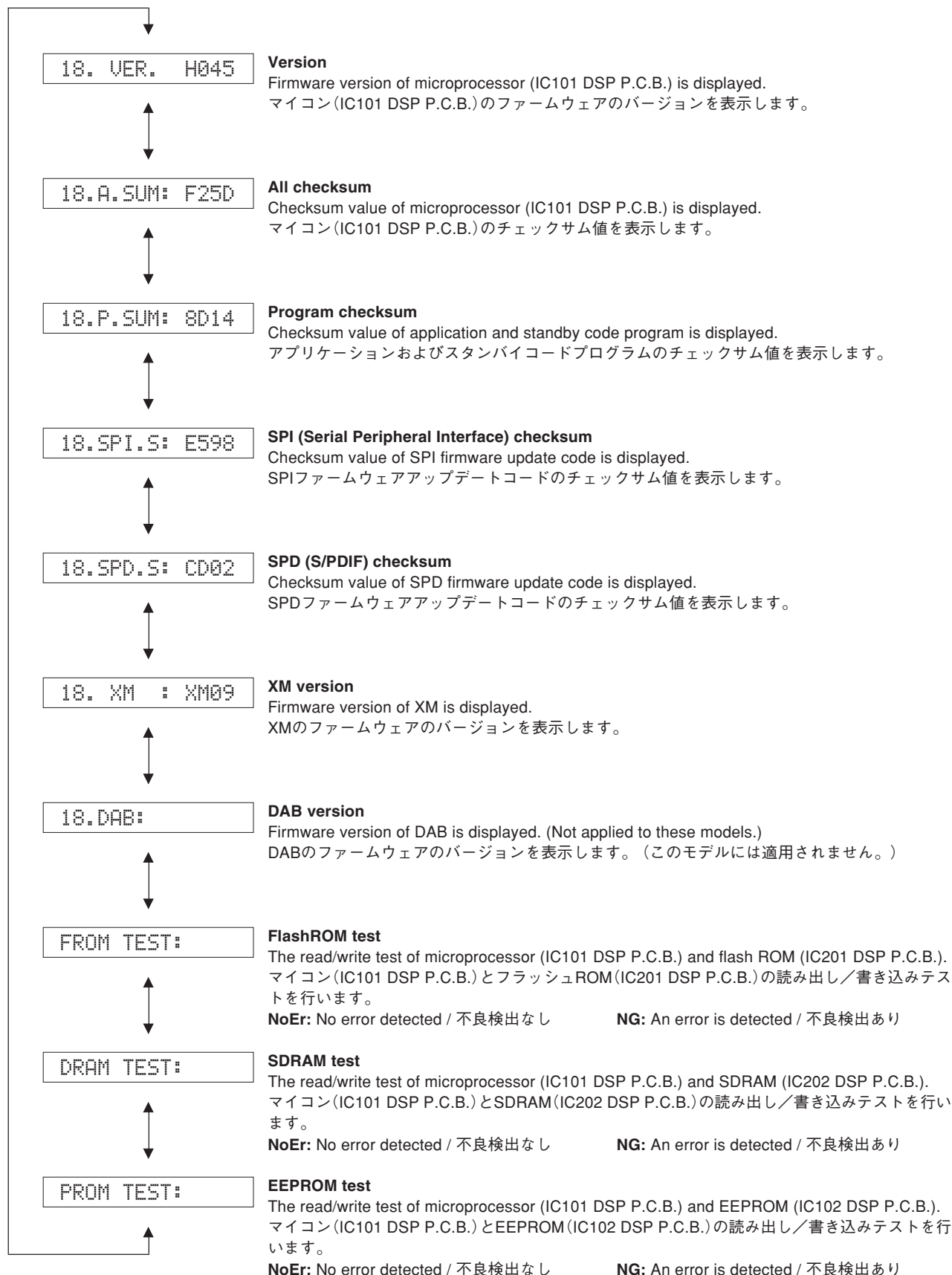
The version and checksum are displayed. The signal is processed using EFFECT OFF.

The checksum is obtained by adding the data at every 8-bit for each program area and expressing the result as a 4-figure hexadecimal data.

18. ROM VER/SUM

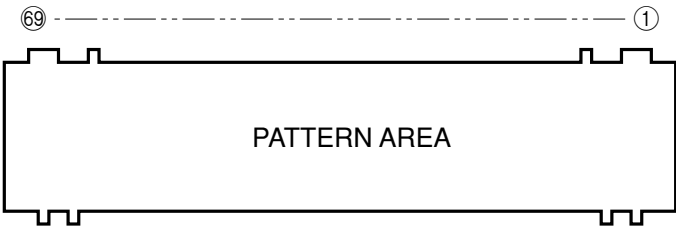
プログラムのバージョン、チェックサムを表示します。信号はエフェクトオフです。

チェックサムは、プログラムエリア別にデータを8ビットごとに加算していき、4桁の16進データで現したものです。



■ DISPLAY DATA

● V2001 : 17-BT-29GNK (OPERATION P.C.B.)



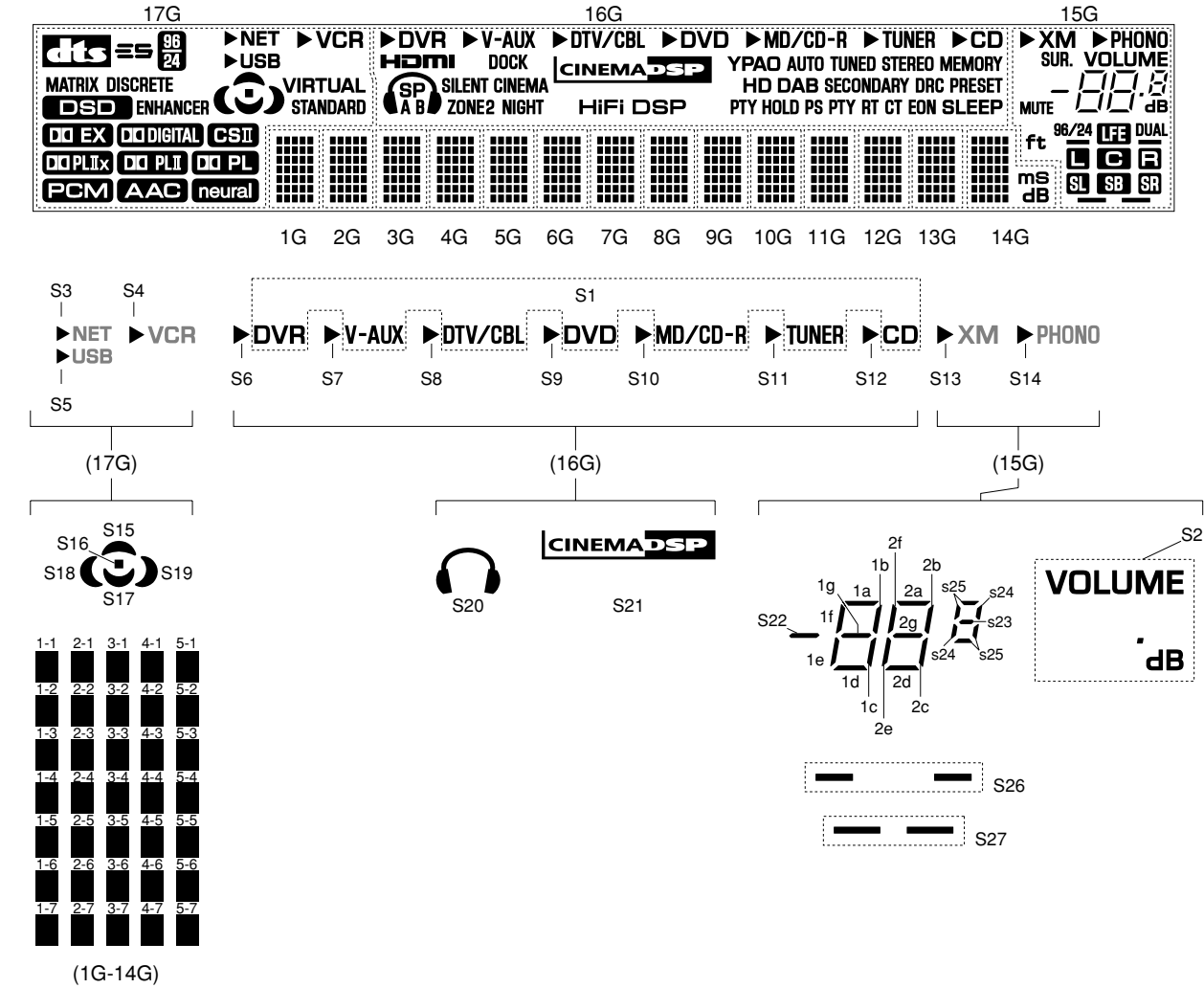
● PIN CONNECTION

Pin No.	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35
Connection	F2	NX	NP	NP	P1	P2	P3	P4	P5	P6	P7	P8	P9	P10	P11	P12	P13	P14	P15	P16	P17	P18	P19	P20	P21	P22	P23	P24	P25	P26	P27	P28	P29	P30	P31

Pin No.	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
Connection	P32	P33	P34	P35	P36	P37	NX	NX	NX	NX	NX	NX	NX	17G	16G	15G	14G	13G	12G	11G	10G	9G	8G	7G	6G	5G	4G	3G	2G	1G	NP	NP	NX	F1

Note : 1) F1, F2 Filament pin 2) NP No pin 3) NX No extend pin 4) 1G~17G Grid pin

● GRID ASSIGNMENT

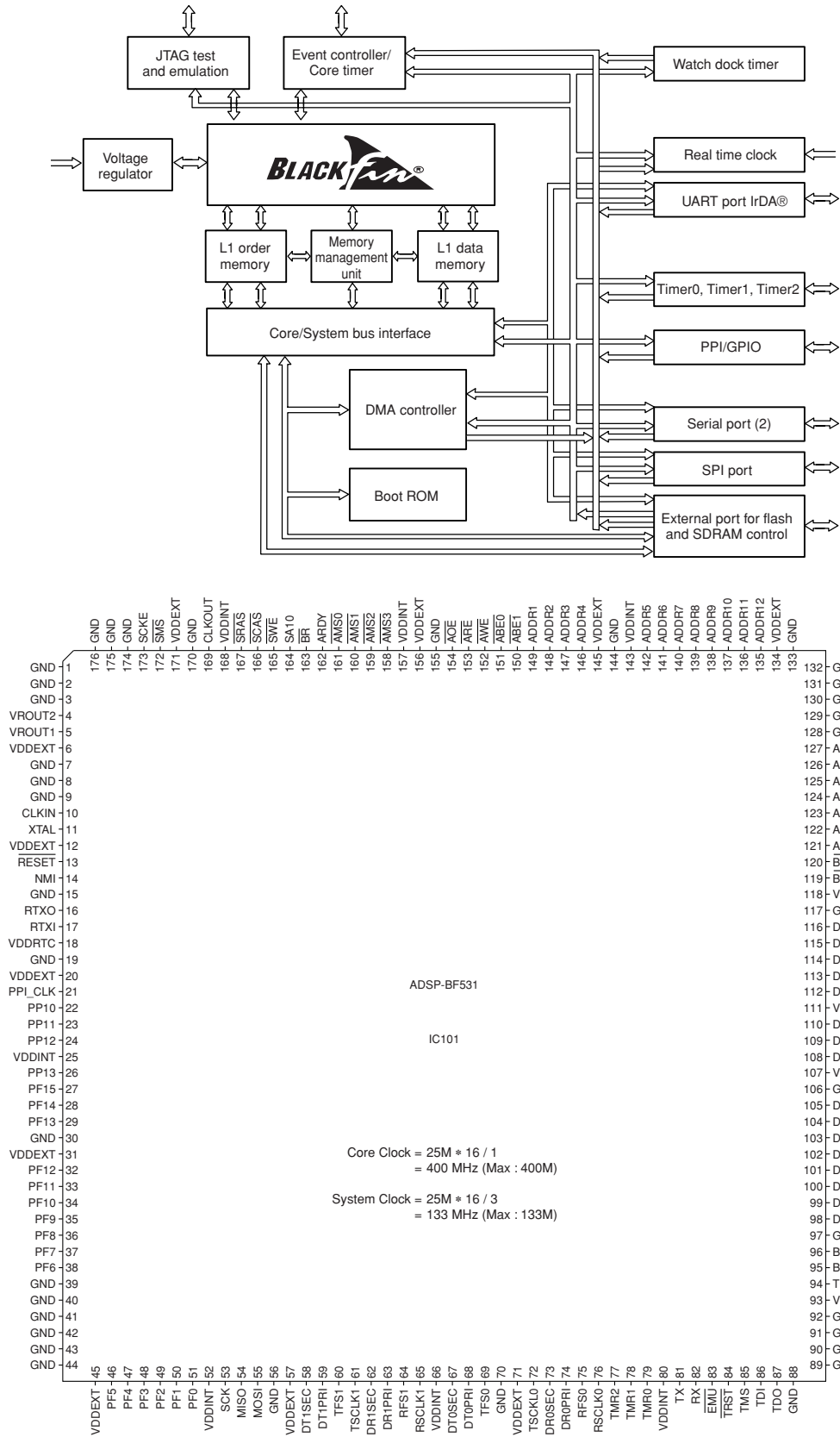


● ANODE CONNECTION

	17G	16G	15G	14G	13G-1G
1P		S1	S2	1-1	1-1
2P		S6	S26	2-1	2-1
3P	MATRIX	S7	S27	3-1	3-1
4P	DISCRETE	S8	S22	4-1	4-1
5P		S9	1a	5-1	5-1
6P		S10	1b	1-2	1-2
7P	ENHANCER	S11	1c	2-2	2-2
8P		S12	1d	3-2	3-2
9P			1e	4-2	4-2
10P		S20	1f	5-2	5-2
11P		SP	1g	1-3	1-3
12P		A	2a	2-3	2-3
13P		B	2b	3-3	3-3
14P		SILENT CINEMA	2c	4-3	4-3
15P		ZONE2	2d	5-3	5-3
16P		NIGHT	2e	1-4	1-4
17P	NET	DOCK	2f	2-4	2-4
18P	USB	S21	2g	3-4	3-4
19P	VCR	HiFi DSP	S23	4-4	4-4
20P	S3	YPAO	S24	5-4	5-4
21P	S5	AUTO	S25	1-5	1-5
22P	S4	TUNED		2-5	2-5
23P	S15	STEREO		3-5	3-5
24P	S16	MEMORY	S13	4-5	4-5
25P	S17	HD	S14	5-5	5-5
26P	S18			1-6	1-6
27P	S19		MUTE	2-6	2-6
28P			DUAL	3-6	3-6
29P	STANDARD		96/24	4-6	4-6
30P	—	PTY (HOLD)	ft	5-6	5-6
31P	—	HOLD		1-7	1-7
32P	—	PS		2-7	2-7
33P	—	PTY		3-7	3-7
34P	—	RT		4-7	4-7
35P	—	CT		5-7	5-7
36P	—	EON		mS	—
37P	—	SLEEP		dB	—

IC DATA

IC101: ADSP-BF531 CPU (DSP P.C.B.)
Microprocessor



Pin No.	Port Name	Function Name	I/O	Detail of Function
1	DGND	GND	–	Ground of external
2	DGND	GND	–	Ground of external
3	DGND	GND	–	Ground of external
4	/VINTSW	VROUT2	O	Voltage regulator drive for Q101
5	/VINTSW	VROUT1	O	Voltage regulator drive for Q101
6	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
7	DGND	GND	–	Ground of external
8	DGND	GND	–	Ground of external
9	DGND	GND	–	Ground of external
10	CLKIN	CLKIN	I	Clock/oscillation input
11	XTAL	XTAL	O	Oscillation output
12	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
13	/DRESET	/RESET	I	Delayed reset
14	NMI/DGND	NMI	I	(Pull-down)
15	DGND	GND	–	Ground of external
16	–	RTXO	O	
17	RTXI/DGND	RTXI	I	(Pull-down)
18	–	VDDRTC	–	
19	DGND	GND	–	Ground of external
20	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
21	–	PPI_CLK	I	
22	–	PP10	I/O	
23	–	PP11	I/O	
24	–	PP12	I/O	
25	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
26	–	PP13	I/O	
27	DTXM	PF15	O	UART transmission for XM (U, C models)
28	DRXM	PF14	I	UART reception for XM (U, C models)
29	–	PF13	I	
30	DGND	GND	–	Ground of external
31	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
32	/ICXM	PF12	O	Reset for XM (U, C models)
33	/XMPWR	PF11	O	XM power switch (U, C models)
34	INTAK	PF10	I	CODEC IC (IC301) interrupt
35	FSYNC/TFS0	PF9	I	Frame sync detect
36	R2A_DATA	PF8	O	DATA for R2A volume/selecter IC (IC162)
37	R2A_CLK	PF7	O	CLK for R2A volume/selecter IC (IC161)
38	VRB	PF6	I	Volume rotary B
39	DGND	GND	–	Ground of external
40	DGND	GND	–	Ground of external
41	DGND	GND	–	Ground of external
42	DGND	GND	–	Ground of external
43	DGND	GND	–	Ground of external
44	DGND	GND	–	Ground of external
45	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
46	VRA	PF5	I	Volume rotary A
47	REM	PF4	I	IR remote control pulse input
48	PSW	PF3	I	Power switch (STANDBY/ON)
49	/SPISEL2	PF2	O	CS for EEPROM (IC102)
50	/SPISEL1	PF1	O	CS for 4 ch ADC (IC401)
51	/EXPE	PF0	O	Extended port enable
52	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
53	SPISCK	SCK	I/O	SPI clock
54	SPIMI	MISO	I/O	Master input/slave output
55	SPIMO	MOSI	I/O	Master output/slave input
56	DGND	GND	–	Ground of external
57	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
58	DT1SEC	DT1SEC	O	Serial port 1, secondary transmission data
59	DT1PRI	DT1PRI	O	Serial port 1, primary transmission data
60	TFS1	TFS1	I/O	Serial port 1, frame asynchronous transmission

Pin No.	Port Name	Function Name	I/O	Detail of Function
61	TSCLK1	TSCLK1	I/O	Serial port 1, serial transmission clock
62	DR1SEC	DR1SEC	I	Serial port 1, secondary reception data
63	DR1PRI	DR1PRI	I	Serial port 1, primary reception data
64	RFS1	RFS1	I/O	Serial port 1, frame synchronization reception
65	RSCLK1	RSCLK1	I/O	Serial port 1, serial reception clock
66	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
67	DT0SEC	DT0SEC	O	Serial port 0, secondary transmission data
68	DT0PRI	DT0PRI	O	Serial port 0, primary transmission data
69	TFS0	TFS0	I/O	Serial port 0, frame asynchronous transmission
70	DGND	GND	–	Ground of external
71	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
72	TSCLK0	TSCLK0	I/O	Serial port 0, serial transmission clock
73	DR0SEC	DR0SEC	I	Serial port 0, secondary reception data
74	DR0PRI	DR0PRI	I	Serial port 0, primary reception data
75	RFS0	RFS0	I/O	Serial port 0, frame synchronization reception
76	RSCLK0	RSCLK0	I/O	Serial port 0, serial reception clock
77	–	TMR2	I/O	
78	–	TMR1	I/O	
79	LIMITER	TMR0	O	Limiter control output
80	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
81	TxDi	TX	O	UART transmission for DOCK (iPod)
82	RxDi	RX	I	UART reception for DOCK (iPod)
83	–	/EMU	O	
84	–	/TRST	I	
85	–	TMS	I	
86	–	TDI	I	
87	–	TDO	O	
88	DGND	GND	–	Ground of external
89	DGND	GND	–	Ground of external
90	DGND	GND	–	Ground of external
91	DGND	GND	–	Ground of external
92	DGND	GND	–	Ground of external
93	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
94	–	TCK	I	
95	BMODE1	BMODE1	I	(Pull-down)
96	BMODE0	BMODE0	I	(Pull-up)
97	DGND	GND	–	Ground of external
98	D16	DATA15	I/O	SDRAM data bus 16
99	D15	DATA14	I/O	SDRAM data bus 15
100	D14	DATA13	I/O	SDRAM data bus 14
101	D13	DATA12	I/O	SDRAM data bus 13
102	D12	DATA11	I/O	SDRAM data bus 12
103	D11	DATA10	I/O	SDRAM data bus 11
104	D09	DATA9	I/O	SDRAM data bus 09
105	D08	DATA8	I/O	SDRAM data bus 08
106	DGND	GND	–	Ground of external
107	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
108	D07	DATA7	I/O	SDRAM data bus 07
109	D06	DATA6	I/O	SDRAM data bus 06
110	D05	DATA5	I/O	SDRAM data bus 05
111	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
112	D04	DATA4	I/O	SDRAM data bus 04
113	D03	DATA3	I/O	SDRAM data bus 03
114	D02	DATA2	I/O	SDRAM data bus 02
115	D01	DATA1	I/O	SDRAM data bus 01
116	D00	DATA0	I/O	SDRAM data bus 00
117	DGND	GND	–	Ground of external
118	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
119	–	/BG	O	
120	–	/BGH	O	

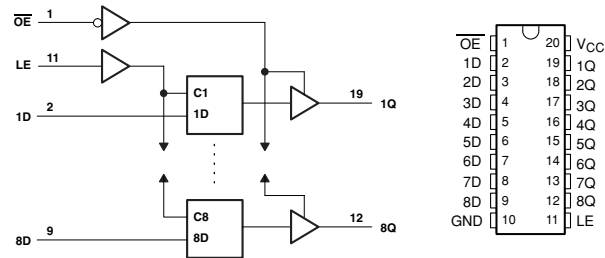
RX-V461/HTR-6040/DSP-AX461

Pin No.	Port Name	Function Name	I/O	Detail of Function
121	A19	ADDR19	O	SDRAM address bus 19
122	A18	ADDR18	O	SDRAM address bus 18
123	A17	ADDR17	O	SDRAM address bus 17
124	A16	ADDR16	O	SDRAM address bus 16
125	A15	ADDR15	O	SDRAM address bus 15
126	A14	ADDR14	O	SDRAM address bus 14
127	A13	ADDR13	O	SDRAM address bus 13
128	DGND	GND	–	Ground of external
129	DGND	GND	–	Ground of external
130	DGND	GND	–	Ground of external
131	DGND	GND	–	Ground of external
132	DGND	GND	–	Ground of external
133	DGND	GND	–	Ground of external
134	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
135	A12	ADDR12	O	SDRAM address bus 12
136	A11	ADDR11	O	SDRAM address bus 11
137	A10	ADDR10	O	SDRAM address bus 10
138	A09	ADDR9	O	SDRAM address bus 09
139	A08	ADDR8	O	SDRAM address bus 08
140	A07	ADDR7	O	SDRAM address bus 07
141	A06	ADDR6	O	SDRAM address bus 06
142	A05	ADDR5	O	SDRAM address bus 05
143	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
144	DGND	GND	–	Ground of external
145	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
146	A04	ADDR4	O	SDRAM address bus 04
147	A03	ADDR3	O	SDRAM address bus 03
148	A02	ADDR2	O	SDRAM address bus 02
149	A01	ADDR1	O	SDRAM address bus 01
150	SDQM1	/ABE1	O	SDRAM byte enable/data mask 1
151	SDQM0	/ABE0	O	SDRAM byte enable/data mask 0
152	/AWE	/AWE	O	Write enable (Asynchronous)
153	/ARE	/ARE	O	Read enable
154	/AOE	/AOE	O	Output enable
155	DGND	GND	–	Ground of external
156	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
157	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
158	/AMS3	/AMS3	O	Bank select 3
159	/AMS2	/AMS2	O	Bank select 2
160	/AMS1	/AMS1	O	Bank select 1
161	/AMS0	/AMS0	O	Bank select 0
162	ARDY	ARDY	I	Hardware ready control
163	/BR	/BR	I	(Pull-up)
164	SA10	SA10	O	A10 pin
165	/SWE	/SWE	O	Write enable (Synchronization)
166	/SCAS	/SCAS	O	Sequence address strobe
167	/SRAS	/SRAS	O	Line address strobe
168	VDDINT	VDDINT	–	Power supply of microprocessor (BF1.2)
169	CLKOUT	CLKOUT	O	Clock output
170	DGND	GND	–	Ground of external
171	VDDEXT	VDDEXT	–	I/O power supply (EX3.3)
172	/SMS	/SMS	O	Bank select
173	SCKE	SCKE	O	Clock enable
174	DGND	GND	–	Ground of external
175	DGND	GND	–	Ground of external
176	DGND	GND	–	Ground of external

- Microprocessor extended port

IC204-IC207: SN74LV573APWR (DSP P.C.B.)

Octal 3-state D-latches with 3-state outputs



IC204

Pin No.	Port Name	Function Name	Detail of Function
1	/OE	/EXPE	Extended port enable
2	1D	D00	Data bus 00
3	2D	D01	Data bus 01
4	3D	D02	Data bus 02
5	4D	D03	Data bus 03
6	5D	D04	Data bus 04
7	6D	D05	Data bus 05
8	7D	D06	Data bus 06
9	8D	D07	Data bus 07
10	GND	DGND	Ground of external
11	LE	LEEX1	Bank select 1
12	8Q	/SPISEL3	CS for CODEC IC (IC301, DSP P.C.B.)
13	7Q	ADSEL2	4ch ADC input select 2
14	6Q	ADSEL1	4ch ADC input select 1
15	5Q	ADSEL0	4ch ADC input select 0
16	4Q	/CCBE	SPI bus switch
17	3Q	/CMT	Center mute
18	2Q	/SMT	Surround mute
19	1Q	/FMT	Front mute
20	VCC	EX3.3	Power supply

IC205

Pin No.	Port Name	Function Name	Detail of Function
1	/OE	/EXPE	Extended port enable
2	1D	D08	Data bus 08
3	2D	D09	Data bus 09
4	3D	D10	Data bus 10
5	4D	D11	Data bus 11
6	5D	D12	Data bus 12
7	6D	D13	Data bus 13
8	7D	D14	Data bus 14
9	8D	D15	Data bus 15
10	GND	DGND	Ground of external
11	LE	LEEX1	Bank select 1
12	8Q	Ex1-15/CLKSEL	XM clock select (U, C models)
13	7Q	SSEL3	SCENE select LED switch 3
14	6Q	SSEL2	SCENE select LED switch 2
15	5Q	SSEL1	SCENE select LED switch 1
16	4Q	/IC_AK	IC for CODEC IC (IC301, DSP P.C.B.) and VFD (IC201, OPERATION P.C.B.)
17	3Q	/SPISEL4	CS for VFD (IC201, OPERATION P.C.B.)
18	2Q	/3.3SW	+3.3S switch
19	1Q	PRY	Power relay
20	VCC	EX3.3	Power supply

IC206

Pin No.	Port Name	Function Name	Detail of Function
1	/OE	/EXPE	Extended port enable
2	1D	D00	Data bus 00
3	2D	D01	Data bus 01
4	3D	D02	Data bus 02
5	4D	D03	Data bus 03
6	5D	D04	Data bus 04
7	6D	D05	Data bus 05
8	7D	D06	Data bus 06
9	8D	D07	Data bus 07
10	GND	DGND	Ground of external
11	LE	LEEX2	Bank select 2
12	8Q	/VR1	Video select R
13	7Q	SPISEL5	CE for tuner
14	6Q	/8ohmSW	AC H/L relay (RY106, MAIN P.C.B.)
15	5Q	HPRY	Headphone relay (RY102, MAIN P.C.B.)
16	4Q	MRYA	Main speakers A relay (RY101, MAIN P.C.B.)
17	3Q	MRYB	Main speakers B relay (RY102, MAIN P.C.B.)
18	2Q	CSRY	Center/surround speakers relay (RY103/R105, MAIN P.C.B.)
19	1Q	/SWMT	Subwoofer mute
20	VCC	EX3.3	Power supply

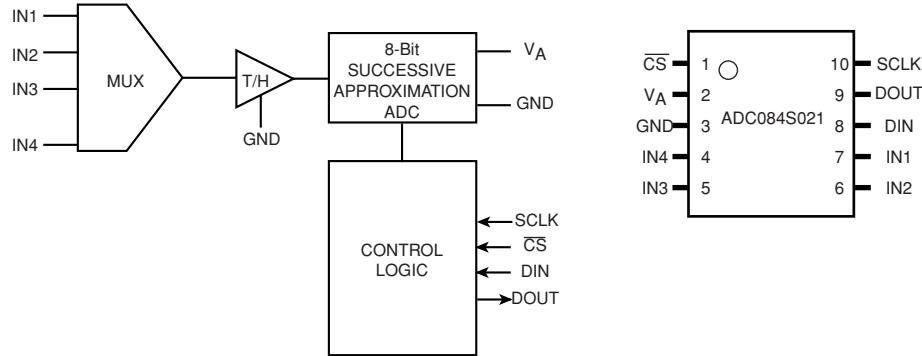
IC207

Pin No.	Port Name	Function Name	Detail of Function
1	/OE	/EXPE	Extended port enable
2	1D	D08	Data bus 08
3	2D	D09	Data bus 09
4	3D	D10	Data bus 10
5	4D	D11	Data bus 11
6	5D	D12	Data bus 12
7	6D	D13	Data bus 13
8	7D	D14	Data bus 14
9	8D	D15	Data bus 15
10	GND	DGND	Ground of external
11	LE	LEEX2	Bank select 2
12	8Q	DST	Direct stereo
13	7Q	/OSDSEL	OSD/Video select
14	6Q	MON	Monitor mute
15	5Q	/SPISEL6	CS for OSD (IC342, VIDEO P.C.B.)
16	4Q	VIC	Video select C
17	3Q	—	
18	2Q	Ex2-09/VIB	Video select B
19	1Q	Ex2-08/VIA	Video select A
20	VCC	EX3.3	Power supply

- **Microprocessor ADC select port**

IC401: ADC084S021C1MM (DSP P.C.B.)

4-channel, 200 kSPS, 8-bit A/D converter



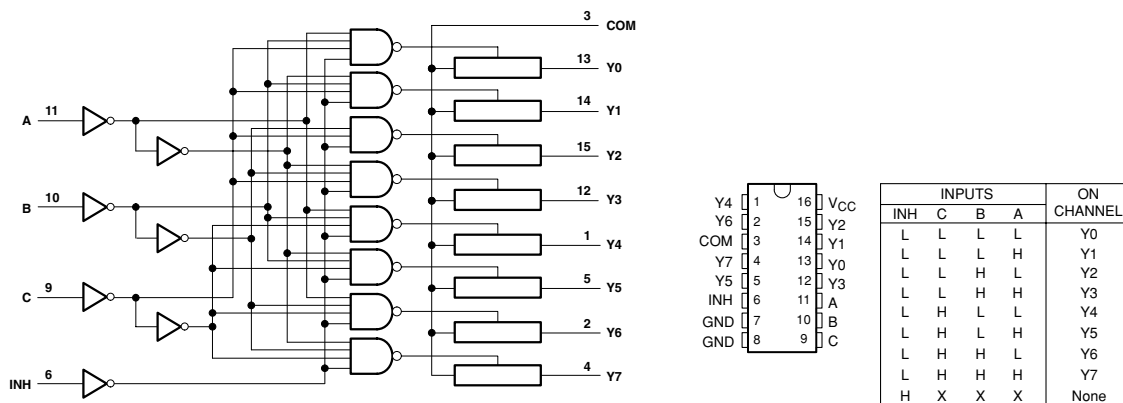
Pin No.	Port Name	Function Name	Detail of Function
1	/CS	/SPISEL1	CS for microprocessor
2	VA	VA	+3.3S
3	GND	DGND	Ground of external
4	IN4	IN4	SPI bus COM (IC402)
5	IN3	IN3	SPI bus COM (IC403)
6	IN2	KEY1	Key input 1
7	IN1	KEY0	Key input 0
8	DIN	SPIMO	Master output/slave input
9	DOUT	SPIMI	Master input/slave output
10	SCLK	SPISCK	SPI clock

Key input (A/D), pull-up resistance 10 k-ohms

Ohm [ohm]	+1.0 k	+1.0 k	+1.5 k	+2.2 k	+3.3 k	+4.7 k	+4.7 k	+6.8 k	+10.0 k	+22.0 k
V [V]	0.3	0.55	0.86	1.2	1.56	1.91	2.14	2.36	2.57	2.81
KEY0 (7 pin)	SCENE 1	SCENE 2	PROGRAM <	PROGRAM >	STRAIGHT	TONE CONTROL	SEARCH MODE	FM/AM	A/B/C/D/E	SPEAKERS
KEY1 (6 pin)	SCENE 3	SCENE 4	DIRECT	AUDIO SELECT	INPUT <	INPUT >	PRESET/ TUNING <	PRESET/ TUNING >	MEMORY	TUNING

IC402, IC403: SN74LV4051APWR (DSP P.C.B.)

8-channel analog multiplexers/demultiplexers



IC402

Pin No.	Port Name	Function Name	Detail of Function
1	Y4	iPAP	DOCK (iPod) detect (ACCPOW)
2	Y6	DEST2	Destination 2 *
3	COM	COM	SPI bus IN4 (IC401)
4	Y7	LINKACTIVE	Link detect (U, C models)
5	Y5	XM_MUTE	XM mute (U, C models)
6	INH	DGND	(Pull-down)
7	GND	DGND	Ground of external
8	GND	DGND	Ground of external
9	COM	ADSEL2	Input select 2
10	B	ADSEL1	Input select 1
11	A	ADSEL0	Input select 0
12	Y3	iPDET	DOCK (iPod) detect (iPDET)
13	Y0	/MIC	MIC detect
14	Y1	/ST	Stereo for tuner
15	Y2	/TUNED	Tuned for tuner
16	Vcc	+3.3S	Power supply

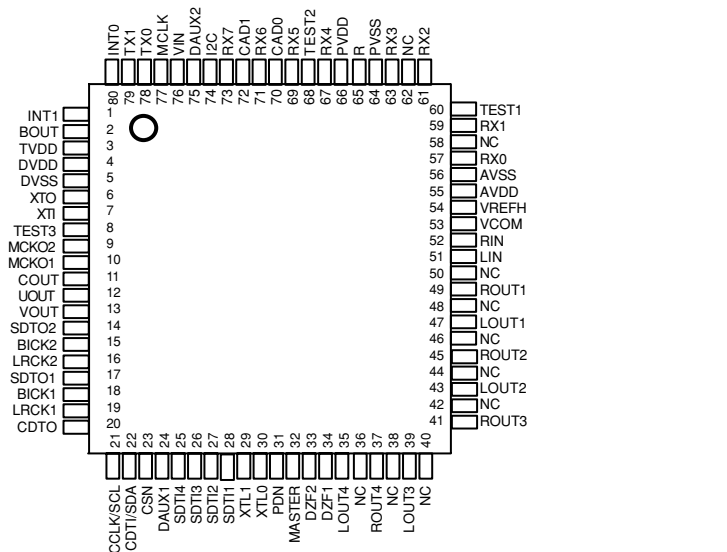
* Destination for A/D port

R416 [ohm]	1.5 k	5.6 k	1.0 k	6.8 k	100 k	3.3 k
R410 [ohm]	3.3 k	22 k	8.2 k	3.3 k	(open)	4.7 k
DEST2 (2 pin) [V]	2.1-2.4	2.5-2.8	2.8-3.1	1.0-1.3	3.1-3.3	1.8-2.1
A/D value (3.3 V=255)	164-189	190-215	216-239	70-99	240-255	133-163
Destination	J	U, C	R	K	A	L

IC403

Pin No.	Port Name	Function Name	Detail of Function
1	Y4	DEST	Destination 1 (fixed)
2	Y6	/PDET	Sub-trans detect
3	COM	COM	SPI bus IN3 (IC401)
4	Y7	/HP	Headphone detect
5	Y5	PRIIN	Current protection
6	INH	DGND	(Pull-down)
7	GND	DGND	Ground of external
8	GND	DGND	Ground of external
9	COM	ADSEL2	Input select 2
10	B	ADSEL1	Input select 1
11	A	ADSEL0	Input select 0
12	Y3	PLDET	Limiter detect
13	Y0	PRDIN	Amplifier DC detect
14	Y1	PRVIN	Voltage protection
15	Y2	THMIN	Thermo protection
16	Vcc	+3.3S	Power supply

2/8-channel audio CODEC with DIR



RX-V461/HTR-6040/DSP-AX461

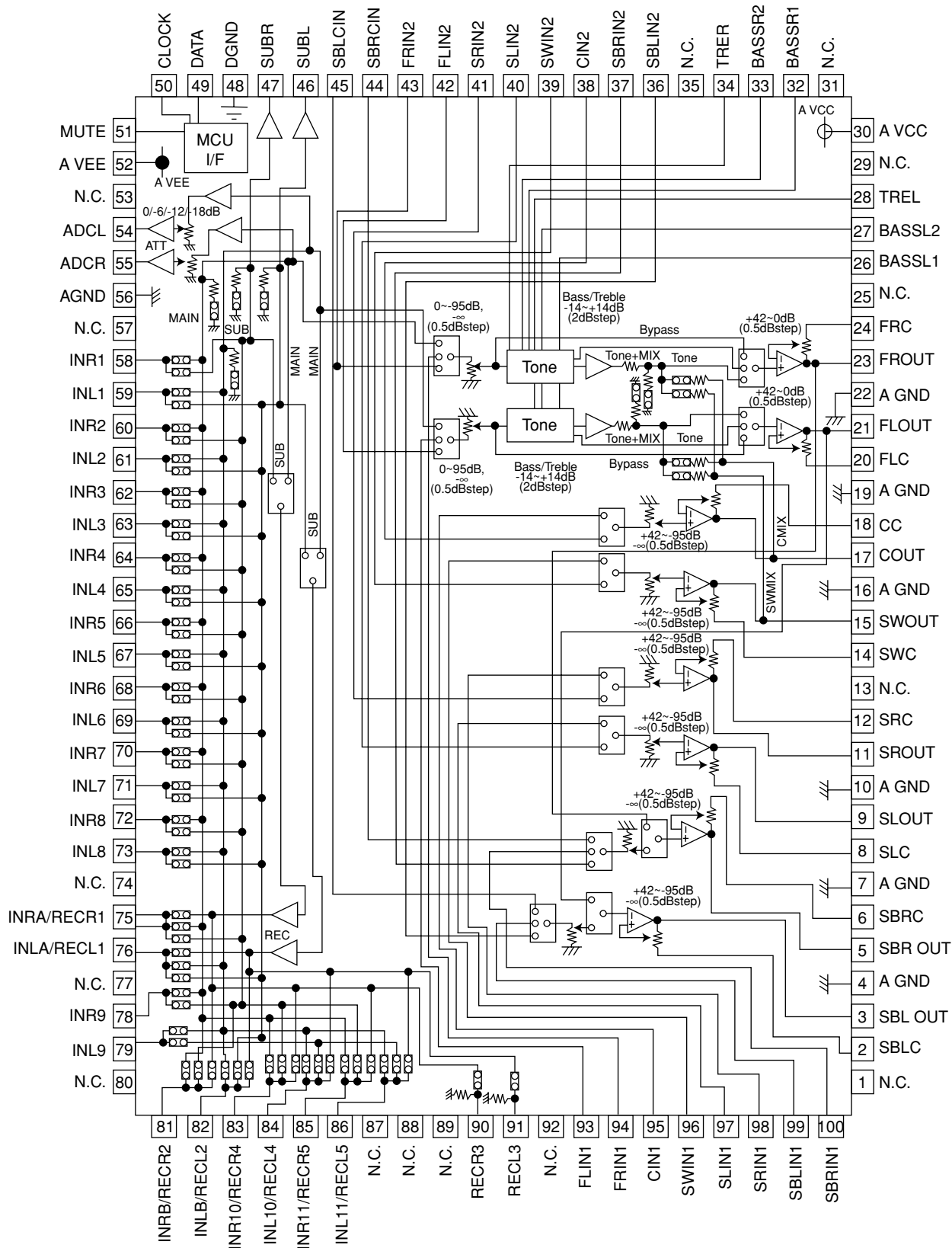
Pin No.	Function Name	I/O	Detail of Function
1	INT1	O	Interrupt 1 pin
2	BOUT	O	Block-start output pin for receiver input "H" during first 40 frames
3	TVDD	–	Output buffer power supply pin, 2.7 V to 5.5 V
4	DVDD	–	Digital power supply pin, 4.5 V to 5.5 V
5	DVSS	–	Digital ground pin
6	XTO	O	X'tal clock output pin
7	XTI	I	X'tal / External clock input pin
8	TEST3	I	Test 3 pin This pin should be connected to DVSS
9	MCKO2	O	Master clock output 2 pin
10	MCKO1	O	Master clock output 1 pin
11	COOUT	O	C-bit output pin for receiver input
12	UOUT	O	U-bit output pin for receiver input
13	VOOUT	O	V-bit output pin for receiver input
14	SDTO2	O	Audio serial data output pin (DIR/DIT part)
15	BICK2	I/O	Audio serial data clock pin (DIR/DIT part)
16	LRCK2	I/O	Channel clock pin (DIR/DIT part)
17	SDTO1	O	Audio serial data output pin (ADC/DAC part)
18	BICK1	I/O	Audio serial data clock pin (ADC/DAC part)
19	LRCK1	I/O	Input channel clock pin
20	CDTO	O	Control data output pin in serial mode, I2C pin= "L"
21	CCLK	I	Control data clock pin in serial mode, I2C pin= "L"
	SCL	I	Control data clock pin in serial mode, I2C pin= "H"
22	CDTI	I	Control data input pin in serial mode, I2C pin= "L"
	SDA	I/O	Control data pin in serial mode, I2C pin= "H"
23	CSN	I	Chip select pin in serial mode, I2C pin="L"
		I	This pin should be connected to DVSS, I2C pin="H"
24	DAUX1	I	AUX audio serial data input pin (ADC/DAC part)
25	SDTI4	I	DAC4 audio serial data input pin
26	SDTI3	I	DAC3 audio serial data input pin
27	SDTI2	I	DAC2 audio serial data input pin
28	SDTI1	I	DAC1 audio serial data input pin
29	XTL1	I	X'tal frequency select 0 pin
30	XTL0	I	X'tal frequency select 1 pin
31	PDN	I	Power-down mode pin When "L", the AK4588 is powered-down, all output pin goes "L", all registers are reset When CAD1-0 pins are changed, the AK4588 should be reset by PDN pin
32	MASTER	I	Master mode select pin "H": Master mode, "L": Slave mode
33	DZF2	O	Zero input detect 2 pin (table 13) When the input data of the group 1 follow total 8192 LRCK cycles with "0" input data, this pin goes to "H" / When RSTN1 bit is "0" or PWDAN bit is "0", this pin goes to "H"
	OVF	O	Analog input overflow detect pin This pin goes to "H" if the analog input of L ch or R ch overflows This pin becomes OVF pin if OVFE bit is set to 1
34	DZF1	O	Zero input detect 1 pin (table 13) When the input data of the group 1 follow total 8192 LRCK cycles with "0" input data, this pin goes to "H" / When RSTN1 bit is "0" or PWDAN bit is "0", this pin goes to "H"
35	LOUT4	O	DAC4 L ch analog output pin
36	NC	–	No connect pin No internal bonding / This pin should be opened
37	ROUT4	O	DAC4 R ch analog output pin
38	NC	–	No connect pin No internal bonding / This pin should be opened
39	LOUT3	O	DAC3 L ch analog output pin
40	NC	–	No connect pin No internal bonding / This pin should be opened

Pin No.	Function Name	I/O	Detail of Function
41	ROUT3	O	DAC3 R ch analog output pin
42	NC	–	No connect pin No internal bonding / This pin should be opened
43	LOUT2	O	DAC2 L ch analog output pin
44	NC	–	No connect pin No internal bonding / This pin should be opened
45	ROUT2	O	DAC2 R ch analog output pin
46	NC	–	No connect pin No internal bonding / This pin should be opened
47	LOUT1	O	DAC1 L ch analog output pin
48	NC	–	No connect pin No internal bonding / This pin should be opened
49	ROUT1	O	DAC1 R ch analog output pin
50	NC	–	No connect pin No internal bonding / This pin should be opened
51	LIN	I	L ch analog input pin
52	RIN	I	R ch analog input pin
53	VCOM	–	Common voltage output pin 2.2 F capacitor should be connected to AVSS externally
54	VREFH	–	Positive voltage reference input pin, AVDD
55	AVDD	–	Analog power supply pin, 4.5 V to 4.5 V
56	AVSS	–	Analog ground pin, 0 V
57	RX0	I	Receiver channel 0 pin (Internal biased pin / Internally biased at PVDD/2)
58	NC	–	No connect pin No internal bonding / This pin should be connected to PVSS
59	RX1	I	Receiver channel 1 pin (Internal biased pin / Internally biased at PVDD/2)
60	TEST1	I	Test 1 pin This pin should be connected to PVSS
61	RX2	I	Receiver channel 2 pin (Internal biased pin / Internally biased at PVDD/2)
62	NC	–	No connect pin No internal bonding / This pin should be connected to PVSS
63	RX3	I	Receiver channel 3 pin (Internal biased pin / Internally biased at PVDD/2)
64	PVSS	–	PLL ground pin
65	R	–	External resistor pin 12 k-ohms +/-1 % resistor should be connected to PVSS externally
66	PVDD	–	PLL power supply pin, 4.5 V to 4.5 V
67	RX4	I	Receiver channel 4 pin (Internal biased pin / Internally biased at PVDD/2)
68	TEST2	I	Test 2 pin This pin should be connected to PVSS
69	RX5	I	Receiver channel 5 pin (Internal biased pin / Internally biased at PVDD/2)
70	CAD0	I	Chip address 0 pin (ADC/DAC part)
71	RX6	I	Receiver channel 6 pin (Internal biased pin / Internally biased at PVDD/2)
72	CAD1	I	Chip address 1 pin (ADC/DAC part)
73	RX7	I	Receiver channel 7 pin (Internal biased pin / Internally biased at PVDD/2)
74	I2C	I	Control mode select pin “L”: 4-wire serial, “H”: I2C bus
75	DAUX2	I	Auxiliary audio data input pin (DIR/DIT part)
76	VIN	I	V-bit input pin for transmitter output
77	MCLK	I	Master clock input pin
78	TX0	O	Transmit channel (through data) output 0 pin
79	TX1	O	Transmit channel output 1 pin When TX bit = “0”, transmit channel (through data) output 1 pin. When TX bit = “1”, transmit channel (DAUX2 data) output pin (default)
80	INT0	O	Interrupt 0 pin

Note: All input pins except internal biased pins and internal pull-down pin should not be left floating.

IC161: R2A15215FP (MAIN P.C.B.)

8-channel electronic volume with 11 input selector and tone control

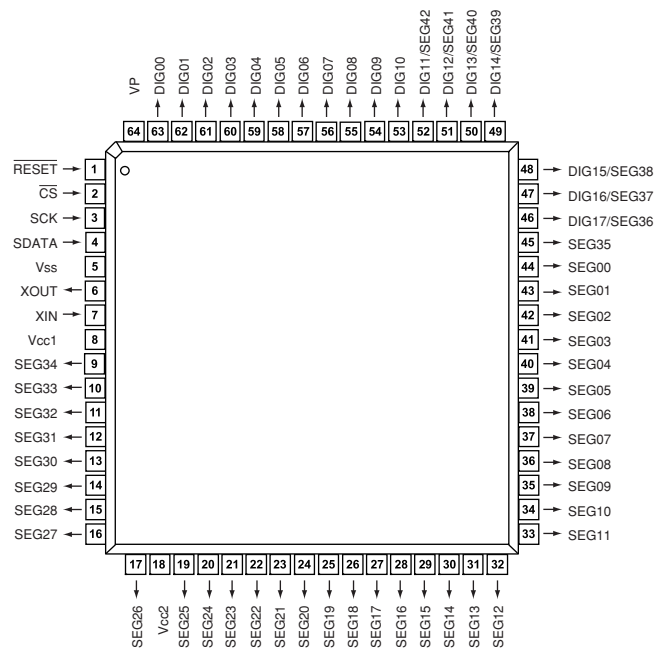
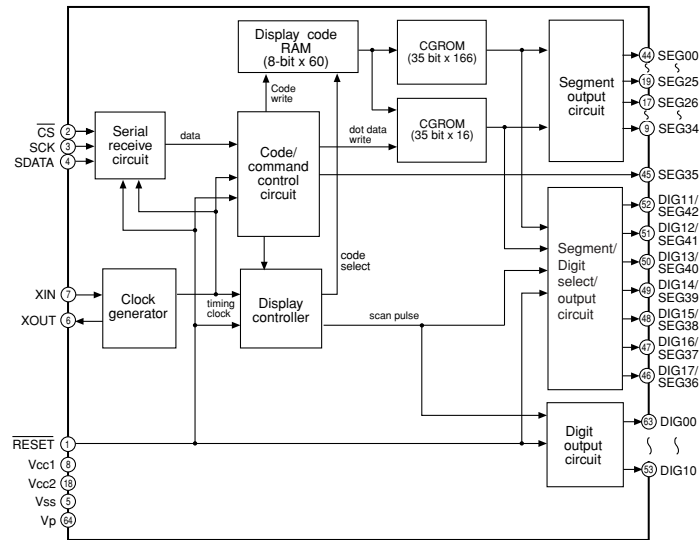


Pin No.	Function Name	Detail of Function
1	N.C.	No connected
2	SBLC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
3	SBLOUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
4	AGND	Analog GND terminal
5	SBROUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
6	SBRC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
7	AGND	Analog GND terminal
8	SLC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
9	SLOUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
10	AGND	Analog GND terminal
11	SROUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
12	SRC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
13	N.C.	No connected
14	SWC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
15	SWOUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
16	AGND	Analog GND terminal
17	COUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
18	CC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
19	AGND	Analog GND terminal
20	FLC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
21	FLOUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
22	AGND	Analog GND terminal
23	FROUT	FL/FR/C/SW/SL/SR/SBL/SBR ch output terminal
24	FRC	L/R/C/SW/SL/SR/SBL/SBR ch terminal to connect capacitor to reduce noise from changing the volume
25	N.C.	No connected
26	BASSL1	L/R ch tone control (Bass) terminal for setting frequency characteristics
27	BASSL2	L/R ch tone control (Bass) terminal for setting frequency characteristics
28	TREL	L/R ch tone control (Treble) terminal for setting frequency characteristics
29	N.C.	No connected
30	AVCC	Positive side power terminal
31	N.C.	No connected
32	BASSR1	L/R ch tone control (Bass) terminal for setting frequency characteristics
33	BASSR2	L/R ch tone control (Bass) terminal for setting frequency characteristics
34	TRER	L/R ch tone control (Treble) terminal for setting frequency characteristics
35	N.C.	No connected
36	SBLIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
37	SBRIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
38	CIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
39	SWIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
40	SLIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
41	SRIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
42	FLIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
43	FRIN2	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
44	SBRCIN	SBL/SBR ch volume input terminal
45	SBLCIN	SBL/SBR ch volume input terminal
46	SUBL	L/R ch SUB output terminal
47	SUBR	L/R ch SUB output terminal
48	DGND	Digital GND terminal
49	DATA	Control data input terminal
50	CLOCK	Control data input terminal
51	MUTE	External mute control terminal
52	AVEE	Negative side power terminal
53	N.C.	No connected
54	ADCL	L/R ch ADC output terminal
55	ADCR	L/R ch ADC output terminal

RX-V461/HTR-6040/DSP-AX461

Pin No.	Function Name	Detail of Function
56	AGND	Analog GND terminal
57	N.C.	No connected
58	INR1	L/R ch input terminal (input selector)
59	INL1	L/R ch input terminal (input selector)
60	INR2	L/R ch input terminal (input selector)
61	INL2	L/R ch input terminal (input selector)
62	INR3	L/R ch input terminal (input selector)
63	INL3	L/R ch input terminal (input selector)
64	INR4	L/R ch input terminal (input selector)
65	INL4	L/R ch input terminal (input selector)
66	INR5	L/R ch input terminal (input selector)
67	INL5	L/R ch input terminal (input selector)
68	INR6	L/R ch input terminal (input selector)
69	INL6	L/R ch input terminal (input selector)
70	INR7	L/R ch input terminal (input selector)
71	INL7	L/R ch input terminal (input selector)
72	INR8	L/R ch input terminal (input selector)
73	INL8	L/R ch input terminal (input selector)
74	N.C.	No connected
75	INRA/RECR1	L/R ch input terminal (input selector) / L/R ch REC output terminal
76	INLA/RECL1	L/R ch input terminal (input selector) / L/R ch REC output terminal
77	N.C.	No connected
78	INR9	L/R ch input terminal (input selector)
79	INL9	L/R ch input terminal (input selector)
80	N.C.	No connected
81	INRB/RECR2	L/R ch input terminal (input selector) / L/R ch REC output terminal
82	INLB/RECL2	L/R ch input terminal (input selector) / L/R ch REC output terminal
83	INR10/RECR4	L/R ch input terminal (input selector) / L/R ch REC output terminal
84	INL10/RECL4	L/R ch input terminal (input selector) / L/R ch REC output terminal
85	INR11/RECR5	L/R ch input terminal (input selector) / L/R ch REC output terminal
86	INL11/RECL5	L/R ch input terminal (input selector) / L/R ch REC output terminal
87	N.C.	No connected
88	N.C.	No connected
89	N.C.	No connected
90	RECR3	L/R ch REC output terminal
91	RECL3	L/R ch REC output terminal
92	N.C.	No connected
93	FLIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
94	FRIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
95	CIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
96	SWIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
97	SLIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
98	SRIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
99	SBLIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)
100	SBRIN1	L/R/C/SW/SL/SR/SBL/SBR ch input terminal (multi input 1/2)

IC201: M66003-0131FP-R (OPERATION P.C.B.)
18 digit 5x7 segment VFD controller/driver



RX-V461/HTR-6040/DSP-AX461

Pin No.	Port Name	Function Name	I/O	Detail of Function
1	Reset	/RESET	Reset input	When "L", M66003 is initialized
2	CS	/CEFL	Chip select input	When "L", communication with the MCU is possible When "H", any instruction from the MCU is neglected
3	SCK	CKFL	Shift clock input	Serial input data is taken and shifted by the positive edge of SCK
4	SDATA	DTFL	Serial data input	
5	Vss	VSS		GND (0V)
6	XOUT	XOUT	Clock output	When use as a CR oscillator, connect external resistor and capacitor / When use an external clock, input external clock to XIN, and XOUT must be opened
7	XIN	XIN	Clock input	
8	Vcc1	VDD		Positive power supply for internal logic
9	SEG34	P1I	Segment output	Positive power supply for DIG and SEG outputs
10	SEG33	P2	Segment output	
11	SEG32	P3	Segment output	
12	SEG31	P4	Segment output	
13	SEG30	P5	Segment output	
14	SEG29	P6	Segment output	
15	SEG28	P7	Segment output	
16	SEG27	P8	Segment output	
17	SEG26	P9	Segment output	
18	Vcc2	VDD		Connect to segment (anode) pins of VFD
19	SEG25	P10	Segment output	
20	SEG24	P11	Segment output	
21	SEG23	P12	Segment output	
22	SEG22	P13	Segment output	
23	SEG21	P14	Segment output	
24	SEG20	P15	Segment output	
25	SEG19	P16	Segment output	
26	SEG18	P17	Segment output	
27	SEG17	P18I	Segment output	
28	SEG16	P19	Segment output	
29	SEG15	P20	Segment output	
30	SEG14	P21	Segment output	
31	SEG13	P22	Segment output	
32	SEG12	P23	Segment output	
33	SEG11	P24	Segment output	
34	SEG10	P25	Segment output	
35	SEG09	P26	Segment output	
36	SEG08	P27	Segment output	
37	SEG07	P28	Segment output	
38	SEG06	P29	Segment output	
39	SEG05	P30	Segment output	
40	SEG04	P31	Segment output	
41	SEG03	P32	Segment output	
42	SEG02	P33	Segment output	
43	SEG01	P34	Segment output	
44	SEG00	P35	Segment output	
45	SEG35	P36	Segment output	
46	DIG17/SEG36	P37	Segment output	
47	DIG16/SEG37	G17I	Digit output	Connect to digit (grid) pins of VFD
48	DIG15/SEG38	G16I	Digit output	
49	DIG14/SEG39	G15I	Digit output	
50	DIG13/SEG40	G14	Digit output	
51	DIG12/SEG41	G13	Digit output	
52	DIG11/SEG42	G12	Digit output	
53	DIG10	G11	Digit output	
54	DIG09	G10	Digit output	
55	DIG08	G9	Digit output	
56	DIG07	G8	Digit output	
57	DIG06	G7	Digit output	
58	DIG05	G6	Digit output	
59	DIG04	G5	Digit output	
60	DIG03	G4	Digit output	
61	DIG02	G3	Digit output	
62	DIG01	G2	Digit output	
63	DIG00	G1	Digit output	
64	Vp	VP		Negative power supply to pull down

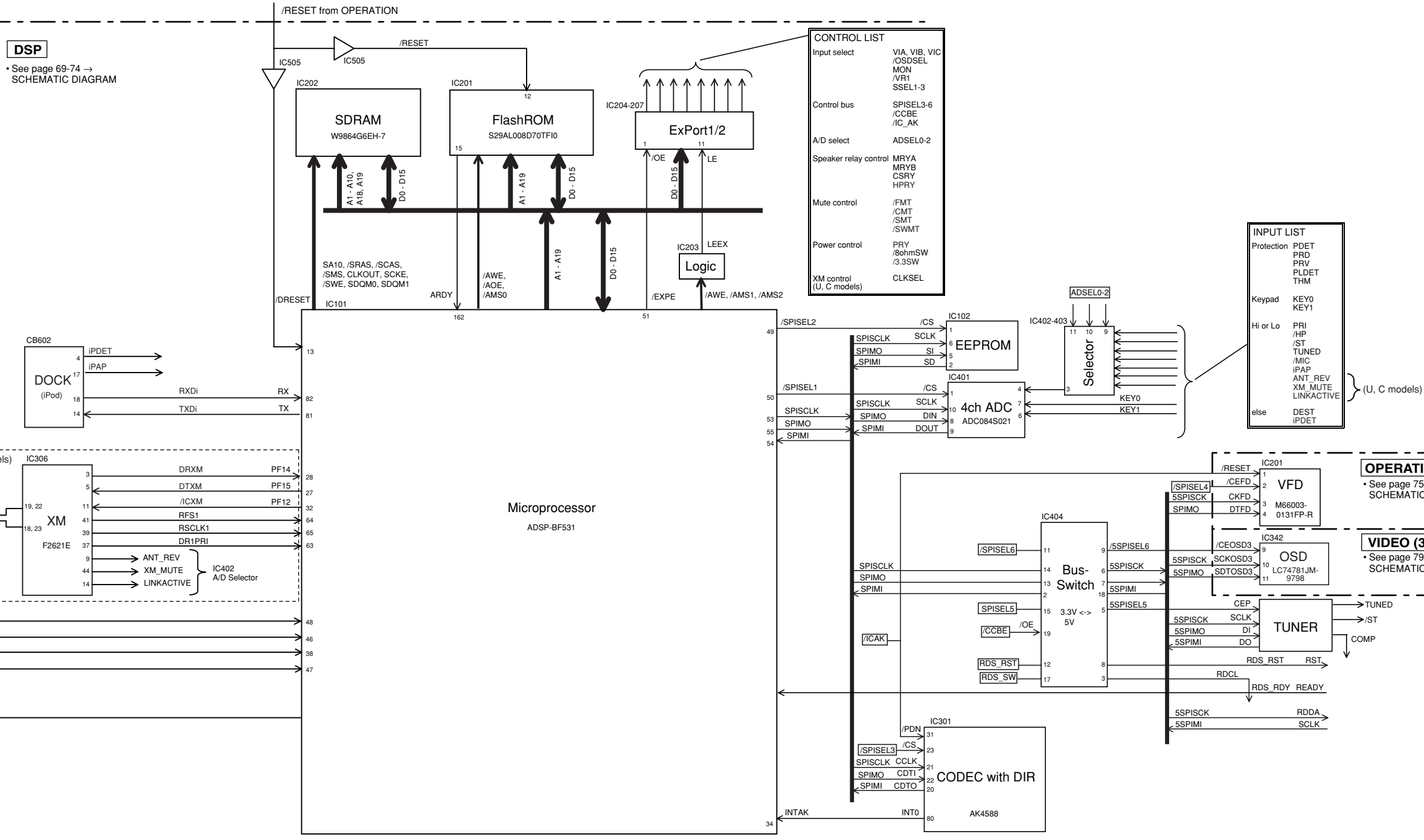
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DSP • See page 69-74 →
SCHEMATIC DIAGRAM



Control Section

OPERATION • See page 75, 76 → SCHEMATIC DIAGRAM

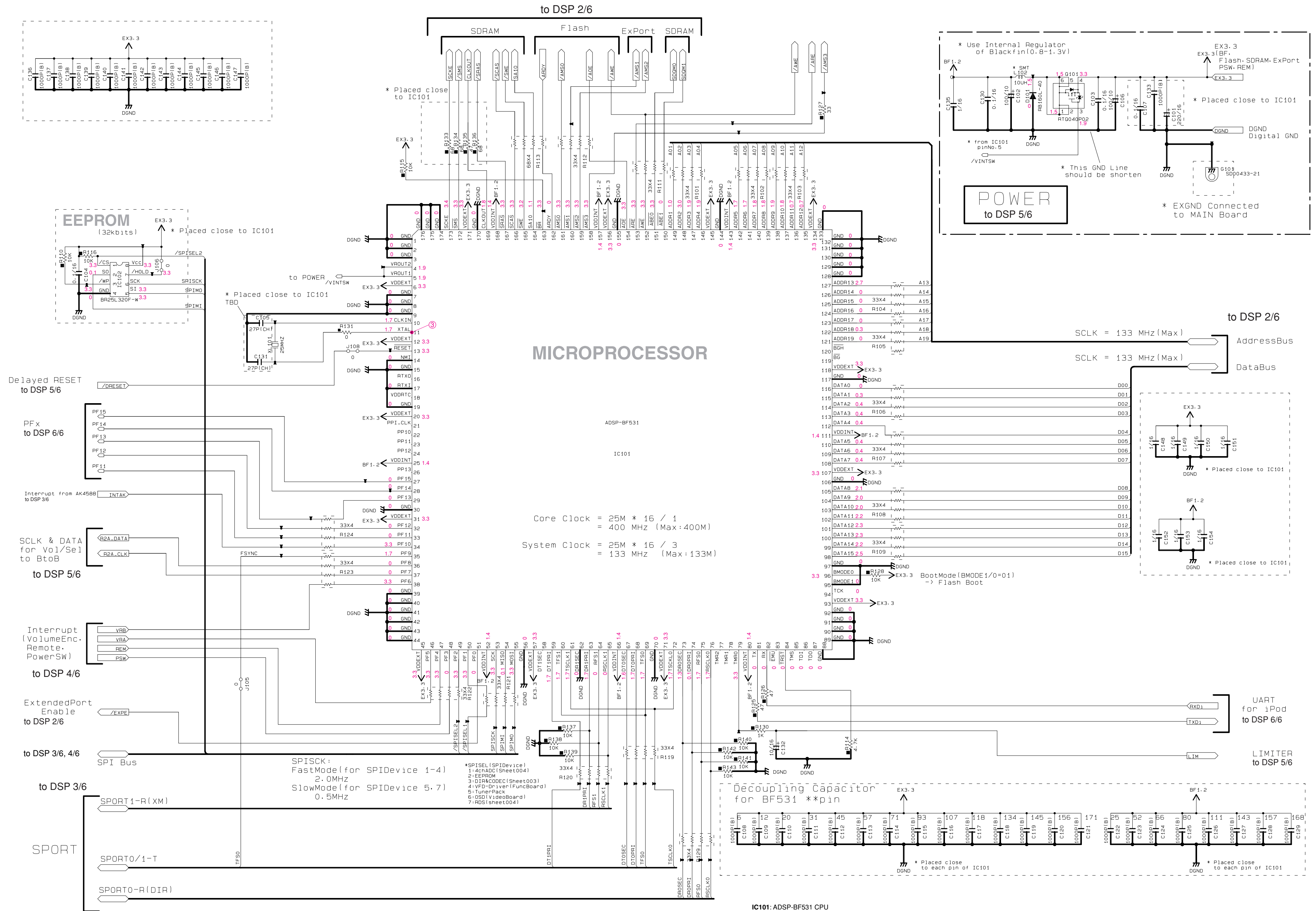


OPERATION
• See page 75, 76 → SCHEMATIC DIAGRAM

MAIN
• See page 77, 78 → SCHEMATIC DIAGRAM

SCHEMATIC DIAGRAMS
DSP 1/6

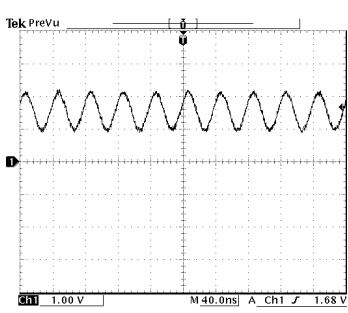
RX-V461/HTR-6040/DSP-AX461



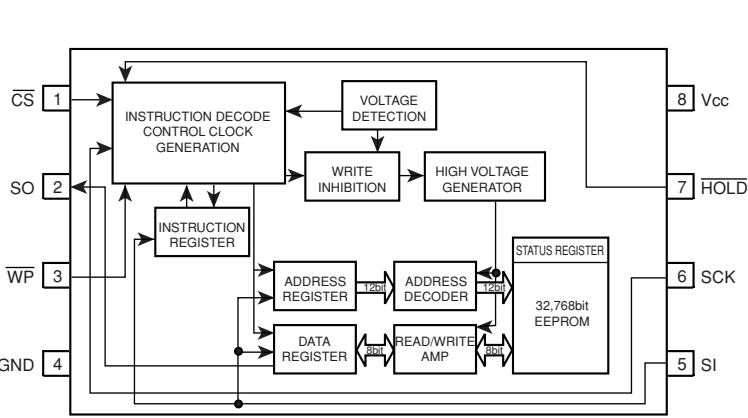
NOTICE (model)
(J)..... JAPAN
(U)..... U.S.A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN

- ★ All voltages are measured with a 10MΩ/V DC electronic voltmeter.
- ★ Components having special characteristics are marked and must be replaced with parts having specifications equal to those originally installed.
- ★ Schematic diagram is subject to change without notice.
- 電圧は、内部抵抗10MΩの電圧計で測定したものです。
- 印刷のある部品は、安全確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
- 本回路図は標準回路図です。改良のため予告なく変更することがございます。

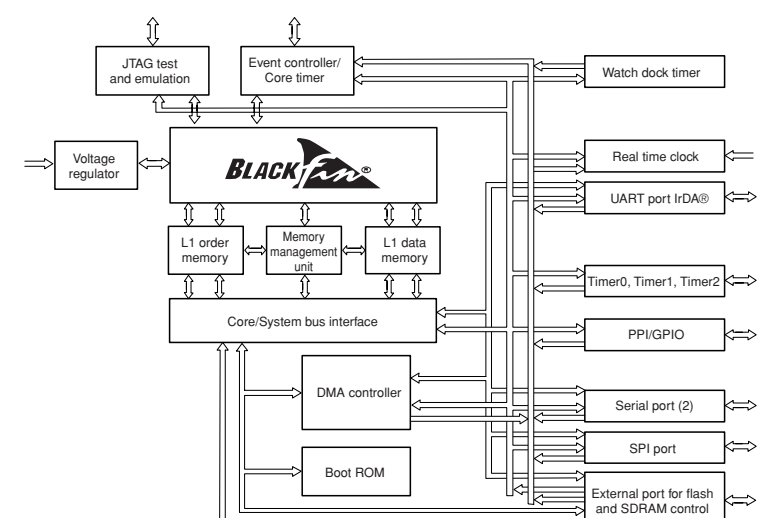
POINT ③ Pin 11 of IC101



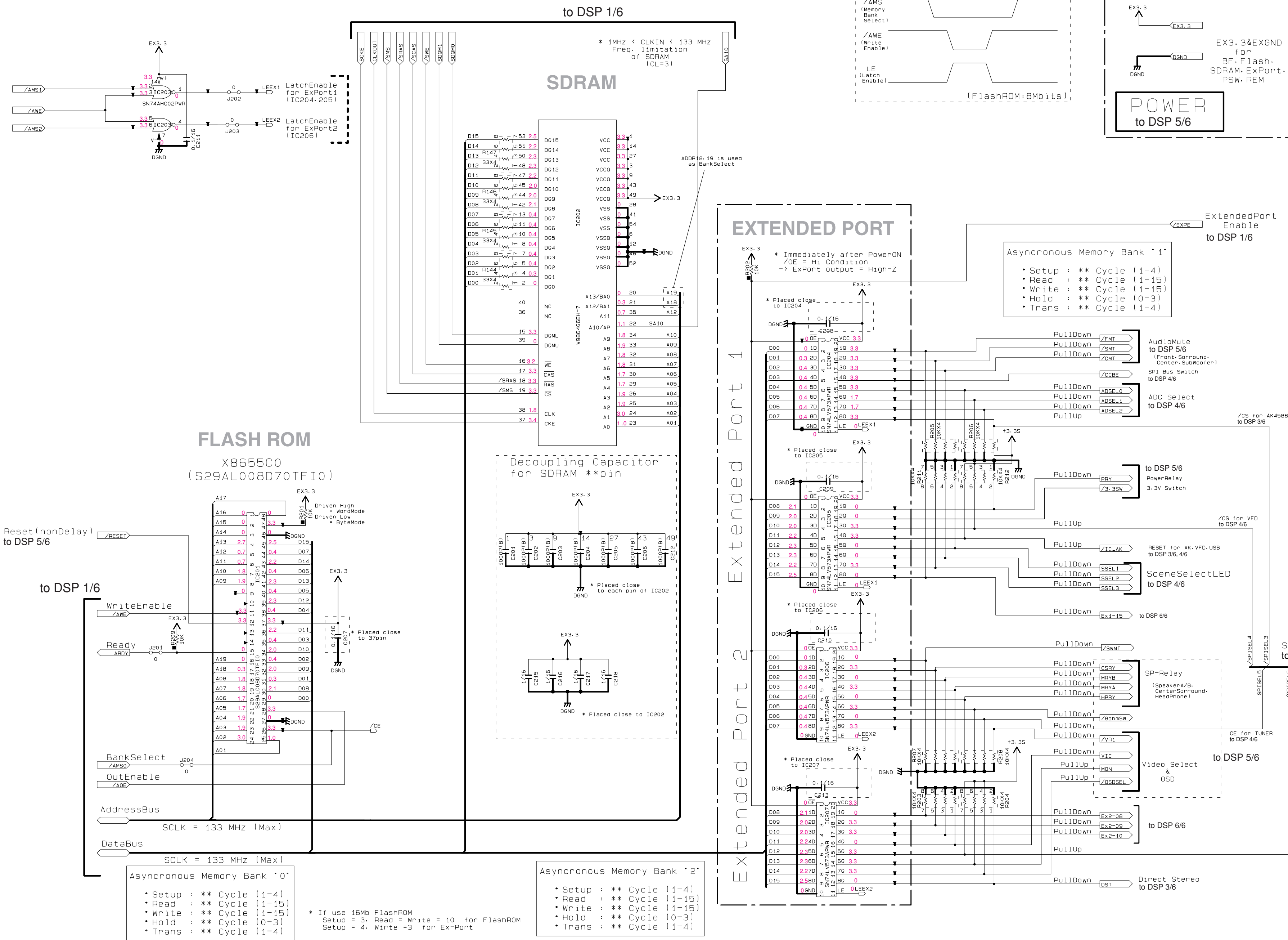
IC102: BR25L320F-W EEPROM
SPI BUS 32 k-bit (4,096 x 8-bit) EEPROM



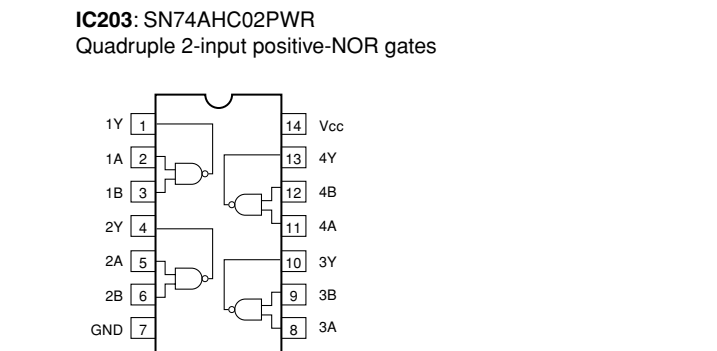
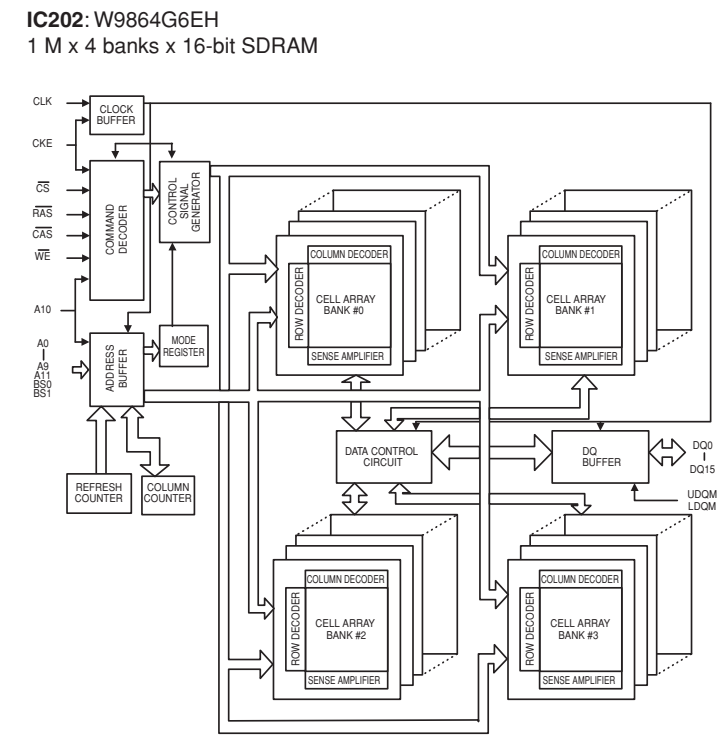
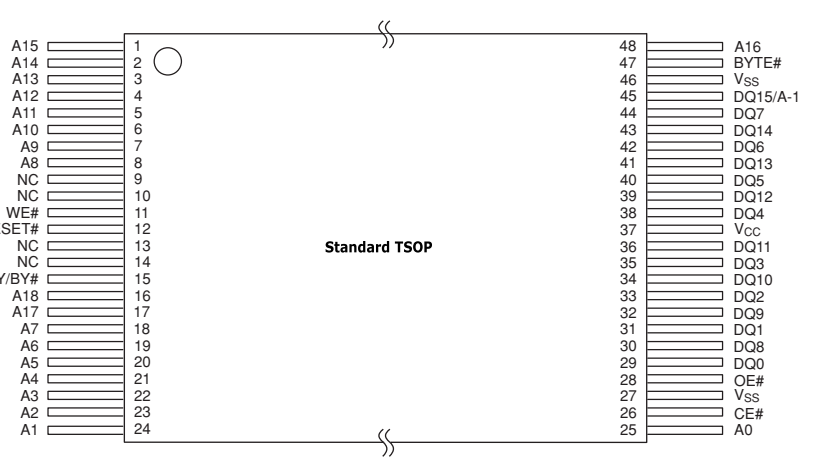
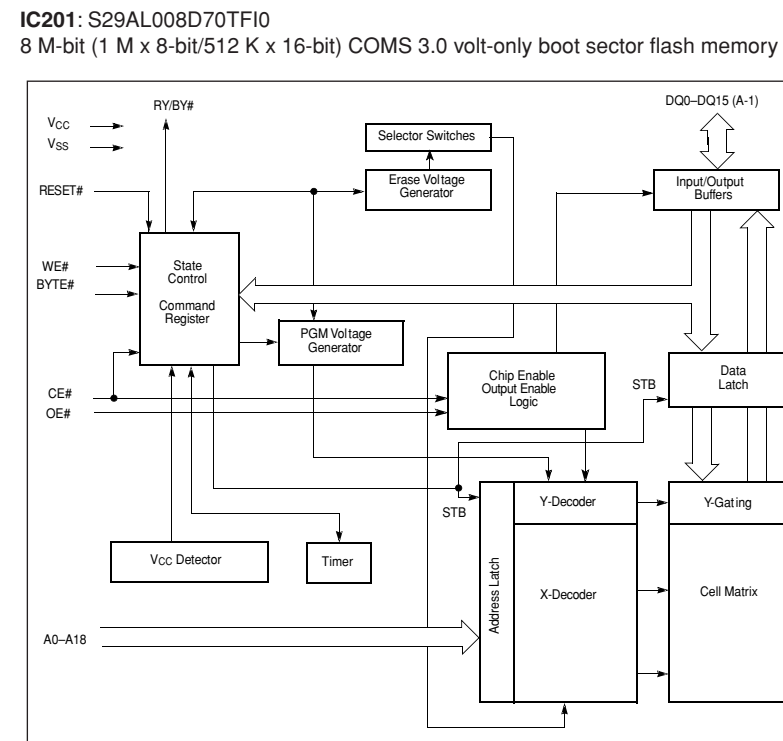
IC101: ADSP-BF531 CPU
Microprocessor



DSP 2/6



NOTICE (model)
(J)..... JAPAN
(U)..... U.S.A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN



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DSP 3/6

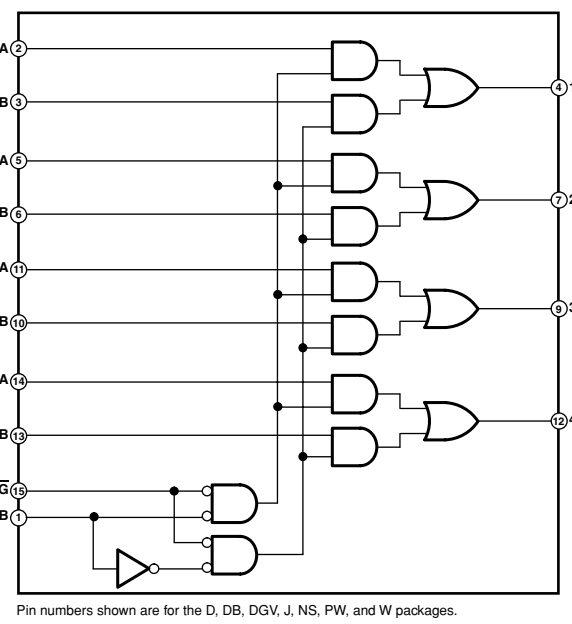
Mark	Reference Parts Number	Parts Name
K31	IC302-307	SN74LV157APWR 74VHC157MTCX-NF40

NOTICE (model)

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(U)..... U.S.A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN

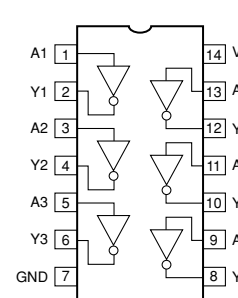
sxx	LOC	J	UC	R	A	K	L	
s1	C319	US13510 0.1/16	X	X	X	X	X	OPT OUT
s2	C311	W53720 IP22012U600-3R0T	X	X	X	X	X	
s3	U303	W555975 GRF4V4500	X	X	X	X	X	
s11	C337 C339 C339 C323 C323 C338 C332 C340	US13510 0.1/16	X	X	X	X	X	
s12	C338 C338	US56550 5PCH	X	X	X	X	X	
s13	C341	UR53747 47/15	X	X	X	X	X	
s15	C8302	X	X	X	X	X	X	
s16	D301 D302 D302	W455970 CAM-D950044024B	X	X	X	X	X	
s17	IC306	W57480 AVR116141RINTB	X	X	X	X	X	
s18	IC307	X	X	X	X	X	X	
s19	J302 J301	W57480 F2628E-01-1R	X	X	X	X	X	
s21	L305	X	X	X	X	X	X	
s22	R315 R315	RD35510 100	X	X	X	X	X	
s23	R318 R319 R320 R321 R322	W57480 IP22015U600-3R0T	X	X	X	X	X	
s24	R320 R319 R320 R321 R322	RD35510 100	X	X	X	X	X	
s25	R323	X	X	X	X	X	X	
s26	R324	X	X	X	X	X	X	
s27	R328 R316	RE04433 334	X	X	X	X	X	
s28	R329	X	X	X	X	X	X	
s29	R326	X	X	X	X	X	X	
s31	XL302	X	X	X	X	X	X	
s41	R336	RE04433 334	X	X	X	X	X	BYPASS

IC302, 307: SN74LV157APWR
Quadruple 2-line to 1-line data selectors/multiplexers

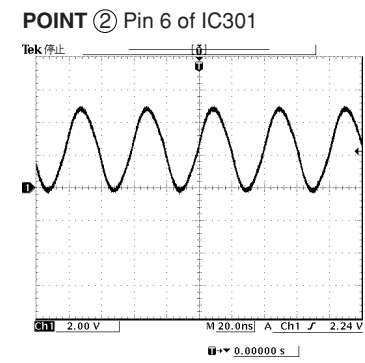
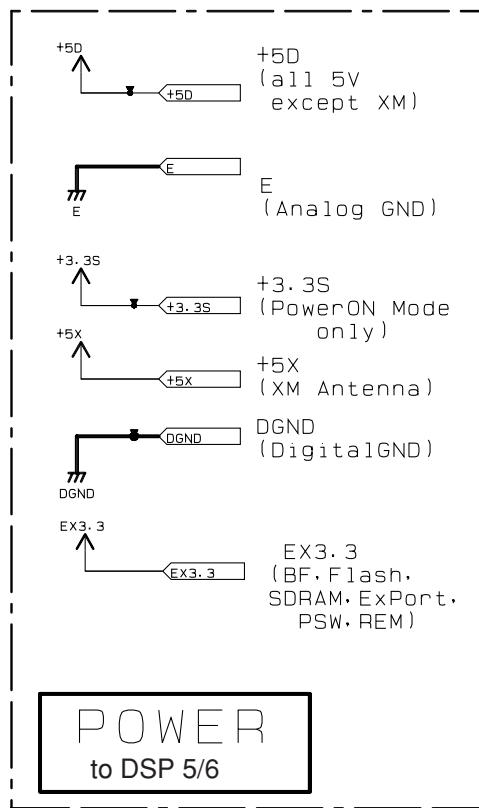
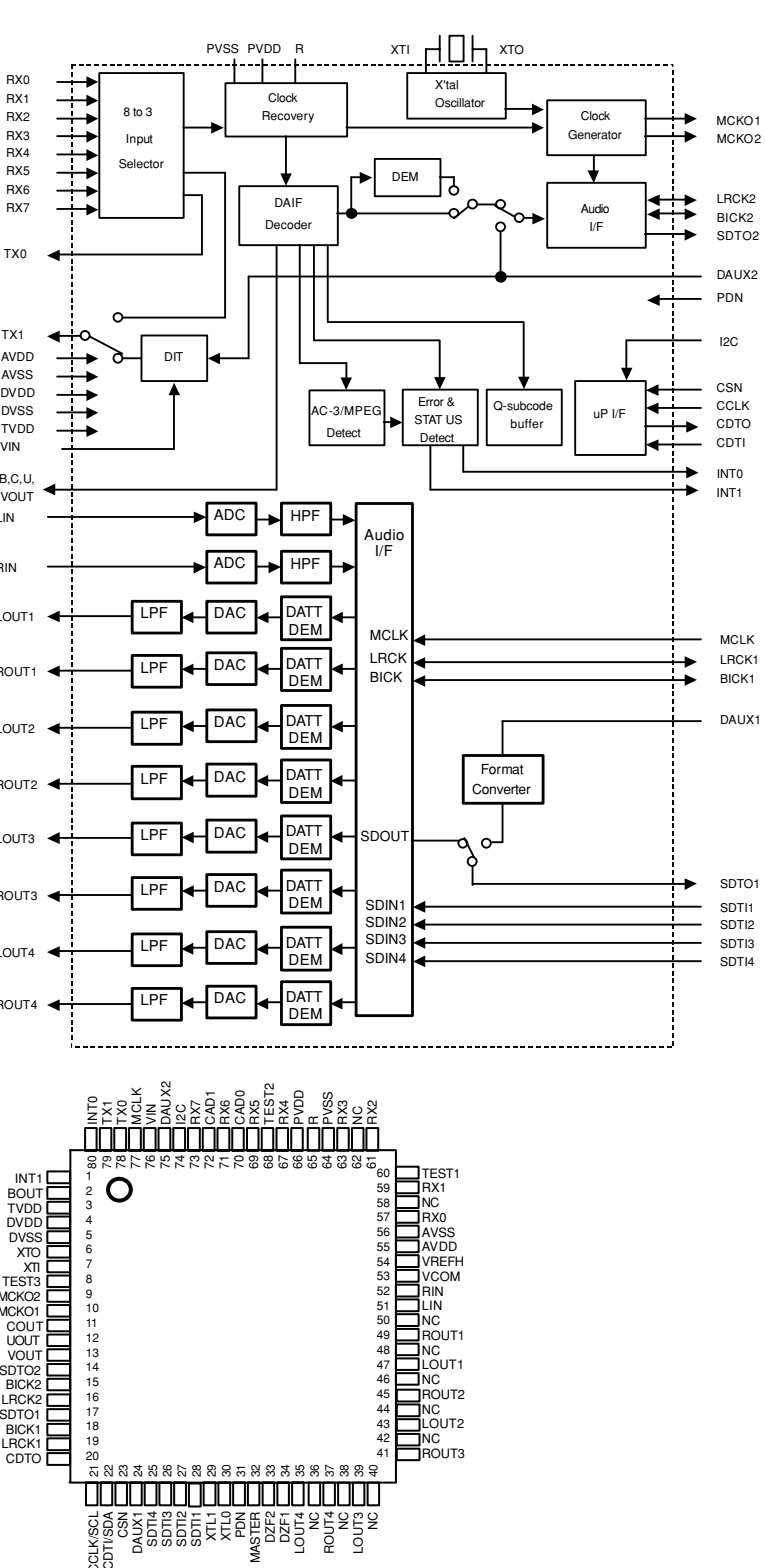


A/B	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1A	2	15														VCC
1B	3	14														G
1Y	4	13														4A
2A	5	12														4B
2B	6	11														3A
2Y	7	10														3B
GND	8	9														3Y

IC305: SN74LVU04APWR
Hex inverters



IC301: AK4588VQ
2/8-channel audio CODEC with DIR



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Page 78 [D10]
to MAIN (5)_CB161

Control
Signal
(FFC)
20pin

Page 77 [A5]
to MAIN (1)_CB101
Control
Signal
(BtoB)
Front

Page 78 [F3]
to MAIN (5)_CB164

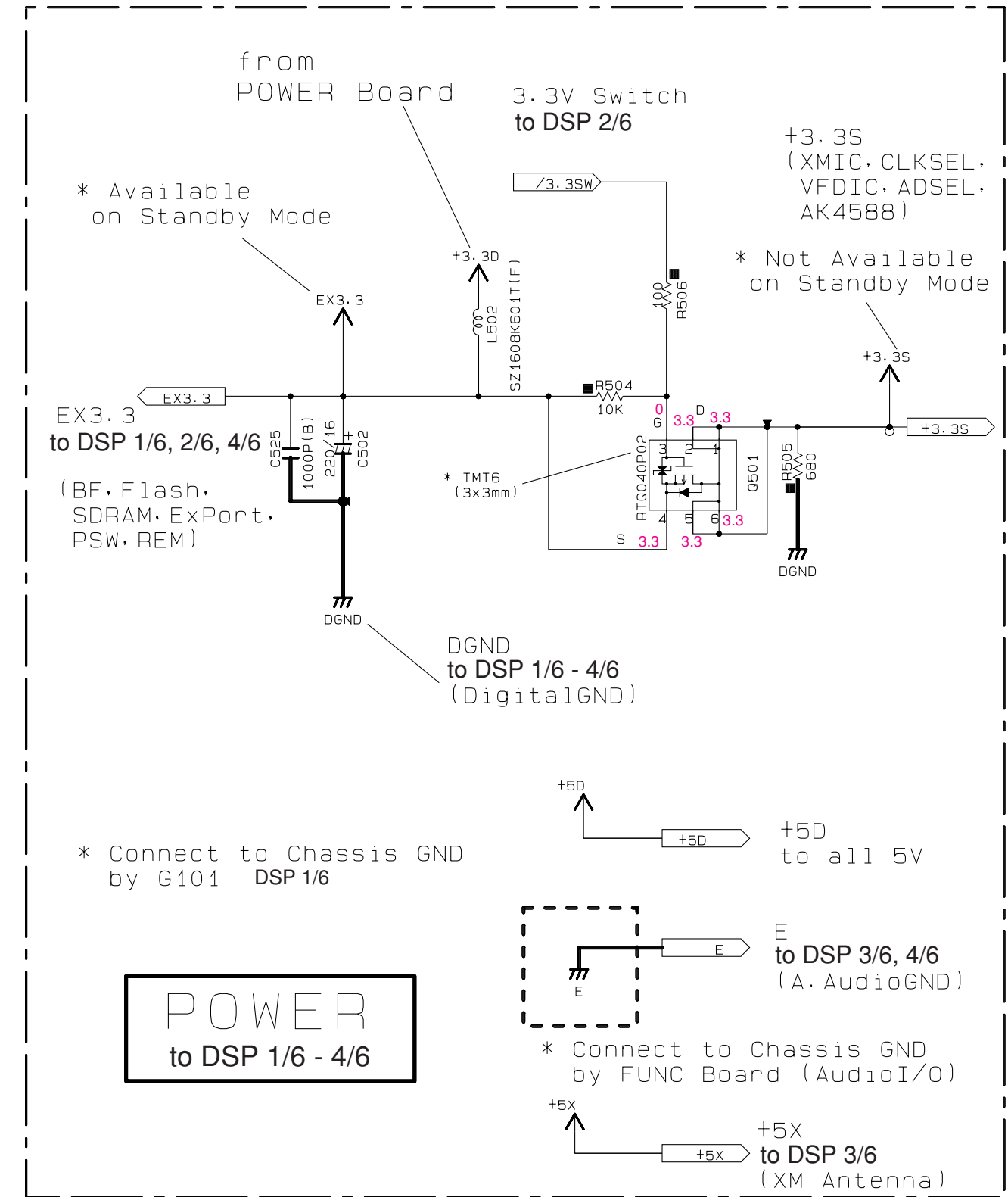
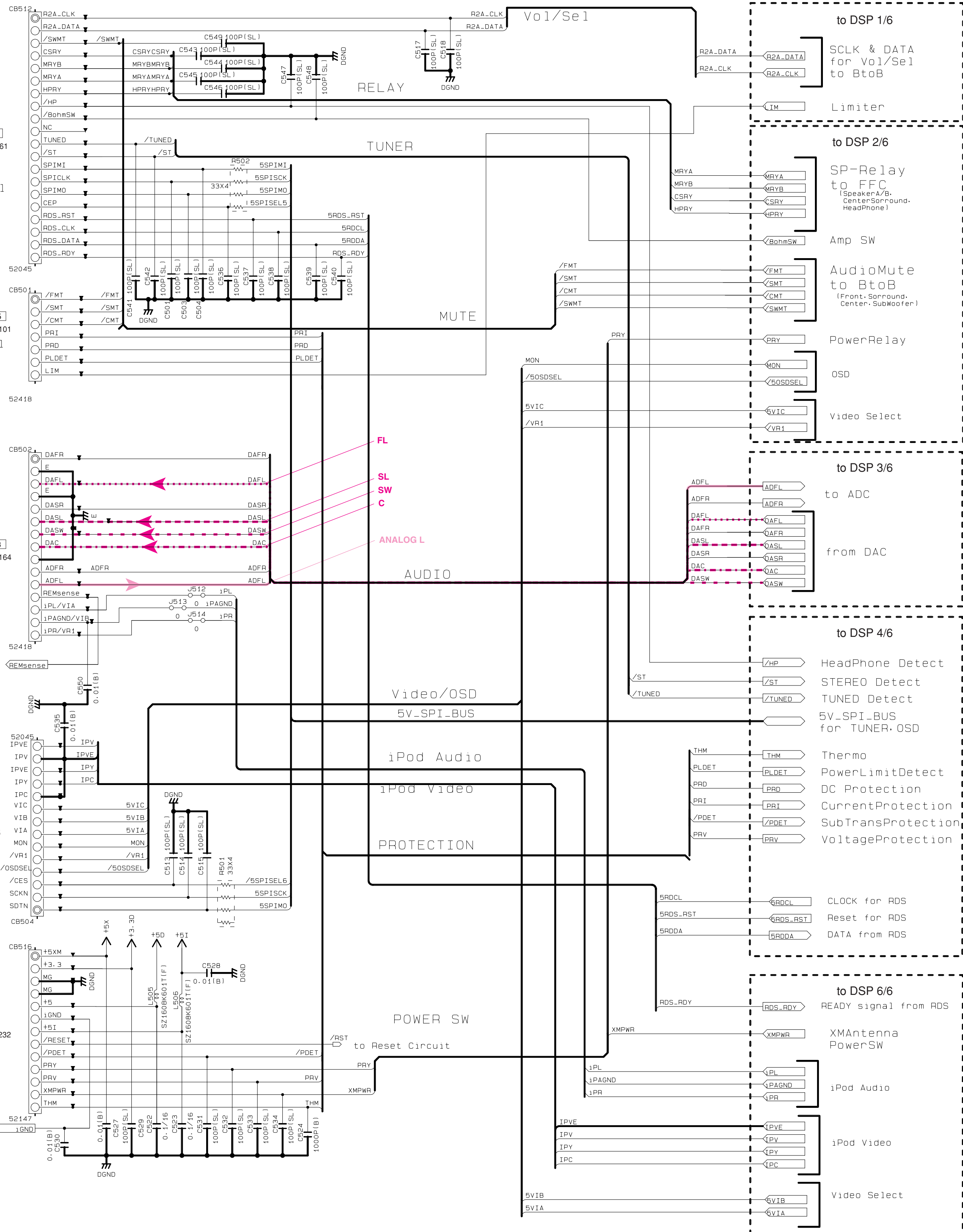
Audio
Signal
(BtoB)
Rear

to DSP 6/6

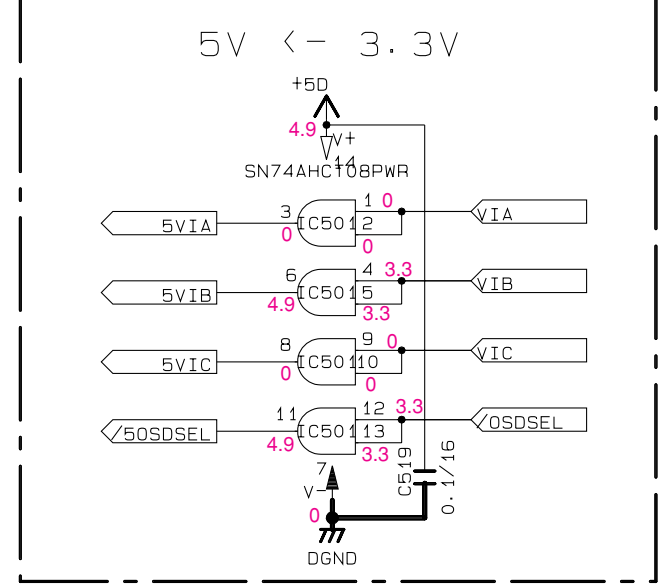
Page 79 [C2]
to VIDEO (1)_CB305

Page 76 [A7]
to OPERATION (2)_CB232

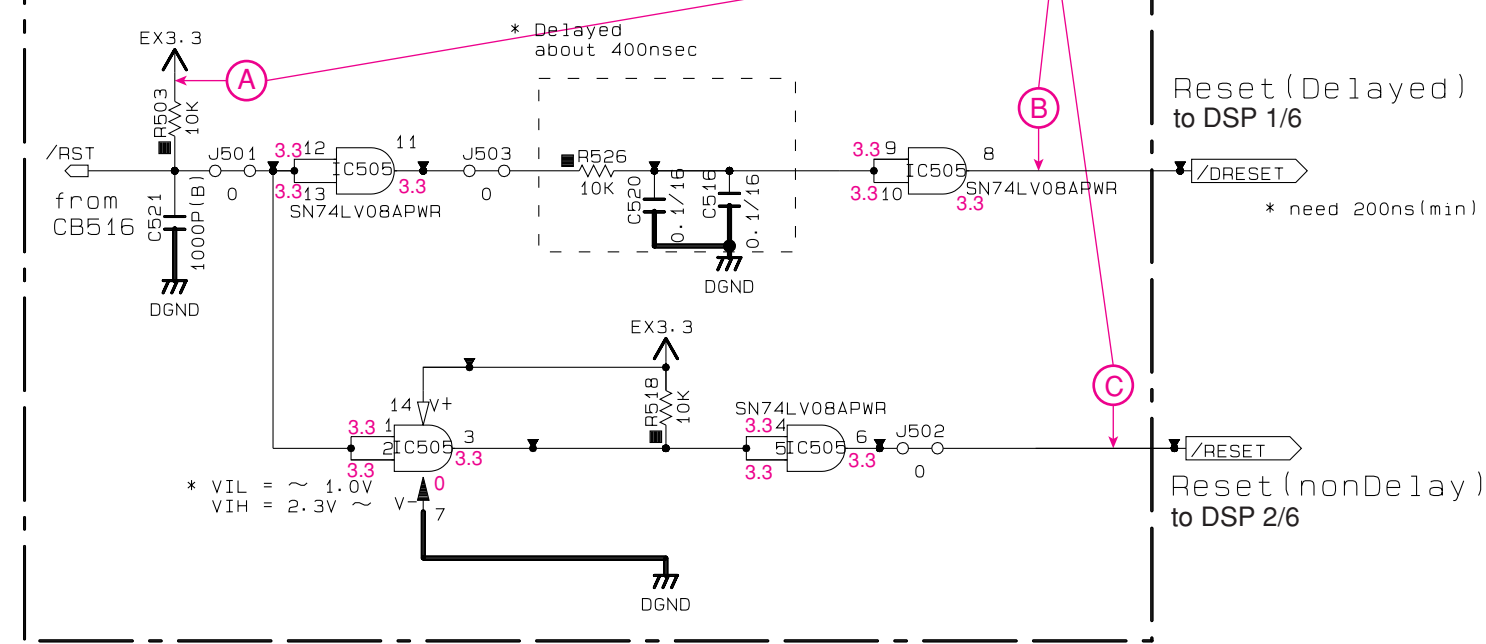
to DSP 6/6



POWER
to DSP 1/6 - 4/6



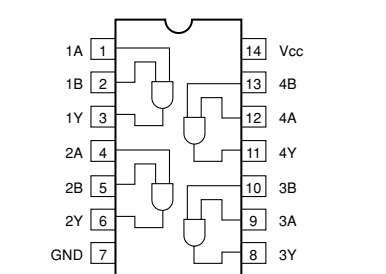
Reset Circuit



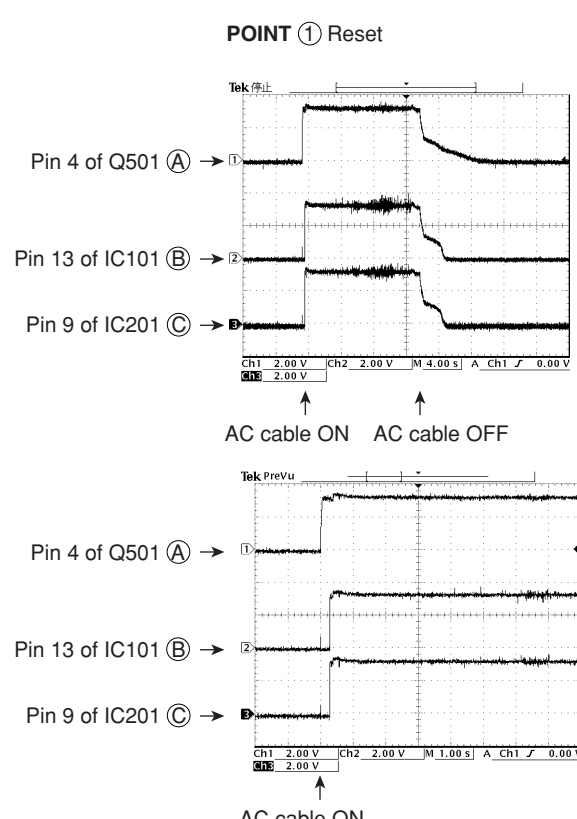
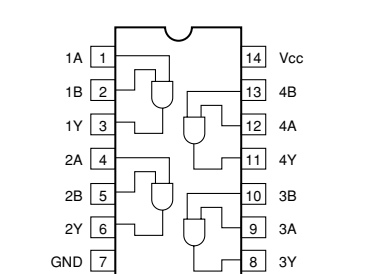
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* Schematic diagram is subject to change without notice.

● 電圧は、内部抵抗10MΩの電圧計で測定したものです。
● 三角形のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
● 本回路図は標準回路図です。改良のため予告なく変更することがございます。

IC501: SN74AHC08PWR
Quadruple 2-input positive-AND gates

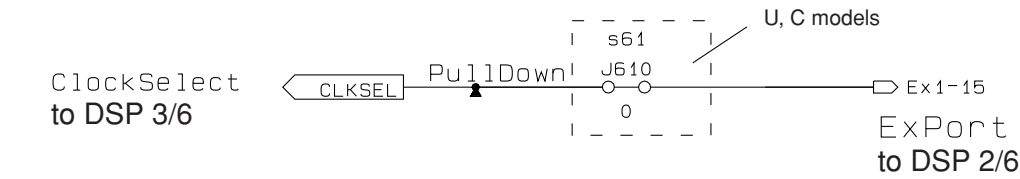
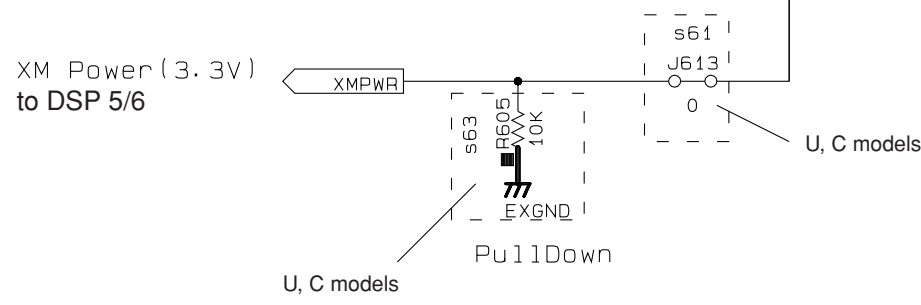
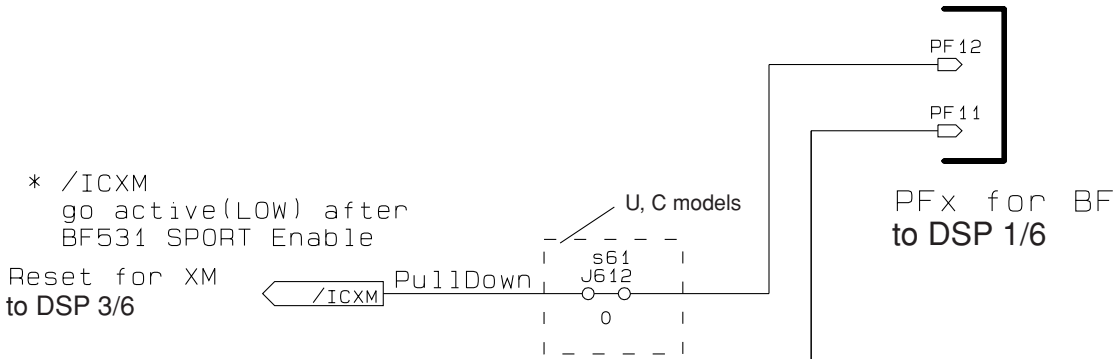
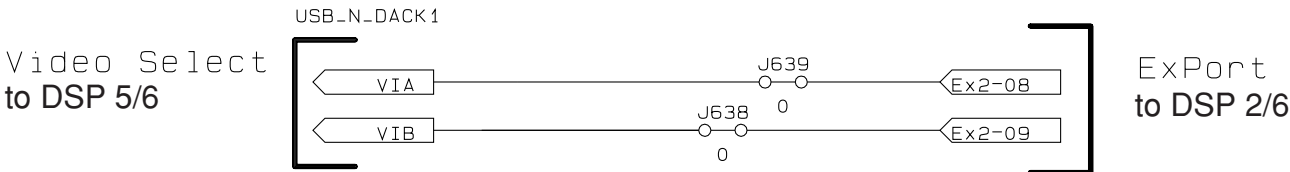
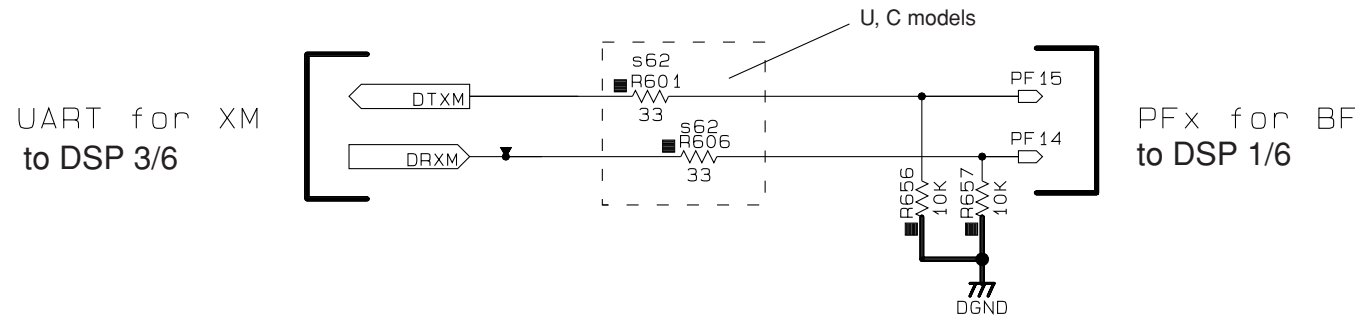


IC505: SN74LV08APWR
Quadruple 2-input positive-AND gates

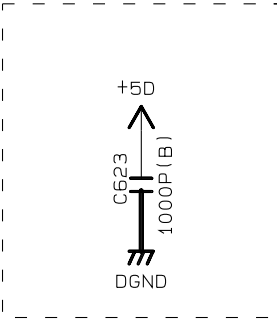


DSP 6/6

Destination
Circuit

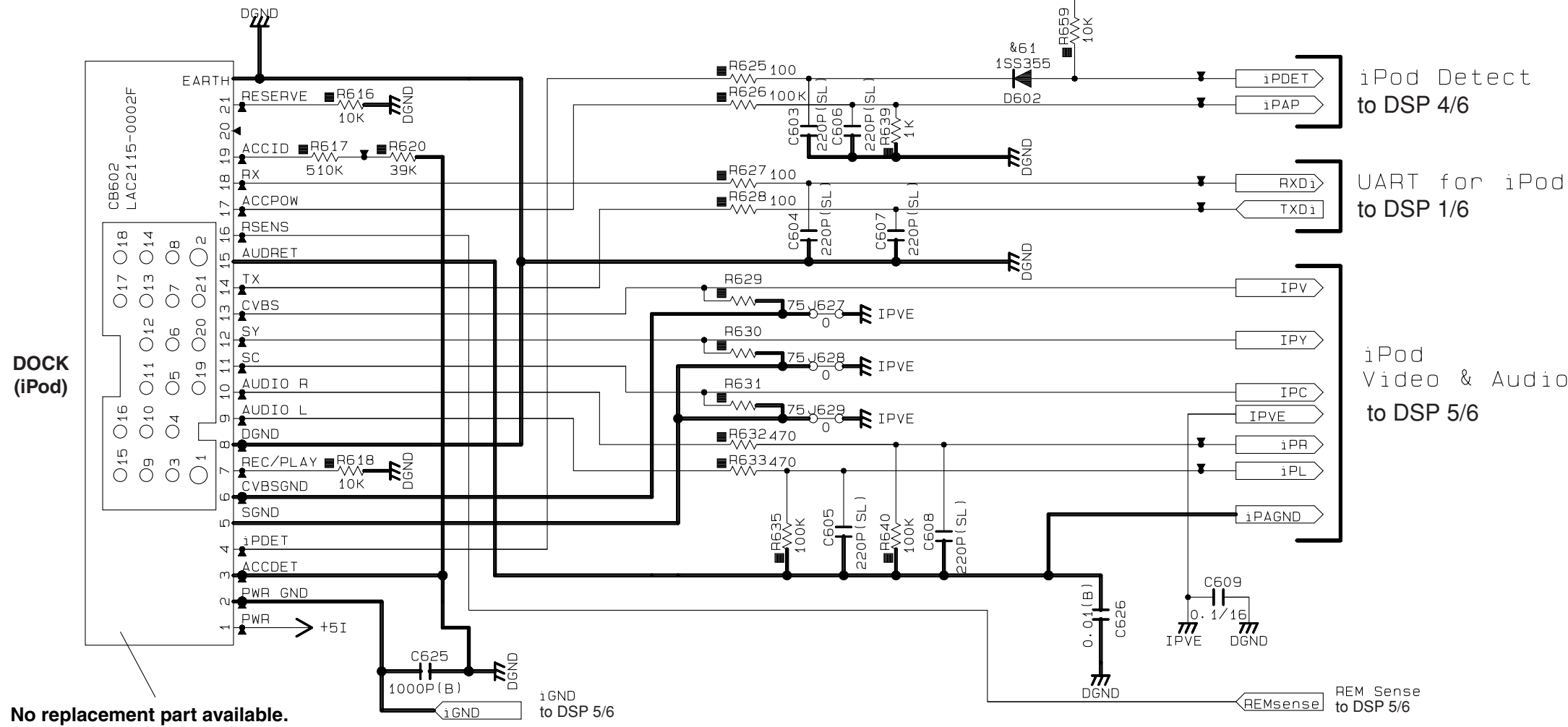


- ★ All voltages are measured with a 10MΩ/V DC electronic voltmeter.
- ★ Components having special characteristics are marked  and must be replaced with parts having specifications equal to those originally installed.
- ★ Schematic diagram is subject to change without notice.
- 電圧は、内部抵抗10MΩの電圧計で測定したものです。
- 三角形印のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
- 本回路図は標準回路図です。改良のため予告なく変更することがございます。



iPod

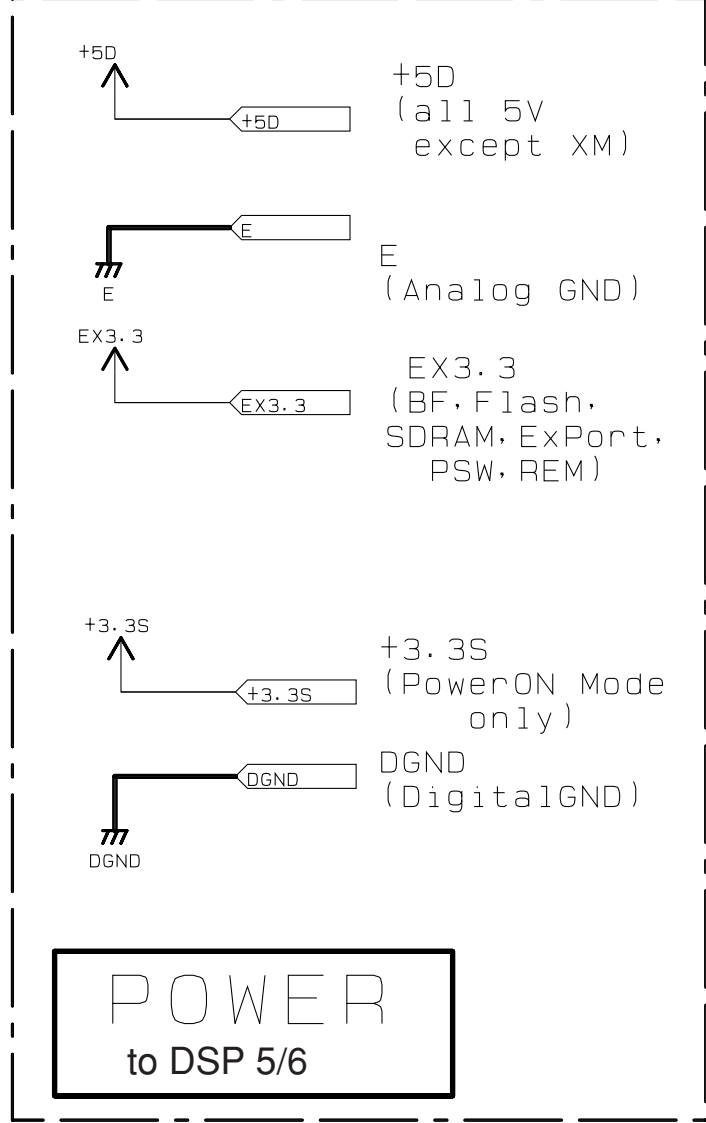
18pin:Rx for uCOM
14pin:Tx for uCOM



sXX	L0C	J	UC	R	A	K	L	
s61	J613 J610 J612	X	RD35000 0	X	X	X	X	
s62	R601 R606	X	RD35433 33	X	X	X	X	XM
s63	R605	X	RD35710 10K	X	X	X	X	

NOTICE (model)

(J)..... JAPAN
(U)..... U.S.A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN



Mark	Reference Parts Number	Parts Name
s61	D602	1SS355 MA111

CAPACITOR		
REMARKS	PARTS NAME	
NO MARK	ELECTROLYTIC CAPACITOR	H
⊗	TANTALUM CAPACITOR	
NO MARK	CERAMIC CAPACITOR	H
●	CERAMIC TUBULAR CAPACITOR	
◎	POLYESTER FILM CAPACITOR	
○	POLYSTYRENE FILM CAPACITOR	
①	MICA CAPACITOR	
Ⓟ	POLYPROPYLENE FILM CAPACITOR	
⊕	SEMICONDUCTIVE CERAMIC CAPACITOR	
Ⓢ	POLYPHENYLENE SULFIDE FILM CAPACITOR	

YPAO
RX-V461/DSP-AX461 models

MSJ-2200C-AG

UK201

R2001 1K

R2007 4.7K

C2110 1000P (B)

C2002

C2111 1000P (B)

C2005 1000P (B)

C2005 100K

C2005 10/15

C2005 222/16

D2002

R2010 10K

R2014 4.7K

W0352 1B

AZ45B0MTR-E1

IC202

IC203

IC204

IC205

C2004 470

C2004 477/6.3

C2005 477/6.3

0.01 (B)

C2001

155395

D2004

155395

D2005

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D2006

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D2007

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D2008

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D2009

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D2010

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D2162

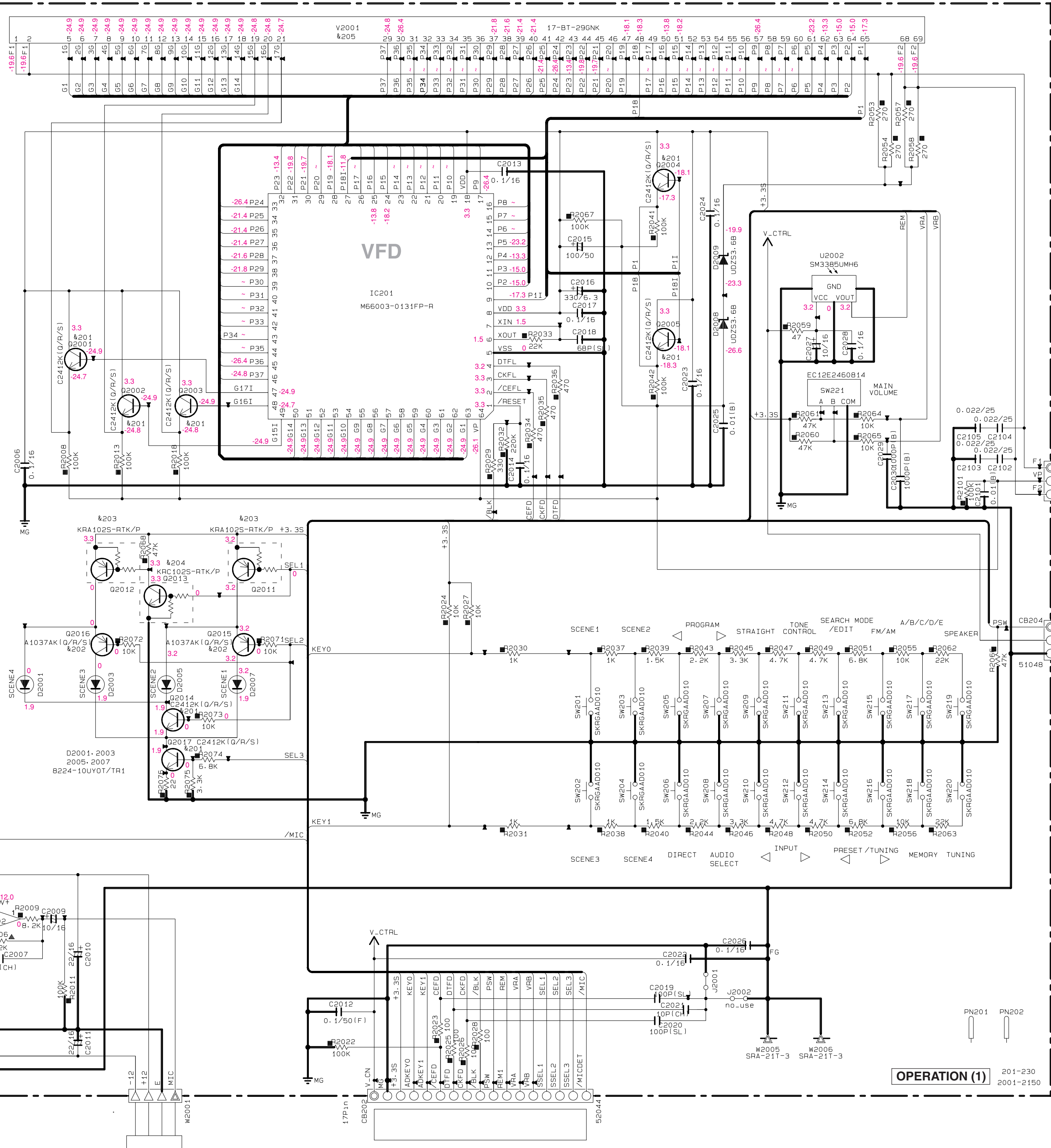
155395

D2163

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D2164

1



Mark	Reference Parts Number	Parts Name
✱201	Q2001-2005, 2014, 2017	25C2412K [Q/R/S] 25065014RL [Q/R/S]
✱202	Q2015, 2016	25A1037AK [Q/R/S] 25A1235AE [F]
✱203	Q2011, 2012	DTA114EKA KRA102S-RTK
✱204	Q2013	DTC114EKA KAC102S-RTK
✱205	V2001	17-BT-29GNK HNA-17MM04T
✱206	D2004, 2006, 2302, 2303 D2351-2354	1SS355 MA111

(J)..... JAPAN
(U)..... U. S. A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN

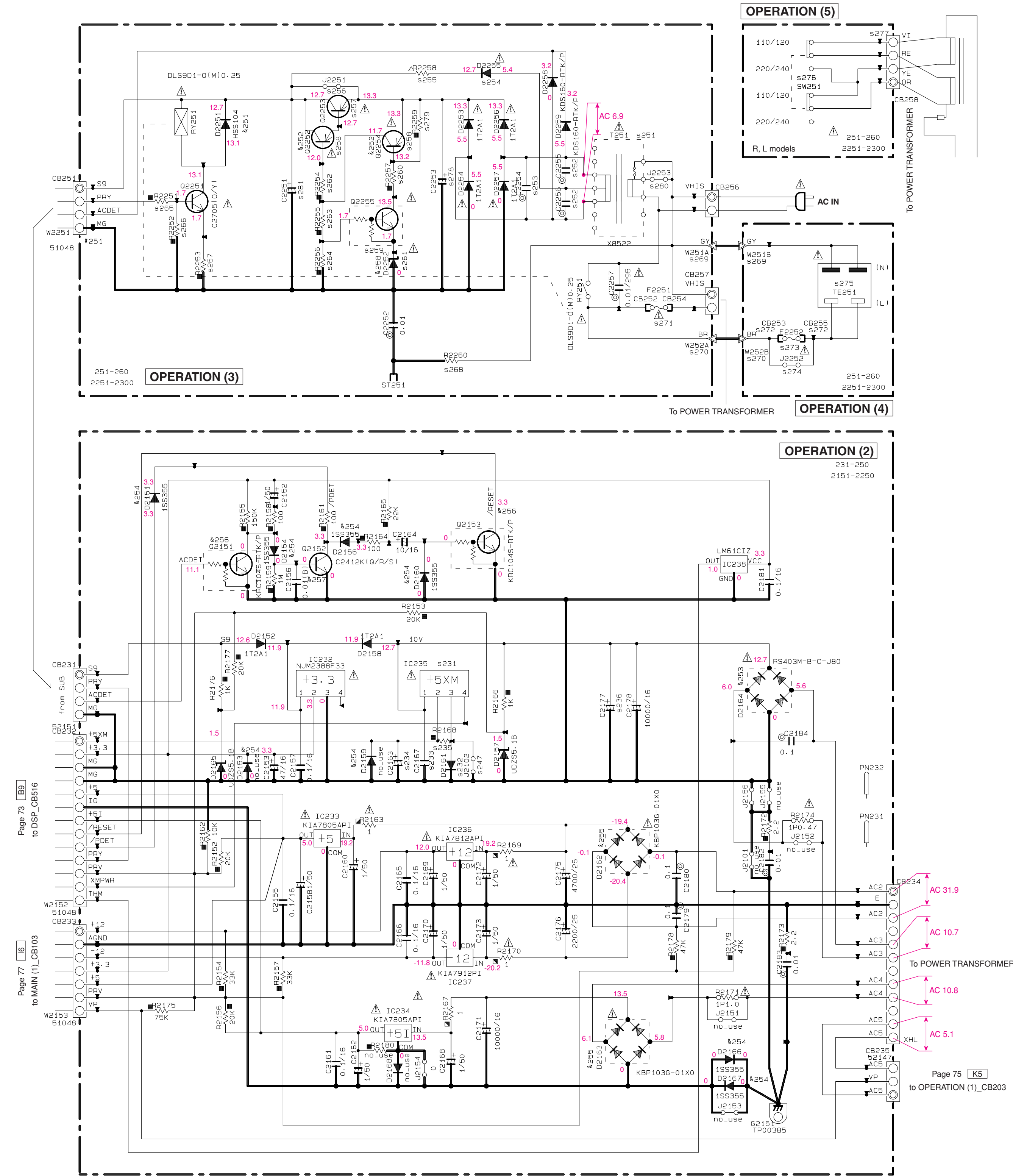
The block diagram illustrates the system architecture. Key components and their connections are as follows:

- Inputs:** CS and SDATA connect to the Serial receive circuit. XIN and XOUT connect to the Clock generator. RESET, Vcc, Vcc1, Vcc2, and Vp connect to the system power/reset lines.
- Core Processing:** The Display code RAM (8-bit x 60) and Code/ command control circuit are interconnected. The Display controller receives code select signals from the Code/ command control circuit and scale pulses from the Digit output circuit.
- Memory and Control:** The CGROM (30-bit x 16) and CGCROM (30-bit x 16) are connected to the Segment output circuit via data select signals.
- Outputs:** The Segment output circuit produces signals SEG00 through SEG34. The Digit output circuit produces signals DKG0 through DKG10.

The circuit diagram shows a differential amplifier with a current mirror load. The input stage consists of a differential pair of NMOS transistors. The gates of these transistors are connected to a common-mode feedback network, which includes a PMOS transistor and a resistor connected to V_{CC} . The sources of the input transistors are connected to a common source node, which is biased by a PMOS transistor and a resistor connected to V_{CC} . The drains of the input transistors are connected to a current mirror load, which consists of two PMOS transistors. The output of the amplifier is taken from the drain of one of the input transistors, which is also connected to a diode-connected transistor (a PMOS transistor with its gate and drain connected) to extend the output swing. The output is also connected to a resistor and a diode to ground, which is biased by a PMOS transistor and a resistor connected to V_{CC} .

75

OPERATION 2/2



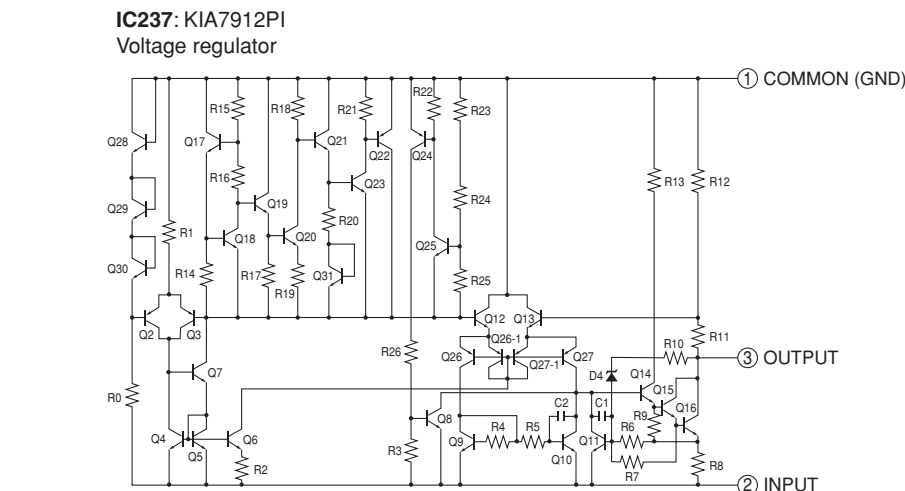
- ★ All voltages are measured with a 10M Ω /V DC electronic voltmeter.
- ★ Components having special characteristics are marked Δ , and must be replaced with parts having specifications equal to those originally installed.
- ★ Schematic diagram is subject to change without notice.
- 電圧は、内部抵抗10M Ω の電圧計で測定したものです。
- Δ 印のある部品は、安全性確保部品を示しています。部品の交換が必要場合、パーツリストに記載されている部品を使用してください。
- 本回路図は標準回路図です。改良のため予告なく変更することがございます。

RESISTOR		
REMARKS	PARTS NAME	
NO MARK	CARBON FILM RESISTOR (P=5)	
$\square$	CARBON FILM RESISTOR (P=10)	
$\triangle$	METAL OXIDE FILM RESISTOR	
Δ	METAL FILM RESISTOR	
$\boxtimes$	METAL PLATE RESISTOR	
$\boxplus$	FIRE PROOF CARBON FILM RESISTOR	
$\square$	CEMENT MOLDED RESISTOR	
---	SEMI VARIABLE RESISTOR	
---	CHIP RESISTOR	

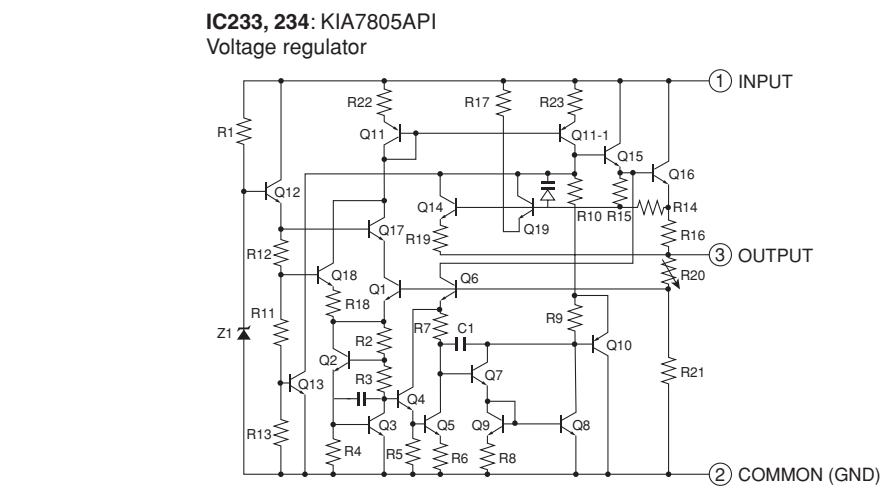
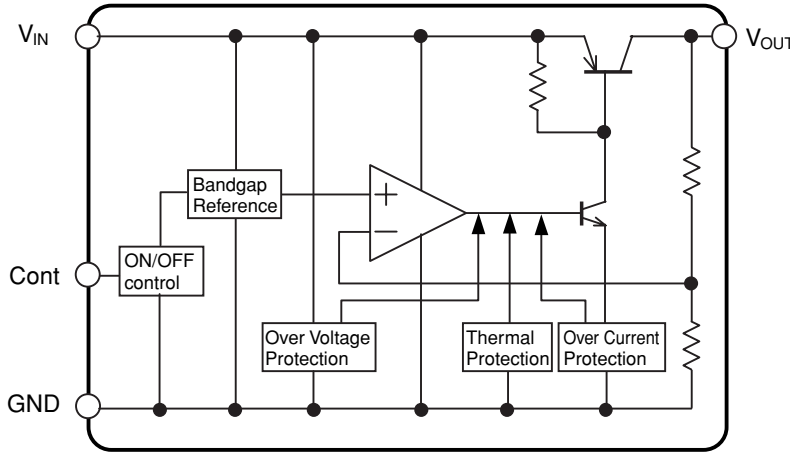
CAPACITOR		
REMARKS	PARTS NAME	
NO MARK	ELECTROLYTIC CAPACITOR	
$\boxtimes$	TANTALUM CAPACITOR	
NO MARK	CERAMIC CAPACITOR	
$\bullet$	CERAMIC TUBULAR CAPACITOR	
$\odot$	POLYESTER FILM CAPACITOR	
$\circ$	POLYSTYRENE FILM CAPACITOR	
①	MICA CAPACITOR	
②	POLYPROPYLENE FILM CAPACITOR	
③	SEMICONDUCTIVE CERAMIC CAPACITOR	

Destination Part List							
sXX	LOC	J	UC	R	K	A	L
s231	IC235	X	X6143A0 NUM2388F05	X	X	X	X
s232	D2161	X	V237660 RB500V-40	X	X	X	X
s233	C2167	X	U513510 0.1/16	X	X	X	X
s234	C2163	X	UR83747 47/16	X	X	X	X
s235	R2168	X	RD35610 1K	X	X	X	X
s236	C2177	X	UR73A10 10000/16	X	X	X	X
s247	J2102	VN50000	X	VN50000	VN50000	VN50000	VN50000
s251	T251	XB520A0 XB520	XB521A0 XB521	XB522A0 XB522	XB523A0 XB523	XB523A0 XB522	XB522A0 XB522
s252	C2256 C2255	X	X	X	X	X	X
s253	C2254	X	X	X	X	X	X
s254	D2255	X	X	X	X	X	X
s255	R2258	X	X	X	X	X	X
s256	J2251	VN50000	VN50000	X	VN50000	VN50000	X
s257	Q2253	X	X	X	X	X	X
s258	Q2252 Q2254	X	X	X	X	X	X
s259	Q2255	X	X	X	X	X	X
s260	R2257	X	X	X	X	X	X
s261	D2252	X	X	X	X	X	X
s262	R2254	X	X	X	X	X	X
s263	R2255	X	X	X	X	X	X
s264	R2256	X	X	X	X	X	X
s265	R2251	X	X	X	X	X	X
s266	R2252	X	X	X	X	X	X
s267	R2253	X	X	X	X	X	X
s268	R2260	X	X	X	X	X	X
s269	W251B W251A	MH18010	MH18010	X	MH18010	MH18010	MH18010
s270	W252B W252A	MH11012	MH11012	X	MH11012	MH11012	MH11012
s271	F2251	WB22120 6.00A125V	WB22120 6.00A125V	WB22120 6.00A125V	VB07170 3.15A250V	VB07170 3.15A250V	WB22120 6.00A125V
s272	CB253 CB255	X	X	X	X	X	X
s273	F2252	X	X	X	X	X	X
s274	J2252	VN50000	X	X	X	X	X
s275	TE251	X	X	X	X	X	X
s276	SW251	X	X	X	X	X	X
s277	CB258	X	X	X	X	X	X
s278	C2253	X	X	X	X	X	X
s279	R2259	X	X	X	X	X	X
s280	J2253	VN50000	VN50000	X	VN50000	VN50000	X
s281	C2251	UR74922 2200/25	UR74922 2200/25	UR75922 2200/35	UR74922 2200/25	UR74922 2200/25	UR75922 2200/35

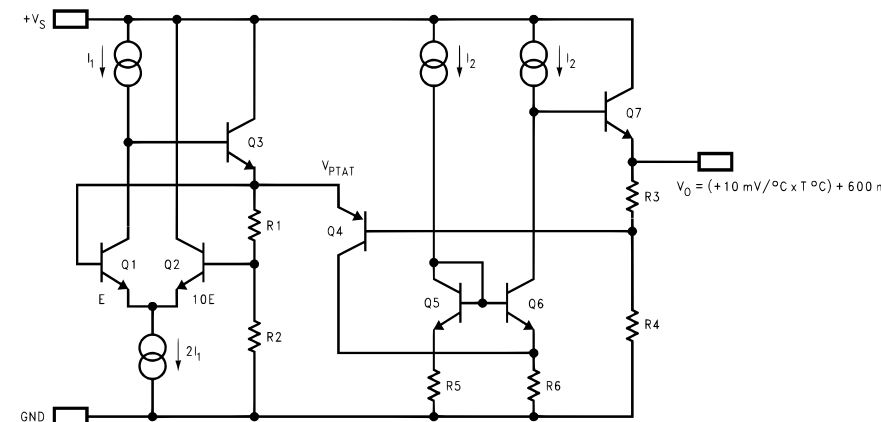
NOTICE (model)
(J)..... JAPAN
(U)..... U. S. A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN



IC235: NUM2388F05
Low dropout voltage regulator with ON/OFF control



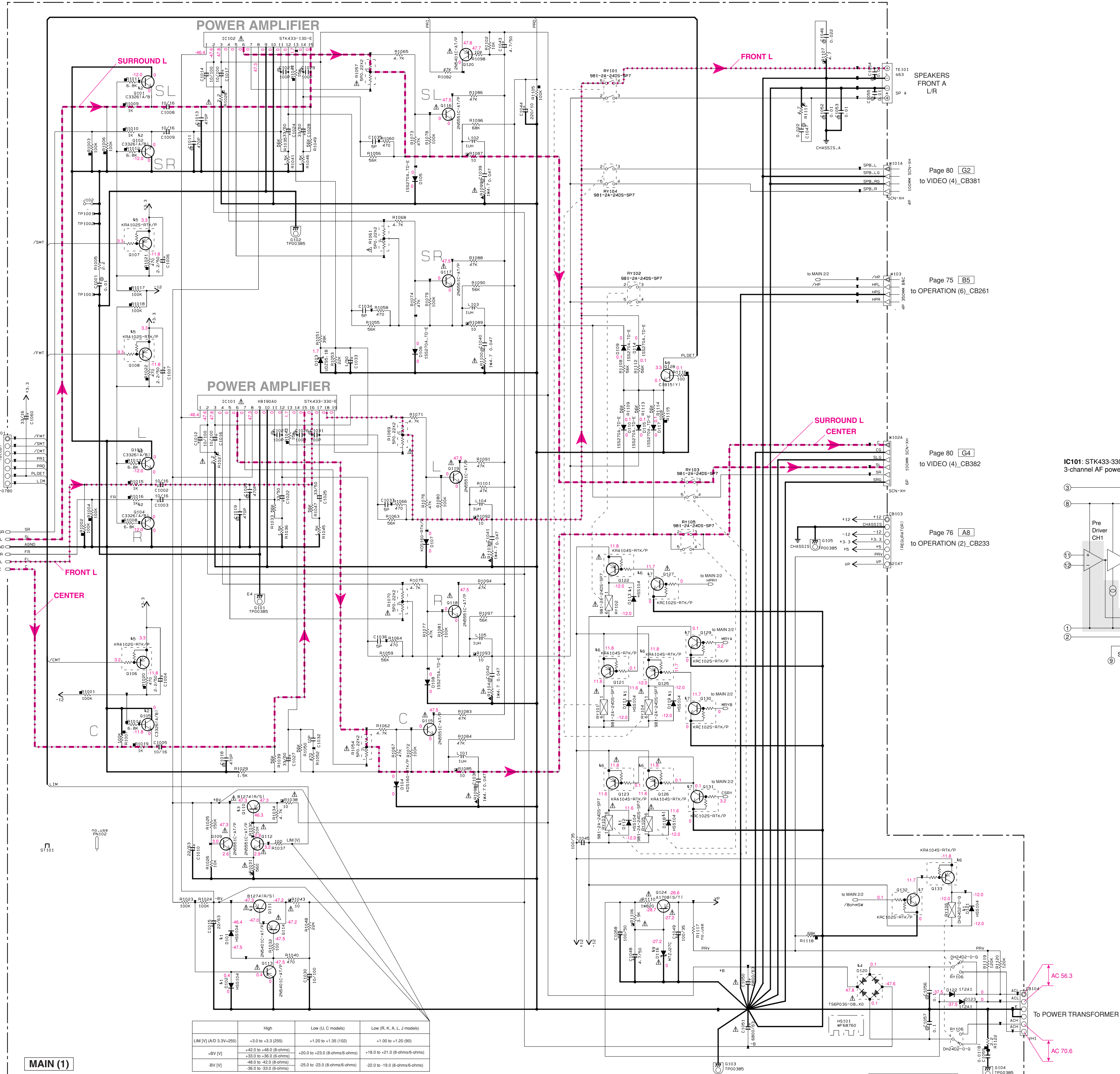
IC233: LM61CI2
Temperature sensor



Interchangeable Parts at Manufacture-Stage

Mark	Reference Parts Number	Parts Name
s251	D2251	HSS104 1SS133 1SS176
s252	Q2252, 2254	2SA1015(Y) KTA1266(Y)
s253	D2164	RS403M-B-C-J80 TS4803G-07
s254	D2151, 2154, 2156 D2160, 2166, 2167	1SS355 MA111
s255	D2162, 2163	KBP103G RS103
s256	Q2151, 2153	DTC144EKA KRC104S-RTK
s257	Q2152	2SC2412K(Q/R/S) 2SD601ARL/AQL(Q/R/S)
s258	D2252	MTZJ4, 7A GDZJ4, 7A

MAIN 1/2



Destination Part List				
SXX	LOC	JRA	UC	KL
563	TE101	KJ26940	KJ26940	KJ26940
		JB-K08211V01-02	JB-K08211V01-02	JB-K08211V01-01

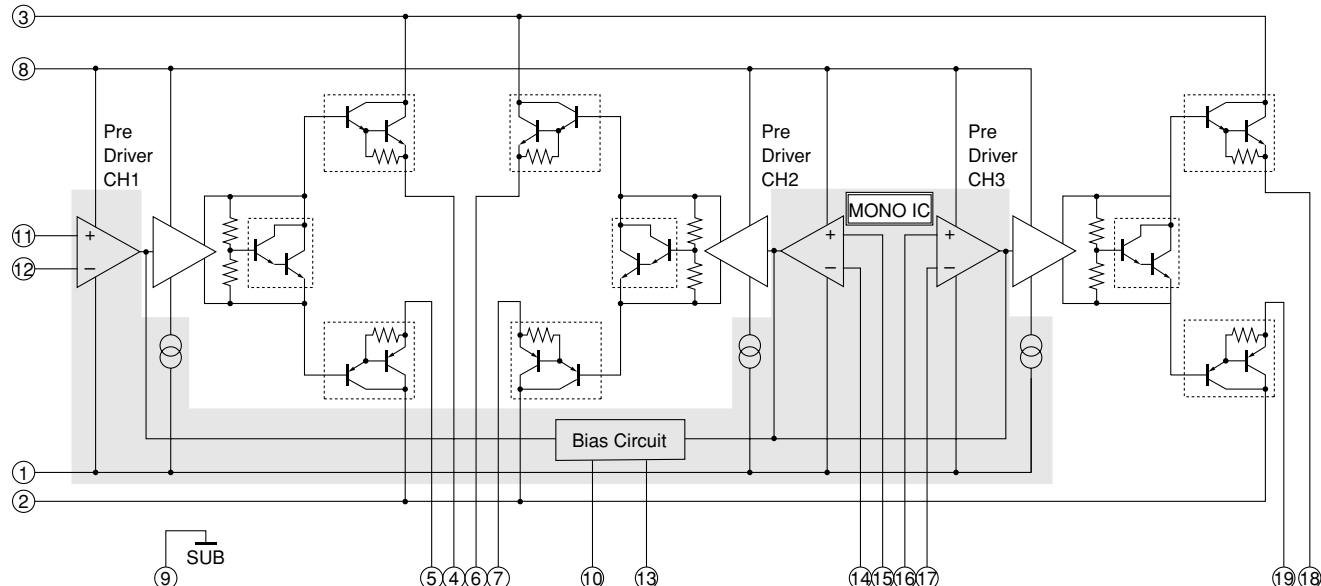
REMARKS	PARTS NAME
NO MARK	ELECTROLYTIC CAPACITOR
⊗	TANTALUM CAPACITOR
NO MARK	CERAMIC CAPACITOR
⊙	POLYESTER FILM CAPACITOR
○	POLYSTYRENE FILM CAPACITOR
⊖	MICA CAPACITOR
⊕	POLYPROPYLENE FILM CAPACITOR
⊙	SEMICONDUCTIVE CERAMIC CAPACITOR
⊙	POLYPHENYLENE SULFIDE FILM CAPACITOR

REMARKS	PARTS NAME
NO MARK	CARBON FILM RESISTOR (P=5)
⊗	CARBON FILM RESISTOR (P=10)
⊕	METAL OXIDE FILM RESISTOR
⊖	METAL FILM RESISTOR
⊙	METAL PLATE RESISTOR
⊕	FIRE PROOF CARBON FILM RESISTOR
⊖	CEMENT MOLDED RESISTOR
⊙	SEMI VARIABLE RESISTOR
⊙	CHIP RESISTOR

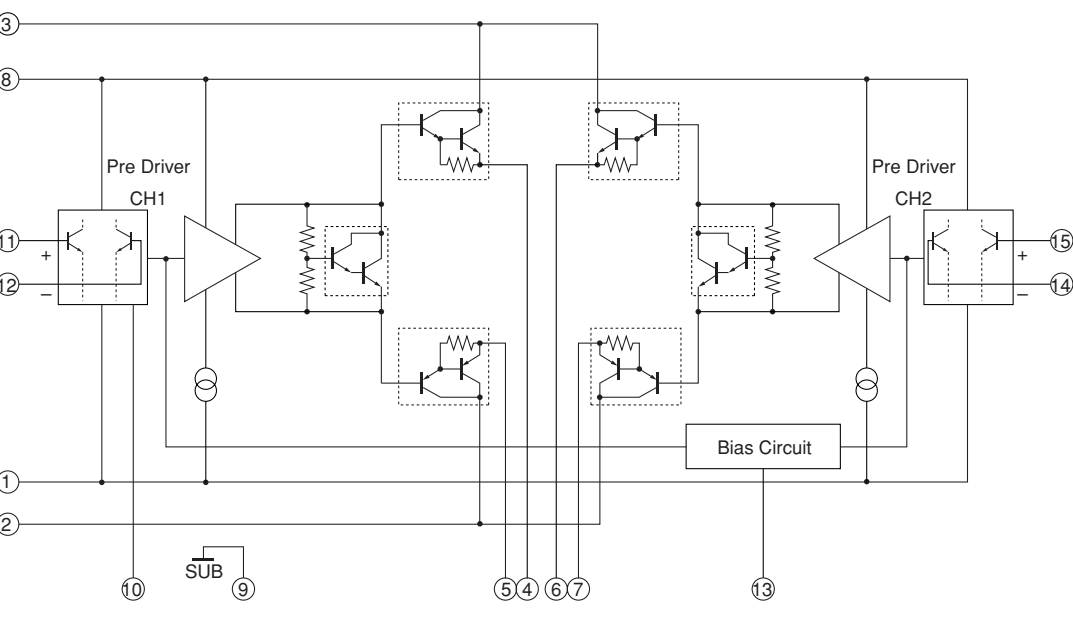
NOTICE (model)
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(U)..... U.S.A.
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN

Interchangeable Parts at Manufacture-Stage		
Mark	Reference Parts Number	Parts Name
41	D101-102-111-113	H55104
	118-119-121	155176
42	G101-105	25C33261A/B1
		25C33261B1
		25C938A1A/B1
		25D19381F1S/7
		25D2704K
43	G110-111	25B12741R/S1
		25B15651E/F1
		K7A1046-Y-U/P
44	D120	T56P03G-08-X0
		R5603M-B-C-U80
45	G106-108	KRA104S-RTK/P
		DTA114EKA
46	G121-123-125-126-133	KRA104S-RTK/P
		DTA144EKA
47	G127-129-132	KRC102-RTK/P
		DTC114EKA
48	G128	25C18151V1
		KT3319B-Y-AT
49	D116	MTZJ27C
		60ZJ27C

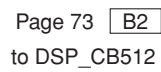
IC101: STK433-330-E
3-channel AF power amplifier, stand-by circuit built-in



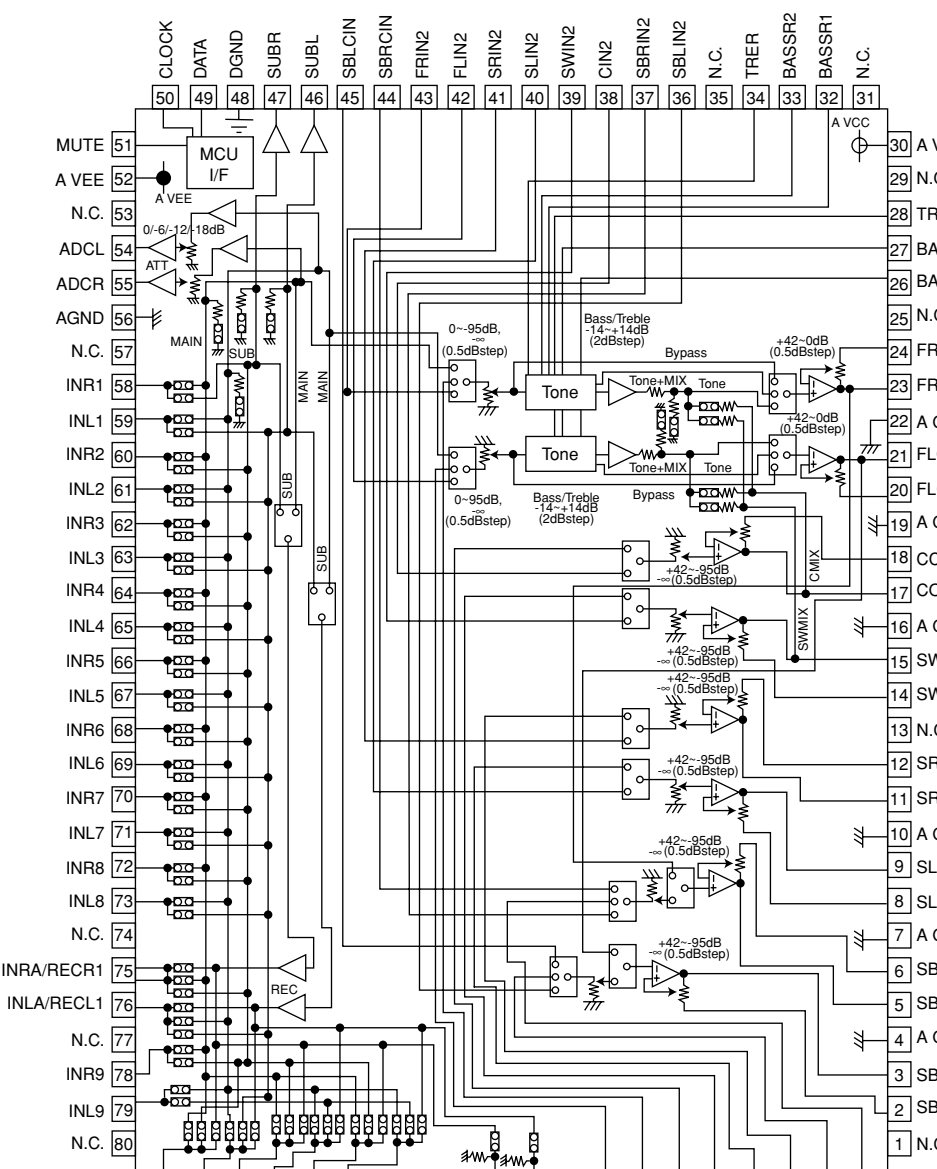
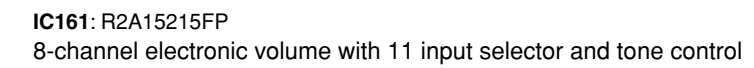
IC102: STK433-130-E
2-channel AF power amplifier, stand-by circuit built-in



- ★ All voltages are measured with a 10MΩ/V DC electronic voltmeter.
- ★ Components having special characteristics are marked and must be replaced with parts having specifications equal to those originally installed.
- ★ Schematic diagram is subject to change without notice.
- 電圧は、内部抵抗10MΩの電圧計で測定したものです。
- 印刷のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
- 本回路図は標準回路図です。改良のため予告なく変更することがございます。

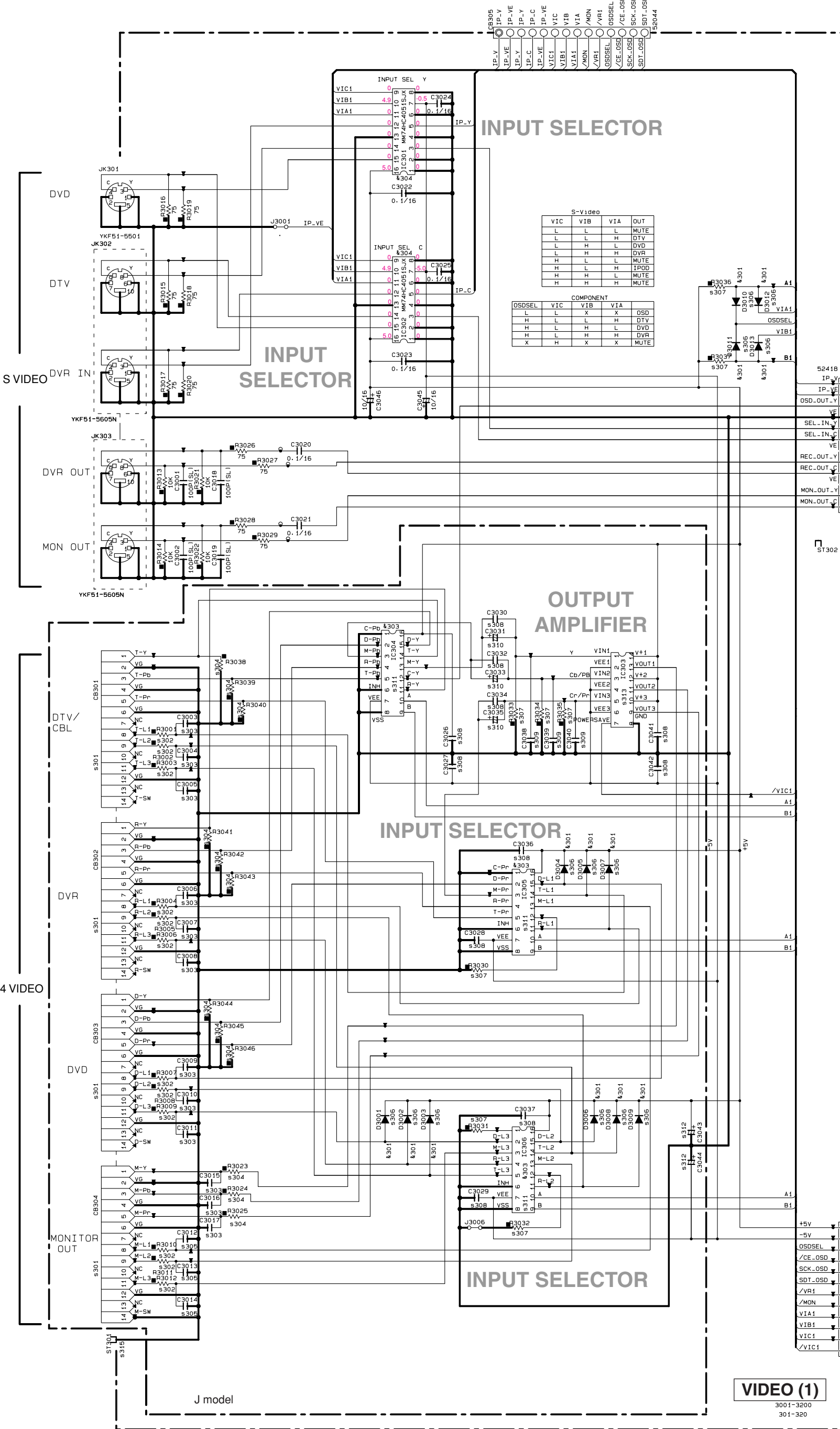
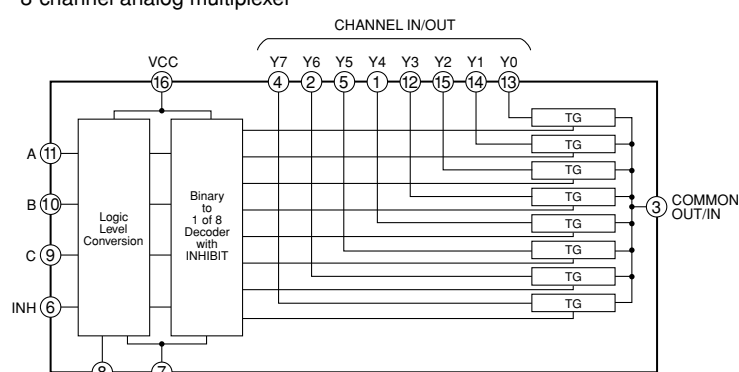
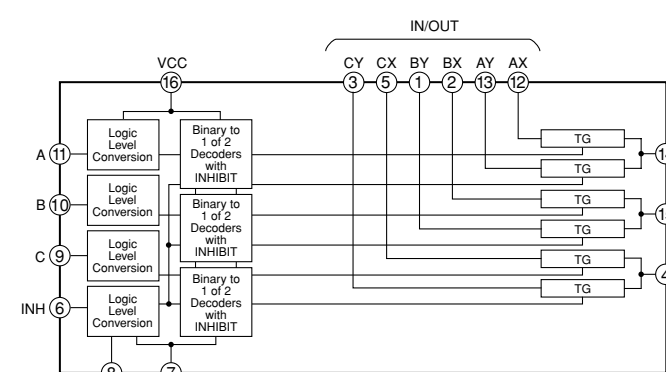
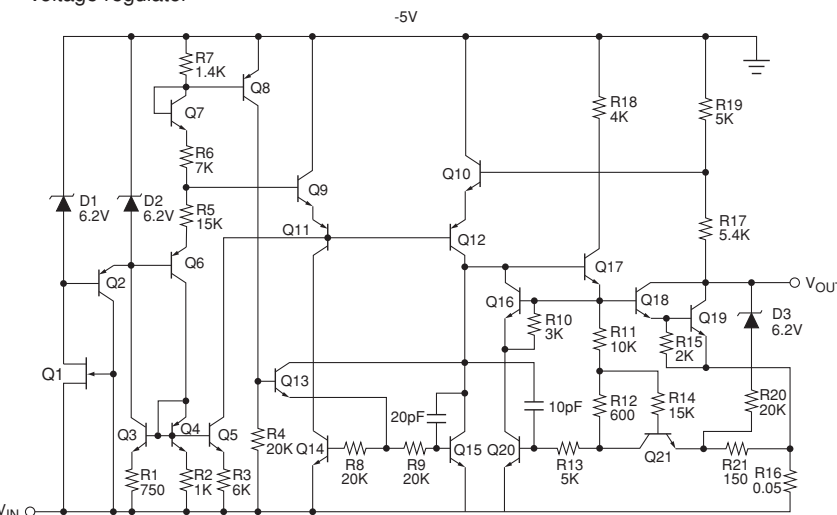
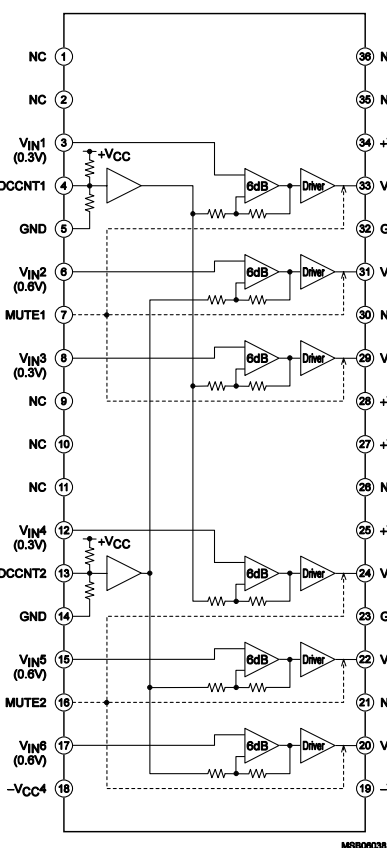
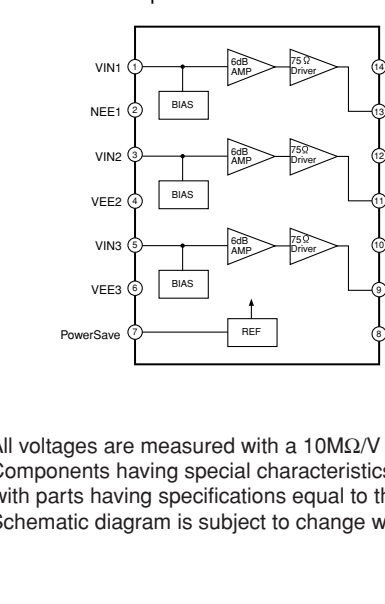


RESISTOR	
REMARKS	PARTS NAME
NO MARK	CARBON FILM RESISTOR (P=5)
☒	CARBON FILM RESISTOR (P=10)
☐	METAL OXIDE FILM RESISTOR
☐	METAL FILM RESISTOR
☐	METAL PLATE RESISTOR
☒	FIRE PROOF CARBON FILM RESISTOR
☐	CEMENT MOLDED RESISTOR
☒	SEMI VARIABLE RESISTOR
☐	CHIP RESISTOR



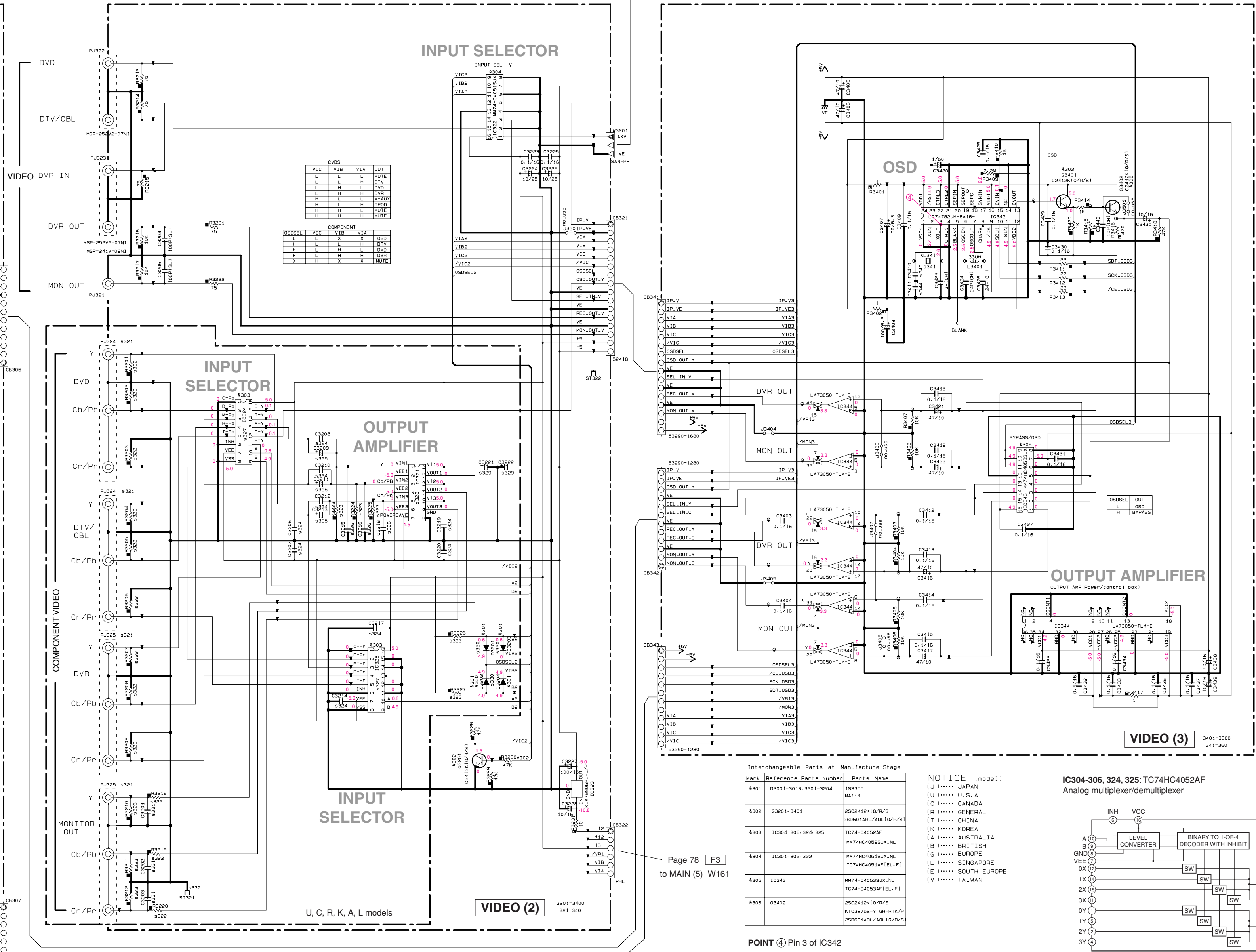
- ★ All voltages are measured with a 10M Ω /V DC electronic voltmeter.
- ★ Components having special characteristics are marked **!**, and must be replaced with parts having specifications equal to those originally installed.
- ★ Schematic diagram is subject to change without notice.
- ★ 電圧は、内部抵抗10M Ω の電圧計で測定したものです。
- ★ **!**の印のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
- ★ 本回路図は標準回路図です。改良のため予告なく変更することがございます。

VIDEO 1/2

Page 73 [B7]
to DSP_CB504IC301, 302, 322: MM74HC4051SJX
8-channel analog multiplexerIC343: MM74HC4053SJX
Triple 2-channel analog multiplexerIC323: KIA79M05PI
Voltage regulatorIC344: LA73050-TLM-E
6-channel 75-ohm video driverIC303, 321: NJM2581M
Video amplifier

* All voltages are measured with a 10MΩ/V DC electronic voltmeter.
 * Components having special characteristics are marked with a triangle (▲) and must be replaced with parts having specifications equal to those originally installed.
 * Schematic diagram is subject to change without notice.

● 電圧は、内部抵抗10MΩの電圧計で測定したものです。
 ● 印のある部品は、安全性能部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
 ● 本回路図は標準回路図です。改良のため予告なく変更することがございます。

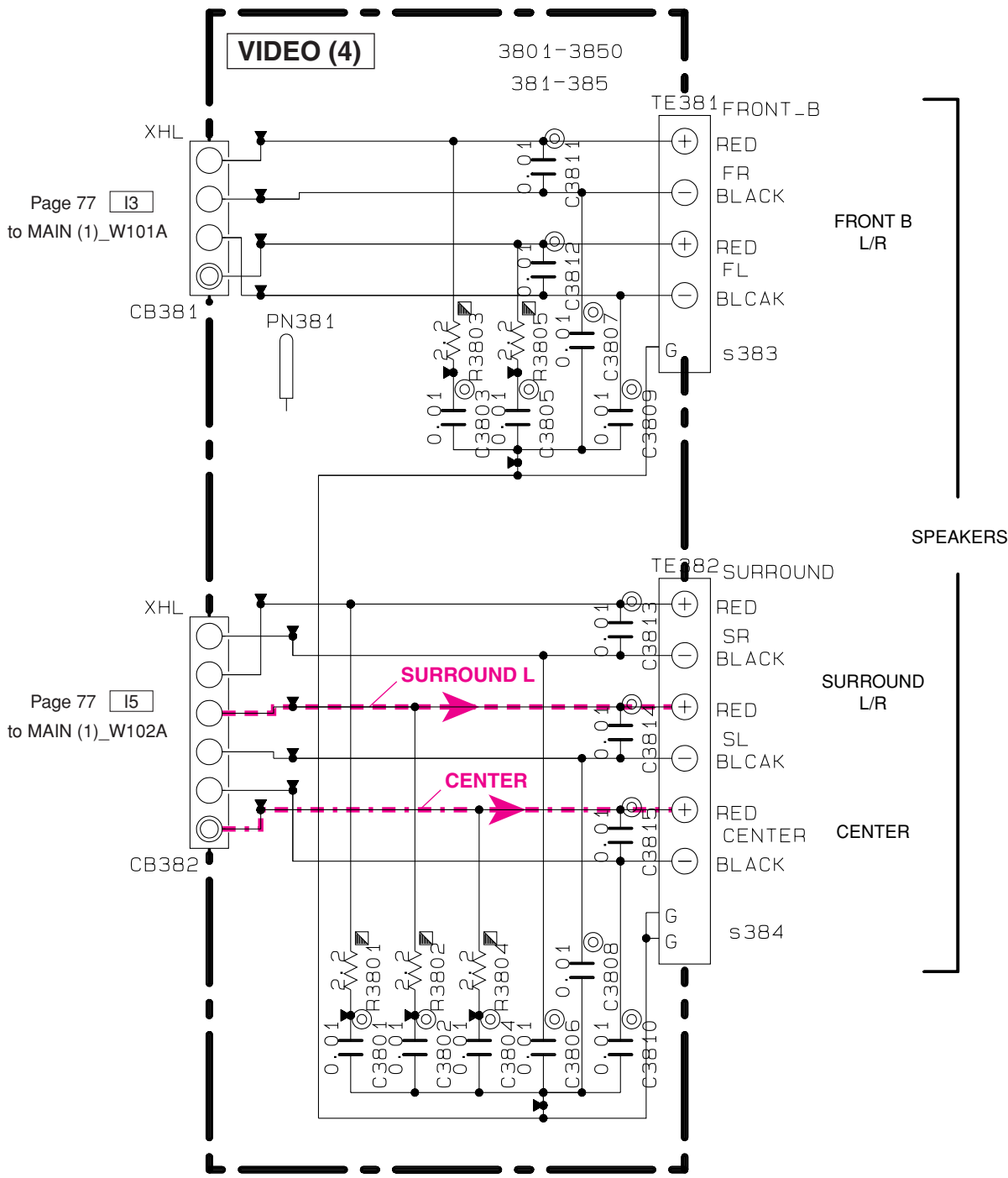
Page 78 [K4]
to MAIN (4)_CB193

VSS1	1	24	VDD1
XtalIn	2	23	RST
XtalOut	3	22	CTRL3
CTAL1	4	21	CTRL2
BLANK	5	20	SEPN
OSCIN	6	19	SEPOUT
OSCOUT	7	18	SEPC
CHARA	8	17	SYN
CS	9	16	VDD1
SCLK	10	15	CVN
SIN	11	14	NC
VDD2	12	13	CVOUT

VIDEO 2/2

Destinatin Part List

sXX	LOC	J	UCR	A	K	L
s301	CB301-CB304	WD39840 YKF45-3011	X	X	X	X
s302	R3001-R3012	RD35510 100	X	X	X	X
s303	C3003-C3011 C3015-C3017	US06080 8PCH	X	X	X	X
s304	R3023-R3025 R3038-R3046	RD35475 75	X	X	X	X
s305	C3012-C3014	US06410 0.01	X	X	X	X
s306	D3001-D3013	VT33290 1SS355	X	X	X	X
s307	R3030-R3037	RD35710 10K	X	X	X	X
s308	C3026-C3030 C3032, C3034 C3036, C3037 C3041, C3042	US13510 0.1/16	X	X	X	X
s309	C3038-C3040	US06122 22PCH	X	X	X	X
s310	C3031, C3033 C3035	UR83747 47/16	X	X	X	X
s311	IC304-IC306	XS790A0 TC74HC4052AF	X	X	X	X
s312	C3043, C3044	UR83710 10/16	X	X	X	X
s313	IC303	X2904A0 NJM2581M(TE2)	X	X	X	X
s315	ST301	X	WA78960	WA78960	WA78960	WA78960
s321	PJ324, PJ325	X	WG50510 MSD-276V-05N1-L	WG50510 MSD-276V-05N1-L	WG50510 MSD-276V-05N1-L	WG50510 MSD-276V-05N1-L
s322	R3201-R3209 R3218-R3220	X	RD35475 75	RD35475 75	RD35475 75	RD35475 75
s323	R3210-R3212 R3223-R3227	X	RD35710 10K	RD35710 10K	RD35710 10K	RD35710 10K
s324	C3206-C3208 C3210, C3212 C3214, C3217 C3219, C3220	X	US13510 0.1/16	US13510 0.1/16	US13510 0.1/16	US13510 0.1/16
s325	C3209, C3211 C3213	X	UR83747 47/16	UR83747 47/16	UR83747 47/16	UR83747 47/16
s326	C3215, C3216 C3218	X	US06122 22PCH	US06122 22PCH	US06122 22PCH	US06122 22PCH
s327	IC324, IC325	X	XS790A0 TC74HC4052AF	XS790A0 TC74HC4052AF	XS790A0 TC74HC4052AF	XS790A0 TC74HC4052AF
s328	IC321	X	X2904A0 NJM2581M(TE2)	X2904A0 NJM2581M(TE2)	X2904A0 NJM2581M(TE2)	X2904A0 NJM2581M(TE2)
s329	C3221, C3222	X	UR83710 10/16	UR83710 10/16	UR83710 10/16	UR83710 10/16
s330	D3201-D3204	X	VT33290 1SS355	VT33290 1SS355	VT33290 1SS355	VT33290 1SS355
s331	C3201-C3203	X	US06080 8PCH	US06080 8PCH	US06080 8PCH	US06080 8PCH
s332	ST321	WA78960 -	X	X	X	X
s341	XL341	VV94980 14.31818MHZ	VV94980 14.31818MHZ	WK19610 17.734475MHZ	VV94980 14.31818MHZ	WK19610 17.734475MHZ
s3B3	TE3B1	WJ26540 JB-405ET(V0)-02	WJ26540 JB-405ET(V0)-02	WJ26540 JB-405ET(V0)-02	WJ26550 JB-40BA(V0)-01	WJ26550 JB-40BA(V0)-01
s3B4	TE3B2	WJ26560 JB-602AT(V0)-02	WJ26560 JB-602AT(V0)-02	WJ26560 JB-602AT(V0)-02	WJ46410 JB-606A-01	WJ46410 JB-606A-01
s343	C3410	US06122 22P	US06122 22P	US06050 5P	US06122 22P	US06050 5P
s344	C3411	US06133 33P	US06133 33P	US06050 5P	US06133 33P	US06050 5P



NOTICE (model)

(J)..... JAPAN
(U)..... U.S.A
(C)..... CANADA
(R)..... GENERAL
(T)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... SOUTH EUROPE
(V)..... TAIWAN

RESISTOR

REMARKS	PARTS NAME
NO MARK	CARBON FILM RESISTOR (P=5)
□	CARBON FILM RESISTOR (P=10)
△	METAL OXIDE FILM RESISTOR
▲	METAL FILM RESISTOR
▣	METAL PLATE RESISTOR
▤	FIRE PROOF CARBON FILM RESISTOR
□	CEMENT MOLDED RESISTOR
⊗	SEMI VARIABLE RESISTOR
■	CHIP RESISTOR

CAPACITOR

REMARKS	PARTS NAME	
NO MARK	ELECTROLYTIC CAPACITOR	≡
⊗	TANTALUM CAPACITOR	
NO MARK	CERAMIC CAPACITOR	
●	CERAMIC TUBULAR CAPACITOR	
◎	POLYESTER FILM CAPACITOR	
○	POLYSTYRENE FILM CAPACITOR	≡
①	MICA CAPACITOR	
Ⓟ	POLYPROPYLENE FILM CAPACITOR	
⊙	SEMICONDUCTIVE CERAMIC CAPACITOR	

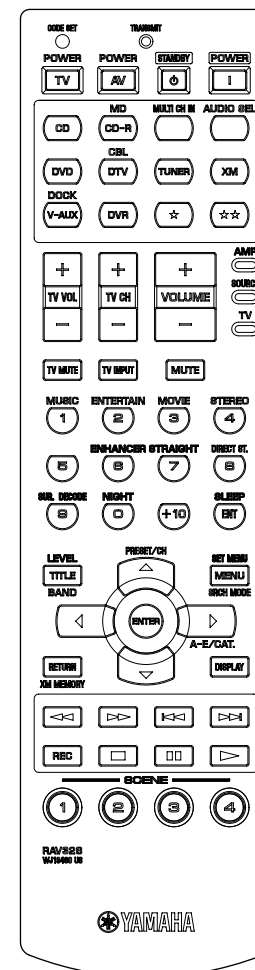
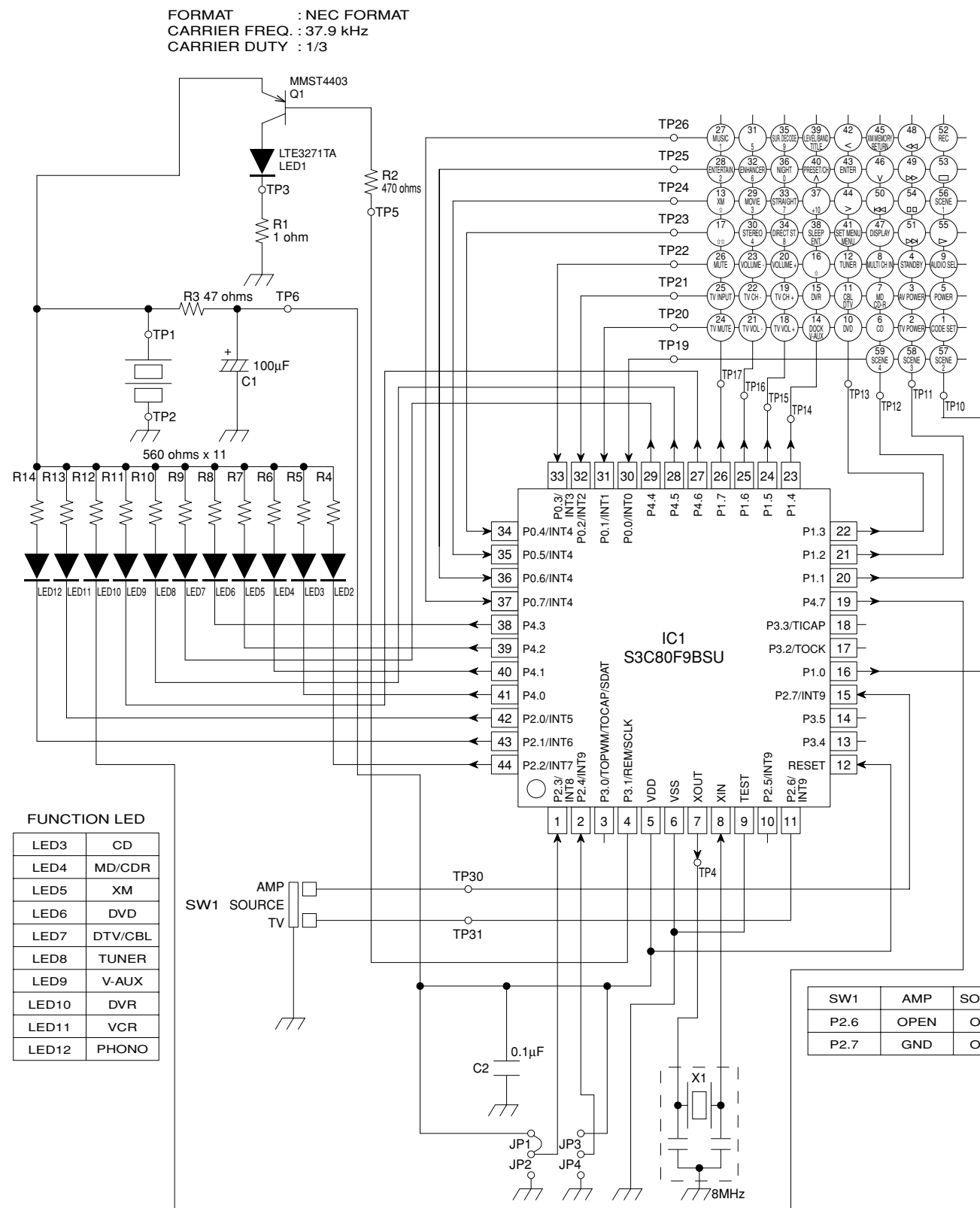
★ All voltages are measured with a 10MΩ/V DC electronic voltmeter.
★ Components having special characteristics are marked ▲, and must be replaced with parts having specifications equal to those originally installed.
★ Schematic diagram is subject to change without notice.

●電圧は、内部抵抗10MΩの電圧計で測定したものです。
●▲印のある部品は、安全性確保部品を示しています。部品の交換が必要な場合、パーツリストに記載されている部品を使用してください。
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■ REMOTE CONTROL

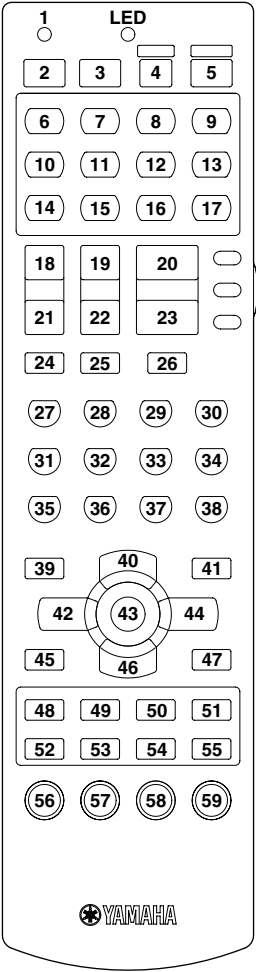
- RAV328 (U, C models)
- SCHEMATIC DIAGRAM

- **PANEL**



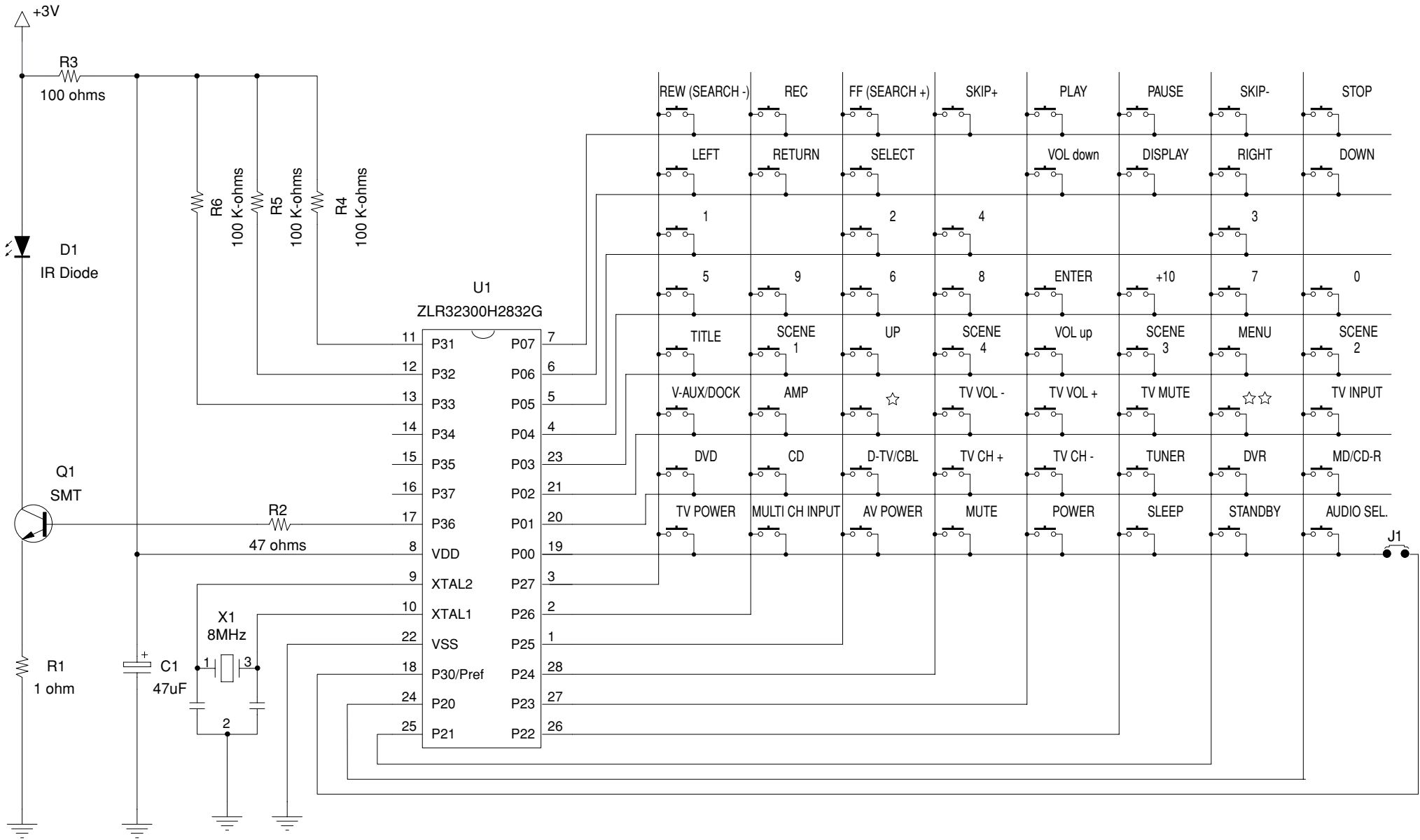
1 • KEY NO. LAYOUT

• KEY CODE

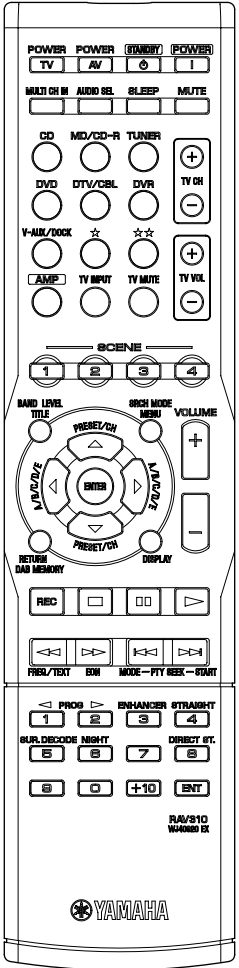


Key No.	Key Label	Common	AMP library Code				Function											
			ID1		ID2													
			MAIN	ZONE2	MAIN	ZONE2												
–	LED	–	–	O (10sec)	–	O (10sec)	Linked with IR signal											
1	CODE SET	–	–	–	–	–	Change to PRESET mode											
4	STANDBY	O	7E-7F	7E-BB	7D-B1	7D-63	Power STANDBY											
5	POWER	O	7E-7E	7E-BA	7D-B2	7D-64	Power ON											
							Library: All device codes can be entered into all modes.										default	
6	CD	O	7A-15	7A-D1	7D-87	7D-67	Output IR signal & change Device mode										CD	62907
7	MD/CD-R	O	7A-C9	7A-CF	7D-86	7D-92											CD-R	71292
8	MULTI CH IN	O	7A-87		7D-8C		Output IR signal											
9	AUDIO SEL.	O	7A-C3		7D-80		Select audio											
10	DVD	O	7A-C1	7A-CD	7D-97	7D-6F	Output IR signal & change Device mode										DVD	42543
11	DTV/CBL	O	7A-54	7A-D9	7D-84	7D-6C											TV	nothing
12	TUNER	O	7A-16	7A-D2	7D-89	7D-66											TUNER	82915
13	XM	O	7A-B4	7A-B8	7D-29	7D-2F	Output IR signal & change Device mode										TUNER	82918
14	V-AUX/DOCK	O	7A-55	7A-D8	7D-8A	7D-68											TUNER	82981
15	DVR	O	7A-13	7A-D7	7D-82	7D-6E											DVR	52544
16	VCR	O	7A-0F	7A-D6	7D-81	7D-6D											VCR	nothing
17	PHONO	O	7A-14	7A-D0	7D-88	7D-65											TV	nothing
SWITCH (AMP/SOURCE/TV)			AMP				SOURCE										TV	
							6	7	13	10	11	12	14	15	16	17		
							CD	MD/CD-R	XM	DVD	DTV/CBL	TUNER	V-AUX/DOCK	DVR	VCR	PHONO		
2	TV POWER	–	→		→		(TV Power)	(TV Power)	(TV Power)	(TV Power)	TV Power	(TV Power)	(TV Power)	(TV Power)	(TV Power)	TV Power	TV Power	
3	AV POWER	–	→		→		Power	Power	Power	Power	(VCR1 Power)	Power	Power	Power	Power	(VCR1 Power)	(VCR1 Power)	
18	TV VOL up	–	→		→		(TV VOL up)	(TV VOL up)	(TV VOL up)	(TV VOL up)	TV VOL up	(TV VOL up)	(TV VOL up)	(TV VOL up)	(TV VOL up)	TV VOL up	TV VOL up	
19	TV CH up	–	→		→		(TV CH up)	(TV CH up)	(TV CH up)	(TV CH up)	TV CH up	(TV CH up)	(TV CH up)	(TV CH up)	(TV CH up)	TV CH up	TV CH up	
20	VOL up	O	7A-1A	7A-DA	7D-8D	7D-70	VOL up											
21	TV VOL down	–	→		→		(TV VOL down)	(TV VOL down)	(TV VOL down)	(TV VOL down)	TV VOL down	(TV VOL down)	(TV VOL down)	(TV VOL down)	(TV VOL down)	TV VOL down	TV VOL down	
22	TV CH down	–	→		→		(TV CH down)	(TV CH down)	(TV CH down)	(TV CH down)	TV CH down	(TV CH down)	(TV CH down)	(TV CH down)	(TV CH down)	TV CH down	TV CH down	
23	VOL down	O	7A-1B	7A-DB	7D-8E	7D-71	VOL down											
24	TV MUTE	–	→		→		(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	TV Mute	(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	TV Mute	TV Mute	
25	TV INPUT	–	→		→		(TV Input)	(TV Input)	(TV Input)	(TV Input)	TV Input	(TV Input)	(TV Input)	(TV Input)	(TV Input)	TV Input	TV Input	
26	MUTE	O	7A-1C	7A-DC	7D-94	7D-72	MUTE											
27	CLASSICAL	–	7A-88		7D-D0		1	1	1	1	1	P1	1	1	1	1	1	
28	CLUB/LIVE	–	7A-89		7D-D1		2	2	2	2	2	P2	2	2	2	2	2	
29	ENTERTAIN	–	7A-8A		7D-D2		3	3	3	3	3	P3	3	3	3	3	3	
30	MOVIE	–	7A-8B		7D-D3		4	4	4	4	4	P4	4	4	4	4	4	
31	STEREO	–	7A-8C		7D-D4		5	5	5	5	5	P5	5	5	5	5	5	
32	ENHANCER	–	7A-94		7D-DC		6	6	6	6	6	P6	6	6	6	6	6	
33	STRAIGHT	–	7A-56		7D-C1		7	7	7	7	7	P7	7	7	7	7	7	
34	PURE DIRECT	–	7A-DD		7D-C0		8	8	8	8	8	P8	8	8	8	8	8	
35	SUR. DECODE	–	7A-8D		7D-D5		9	9	9	9	9	–	9	9	9	9	9	
36	NIGHT	–	7A-95		7D-DD		0/10	0/10	0	0	0/10	–	0/10	0	0/10	0/10	0/10	
37	–	–	–		–		+10	+10	–	+10	-/11	–	+10	+10	+10	+10	-/11	
38	SLEEP	–	7A-57		7D-93		index	index	P. ENTER	Title/index	enter/12	–	enter	Title/index	enter	enter	enter/12	
39	LEVEL	–	7A-86		7D-95		–	–	BAND	TITLE	–	BAND	–	TITLE	–	–	–	
40	UP	–	7A-9D		7D-9D		–	–	PRESET/CH +	UP	–	PRESET +	VCR CH up	UP	VCR CH up	VCR CH up	–	
41	SET MENU	–	7A-84		7D-C3		–	–	SEARCH MODE	MENU	–	–	–	MENU	–	–	–	
42	LEFT	–	7A-9F		7D-9F		–	–	A-B/CAT. -	LEFT	–	A-B -	–	LEFT	–	–	–	
43	ENTER	–	7A-DE		7D-56		–	–	ENTER	SELECT	–	–	–	SELECT	–	–	–	
44	RIGHT	–	7A-9E		7D-9E		–	–	A-E/CAT. +	RIGHT	–	A-E +	–	RIGHT	–	–	–	
45	RETURN	–	7A-AA		7D-B5		–	–	MEMORY	Return	–	–	–	Return	–	–	–	
46	DOWN	–	7A-9C		7D-9C		–	–	PRESET/CH -	DOWN	–	PRESET -	VCR CH down	DOWN	VCR CH down	VCR CH down	–	
47	ON SCREEN	–	7A-C2		7D-C2		DISPLAY	DISPLAY	DISPLAY	DISPLAY	DISPLAY	–	–	DISPLAY	–	–	DISPLAY	
48	REW (SEARCH)	–	→		→		REW	REW	–	REW	(VCR1REW)	–	REW	REW	REW	REW	(VCR1REW)	
49	FF (SEARCH)	–	→		→		FF	FF	–	FF	(VCR1 FF)	–	FF	FF	FF	FF	(VCR1 FF)	
50	CHP/SKIP-	–	→		→		SKIP -	SKIP -	–	SKIP -	-	–	–	SKIP -	–	–	–	
51	CHP/SKIP+	–	→		→		SKIP +	SKIP +	–	SKIP +	-	–	–	SKIP +	–	–	–	
52	REC	–	→		→		DISC SKIP	REC	–	DISC SKIP	(VCR1 REC)	–	REC	REC	REC	REC	(VCR1 REC)	
53	STOP	–	→		→		STOP	STOP	–	STOP	(VCR1 STOP)	–	STOP	STOP	STOP	STOP	(VCR1 STOP)	
54	PAUSE	–	→		→		PAUSE	PAUSE	–	PAUSE	(VCR1 PAUSE)	–	PAUSE	PAUSE	PAUSE	PAUSE	(VCR1 PAUSE)	
55	PLAY	–	→		→		PLAY	PLAY	–	PLAY	(VCR1 PLAY)	–	PLAY	PLAY	PLAY	PLAY	(VCR1 PLAY)	
56	SCENE1	O	7A-007F	7A-017E	7A-0C73	7A-0D72	Select SCENE											
57	SCENE2	O	7A-037C	7A-047B	7A-0F70	7A-106F												
58	SCENE3	O	7A-0679	7A-0778	7A-126D	7A-136C												
59	SCENE4	O	7A-0976	7A-0A75	7A-156A	7A-1669												

- RAV310 (R, K, A, L, J models)
- SCHEMATIC DIAGRAM



- PANEL



1 • KEY NO. LAYOUT

• KEY CODE

2

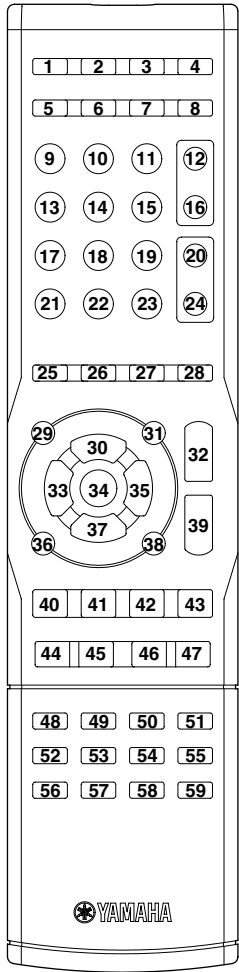
3

4

5

6

7



Key No.	Label	Command Key	YAMAHA signal														
1	TV POWER	—	—	(TV Power)	(TV Power)	(TV Power)	(TV Power)	—	(TV Power)	(TV Power)	(TV Power)	(TV Power)	(TV Power)	(TV Power)	(TV Power)	(TV Power)	(TV Power)
2	AV POWER	—	—	—	7F80	—	7F80	(DVR Power)	048.012	7F01-00	—	—	7F01-20	—	—	(device)	
3	STANDBY	O	7E-7F	STANDBY													
4	POWER	O	7E-7E	POWER ON													
5	MULTI CH INPUT	O	7A-87	MULTI CH INPUT													
6	AUDIO SEL.	O	7A-C3	AUDIO SELECT													
7	SLEEP	O	7A-57	SLEEP													
8	MUTE	O	7A-1C	MUTE													
9	CD	O	7A-15	<INPUT key>	Output IR signal and change Device mode												Default
10	MD/CD-R	O	7A-C9	<INPUT key>	Output IR signal and change Device mode												Library
11	TUNER	O	7A-16	<INPUT key>	Output IR signal and change Device mode												brand
12	TV CH +	—	—	(TV CH +)	(TV CH +)	(TV CH +)	(TV CH +)	—	(TV CH +)	(TV CH +)	(TV CH +)	(TV CH +)	(TV CH +)	(TV CH +)	(TV CH +)	(TV CH +)	CD
13	DVD	O	7A-C1	<INPUT key>	Output IR signal and change Device mode												CD-R
14	D-TV/CBL	O	7A-54	<INPUT key>	Output IR signal and change Device mode												YAMAHA
15	DVR	O	7A-13	<INPUT key>	Output IR signal and change Device mode												TUNER
16	TV CH -	—	—	(TV CH -)	(TV CH -)	(TV CH -)	(TV CH -)	—	(TV CH -)	(TV CH -)	(TV CH -)	(TV CH -)	(TV CH -)	(TV CH -)	(TV CH -)	(TV CH -)	YAMAHA-1
17	V-AUX/DOCK	O	7A-55	<INPUT key>	Output IR signal and change Device mode												YAMAHA-2
18	☆	O	7A-B4	<INPUT key>	Output IR signal and change Device mode												YAMAHA-1
19	☆☆	O	7F01-3F	<INPUT key>	Output IR signal and change Device mode												YAMAHA1
20	TV VOL +	—	—	(TV VOL +)	(TV VOL +)	(TV VOL +)	(TV VOL +)	—	(TV VOL +)	(TV VOL +)	(TV VOL +)	(TV VOL +)	(TV VOL +)	(TV VOL +)	(TV VOL +)	(TV VOL +)	DVR
21	AMP	O	—	Change to AMP mode													YAMAHA
			Key No Mode	9	10	11	13	14	15	17	18	19	21				
				CD	MD/CD-R	TUNER	DVD	D-TV/CBL	DVR	V-AUX/DOCK	XM	NET/USB	AMP				
22	TV INPUT	—	—	(TV Input)	(TV Input)	(TV Input)	(TV Input)	—	(TV Input)	(TV Input)	(TV Input)	(TV Input)	(TV Input)	(TV Input)	(TV Input)	(TV Input)	
23	TV MUTE	—	—	(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	—	(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	(TV Mute)	
24	TV VOL -	—	—	(TV VOL -)	(TV VOL -)	(TV VOL -)	(TV VOL -)	—	(TV VOL -)	(TV VOL -)	(TV VOL -)	(TV VOL -)	(TV VOL -)	(TV VOL -)	(TV VOL -)	(TV VOL -)	
25	SCENE 1	O	7A-007F	SCENE SELECT													
26	SCENE 2	O	7A-037C	SCENE SELECT													
27	SCENE 3	O	7A-0679	SCENE SELECT													
28	SCENE 4	O	7A-0976	SCENE SELECT													
29	TITLE	—	—	—	—	7A-AE	7CB1	—	048. 200	7F01-0D	7A-70	—	7F01-2D	—	—	7A-86	
30	UP	—	—	—	—	7A-10	7CB4	—	048. 088	7F01-0E	7A-6A	—	7F01-2E	—	—	7A-9D	
31	MENU	—	—	—	—	7A-AB	7CB2	—	048. 084	7F01-0F	7A-6D	—	7F01-2F	—	—	7A-84	
32	VOL up	O	7A-1A	VOL UP													
33	LEFT	—	—	—	—	7A-AC	7CB5	—	048. 090	7F01-10	7A-6E	—	7F01-30	—	—	7A-9F	
34	SELECT	—	—	—	—	7A-AD	7CB8	—	048. 092	7F01-11	7A-6F	—	7F01-31	—	—	7A-DE	
35	RIGHT	—	—	—	—	7A-12	7CB6	—	048. 091	7F01-12	7A-6C	7A06	7F01-32	—	—	7A-9E	
36	RETURN	—	—	—	—	7A-AF	7CB7	—	048. 131	7F01-13	7A-71	—	7F01-33	—	—	7A-AA	
37	DOWN	—	—	—	—	7A-11	7CB3	—	048. 089	7F01-14	7A-6B	—	7F01-34	—	—	7A-9C	
38	DISPLAY	—	—	790A	7F9E	7A-B0	7CA6	—	048. 015	7F01-15	7A-72	—	7F01-35	7C13	—	7A-C2	
39	VOL down	O	7A-1B	VOL DOWN													
40	REC	—	—	7A4F	—	—	7C8B	(VCR REC)	048. 055	7F01-16	—	7A04	7F01-36	—	—	(device)	
41	STOP	—	—	7A09	7F84	—	7C85	(VCR Stop)	048. 049	7F01-1D	—	7A03	7F01-3D	7C5B	—	(device)	
42	PAUSE	—	—	7A09	7F83	—	7C83	(VCR Pause)	048. 048	7F01-1A	—	—	7F01-3A	7C5A	—	(device)	
43	PLAY	—	—	7A08	7F82	—	7C82	(VCR Play)	048. 044	7F01-1E	—	7A00	7F01-3E	7C05	—	(device)	
44	REW (SEARCH -)	—	—	7A0D	7F88	7A-A4	7C86	(VCR REW)	048. 041	7F01-17	—	7A01	7F01-37	7C06	—	(device)	
45	FF (SEARCH +)	—	—	7A0C	7F89	7A-A5	7C87	(VCR FF)	048. 040	7F01-18	—	7A02	7F01-38	7C07	—	(device)	
46	SKIP -	—	—	7A0B	7F86	7A-A6	7CB9	—	048. 033	7F01-1B	—	7A07	7F01-3B	7C02	—	(device)	
47	SKIP +	—	—	7A0A	7F87	7A-A7	7CBA	—	048. 032	7F01-1C	—	7A40	7F01-3C	7C03	—	(device)	
48	1	—	—	7911	7F91	7A-E5	7C94	—	048. 001	7F01-01	7A-61	—	7F01-21	7C17	—	7A-59	
49	2	—	—	7912	7F92	7A-E6	7C95	—	048. 002	7F01-02	7A-62	—	7F01-22	7C18	—	7A-58	
50	3	—	—	7913	7F93	7A-E7	7C96	—	048. 003	7F01-03	7A-63	—	7F01-23	7C19	—	7A-94	
51	4	—	—	7914	7F94	7A-E8	7C97	—	048. 004	7F01-04	7A-64	—	7F01-24	7C1A	—	7A-56	
52	5	—	—	7915	7F95	7A-E9	7C98	—	048. 005	7F01-05	7A-65	—	7F01-25	7C1B	—	7A-8D	
53	6	—	—	7916	7F96	7A-EA	7C99	—	048. 006	7F01-06	7A-66	—	7F01-26	7C1C	—	7A-95	
54	7	—	—	7917	7F97	7A-EB	7C9A	—	048. 007	7F01-07	7A-67	—	7F01-27	7C1D	—	—	
55	8	—	—	7918	7F98	7A-EC	7C9B	—	048. 008	7F01-08	7A-68	—	7F01-28	7C1E	—	7A-DD	
56	9	—	—	7919	7F99	7A-B1	7C9C	—	048. 009	7F01-09	7A-69	—	7F01-29	7C1F	—	—	
57	0	—	—	7910	7F90	7A-B2	7C93	—	048. 000	7F01-0A	7A-60	—	7F01-2A	7C16	—	—	
58	+10	—	—	791A	7F9A	—	7C9D	—	—	7F01-0B	—	—	7F01-2B	7C5D	—	—	
59	ENTER	—	—	790B	7F8A	7A-B3	7C9E	—	—	7F01-0C	7A-BF	—	7F01-2C	7C15	—	—	